157-S-131A 1/78

INSTRUCTION MANUAL

MODEL 157-S-131A PROGRAMMABLE WAVEFORM SYNTHESIZER

WAVETEK
SAN DIEGO

9045 BALBOA AVENUE, SAN DIEGO, CALIFORNIA

Serial No. _____

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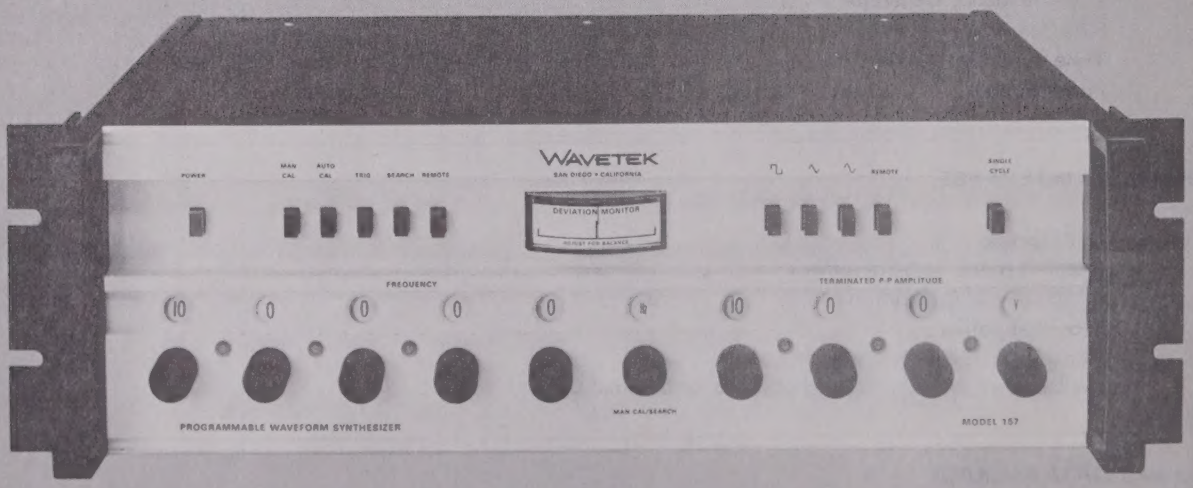
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REMOTE
SINGLE CYCLE
FREQUENCY
TERMINATED P-P AMPLITUDE
MAN CAL/SEARCH
PROGRAMMABLE WAVEFORM SYNTHESIZER
MODEL 157

SECTION 1
GENERAL DESCRIPTION

SCOPE OF MANUAL

This manual provides instructions for operating, testing, and maintaining the Wavetek Programmable Waveform Synthesizer. Sections 1 through 5 include the necessary information for the basic Model 157. Section 6 consists of **Difference Data** that describe specific differences, if any, from the basic model.

SCOPE OF EQUIPMENT

The Model 157 is a precision source of selectable sine, square, or triangle waveforms that are generated from 100 μHz (2¾ hours per cycle) to 1 MHz in 10 ranges. Output amplitudes are selectable from 1 millivolt to 10 volts, peak-to-peak, into a 50-ohm load. This instrument incorporates a voltage-controlled generator that may be swept over a 1000:1 frequency ratio with a 0-volt to 5-volt analog input for frequency modulation of the selected output frequency.

Four modes of operation can be programmed with the front-panel controls. Three modes of operation are programmable from a remote source by static logic levels or contact closures. Frequency, amplitude, and function also are programmable from either the front-panel controls or a remote source.

The instrument is packaged for mounting in a standard 19-inch rack or for operating on a test bench.

MODES OF OPERATION

Front-panel pushbutton operation provides the following operational modes:

1. VCG with manual calibration.
2. Automatic calibration.
3. Trigger.
4. Search.

Remote logic-level or contact-closure programming provides the following operational modes:

1. Automatic calibration.
2. Trigger.
3. VCG without manual calibration.

VCG With Manual Calibration Mode

This mode is selected at the front panel for accurately calibrating the output frequency. After calibration, the Model 157 may be swept about the calibrated center frequency with a VCG input.

Automatic Calibration Mode

This mode is selected at the front panel or programmed from the remote source to provide an accurate fixed-frequency output by closed-loop operation.

Trigger Mode

This mode is selected at the front panel or programmed from the remote source to place the Model 157 in a standby state until a positive pulse is applied through a connector on the rear panel. The standby state for the sine and triangle waveforms is 0-volts dc. For the square wave, the standby state is the positive peak value of the programmed amplitude. A tone-burst output starts at the positive-going edge of the input pulse and continues, at the programmed frequency, for the duration of the pulse plus the time required for the generator to complete the last cycle. Another front-panel pushbuttom permits a single cycle of the programmed frequency to be generated without an external pulse input. The same function can be accomplished remotely by injecting an input pulse having less duration than a single cycle of the programmed frequency.

Search Mode

This mode is selected at the front panel for sweeping the entire selected frequency range to find a specific frequency. In this mode, digital programming of frequency is disabled and the generator functions in open-loop operation.

VCG Without Manual Calibration Mode

This mode is obtained in remote-control operation when none of the preceding functions are programmed. In this

mode, the generator is in the open-loop operation and the output frequency is determined by the digitally-programmed frequency and a VCG input signal. If the input signal is dc, the generator output frequency will be the digitally-programmed frequency proportionally modified by the level of the input signal. A positive input level increases the generator output frequency, and a negative level decreases the output frequency. If the input signal is ac, the output frequency will be frequency modulated over a band that is proportional to the signal amplitude at a rate that is equal to the signal frequency.

REMOTE CONTROL OPERATIONS

Transfer of control to a remote source is accomplished by depressing the REMOTE pushbuttons and turning the rotary switches to their R positions. Control of generator mode, function, and each rotary switch may be transferred to the REMOTE connector collectively or independently. (Refer to **Operations** section.) Any or all of these controls may then be programmed in BCD/binary format at the 50-pin REMOTE connector on the rear panel. Synchronizing, trigger, and VCG input signals are connected to the Model 157 through separate BNC connectors on the rear panel for either local or remote-program applications. Programming an automatic calibration command simultaneously with a synchronizing, trigger, or VCG input signal is a *not-allowed* operation.

FUNCTIONAL DESCRIPTIONS

Programming

Operation by contact closure (local/remote) or by automatic data processing (ADP) is controlled by binary states. The coding format is modified BCD, BCD, or binary. The coded program controls numerical frequency, frequency range, numerical amplitude, amplitude range, function selection, and generator mode selection. Circuit functions are implemented by reed-relay contact closures and digital-to-analog converters (DAC).

In the simplified block diagram of Figure 1-1, all switch symbols represent a switching function that can be commanded either remotely or locally. The binary code is converted by the frequency DAC into an analog voltage which is applied to an inverter and error-sensing circuit. The outputs of these circuits establish the operating state for the voltage-controlled generator (VCG).

Error Loop Operation

The inverter input to the VCG is disconnected in the search mode (represented by S_1 in Figure 1-1) so that the frequency range may be manually swept with a front-panel control. The frequency error-sensing circuit is disconnected in search, trigger, and VCG modes. In automatic calibration, this circuit operates in closed loop and corrects for frequency drift.

The error-sensing circuit operates between 100 Hz to 1 kHz. Therefore, when the programmed frequency is greater than 1 kHz, the divider circuit is automatically switched between the output of the hysteresis switch and the error-circuit input (represented by S_3).

The divider circuit consists of three cascaded divide-by-ten counters that are progressively engaged as the frequency is increased. With all three circuits engaged, the divider output is 0.001 of the input frequency which reduces the top frequency range of the generator to within the error-circuit band.

High-Frequency Range

Frequences from 100 Hz to 1 MHz are produced by conventional function-generator techniques. The integrator output is a triangle wave and the hysteresis switch output is a square wave, each being interdependent with the other. The frequency program establishes the charging slope for the integrator and selects the proper integrating capacitance for the specific frequency range. In the trigger mode, a square-wave output from the hysteresis switch is coupled through S_2 to enable the trigger circuit.

The triangle wave is coupled through S_6 to the sine converter which transforms the input waveform into a sinusoidal wave at its output. The programmed function at S_7 (triangle, square, or sine) is then given the necessary gain by the output amplifier before being applied to the attenuation network.

The output level program then selects the required attenuation network to provide the desired output amplitude for the selected waveform over the 100 Hz to 1 MHz range.

Low-Frequency Range

Waveforms with frequencies from 100 Hz down to 100 μHz are synthesized from the basic generator square-wave output by the counter and waveform DAC. The counter circuit counts the input pulses to produce square waves at 0.001 of the input frequency. One output is applied through S_2 to enable the trigger circuit in the low-frequency range when this mode is selected. Another counter output pulse is applied through S_5 to the function-selection circuit S_7. All pulses from the counter are coupled to the waveform DAC that transforms the binary bits into a

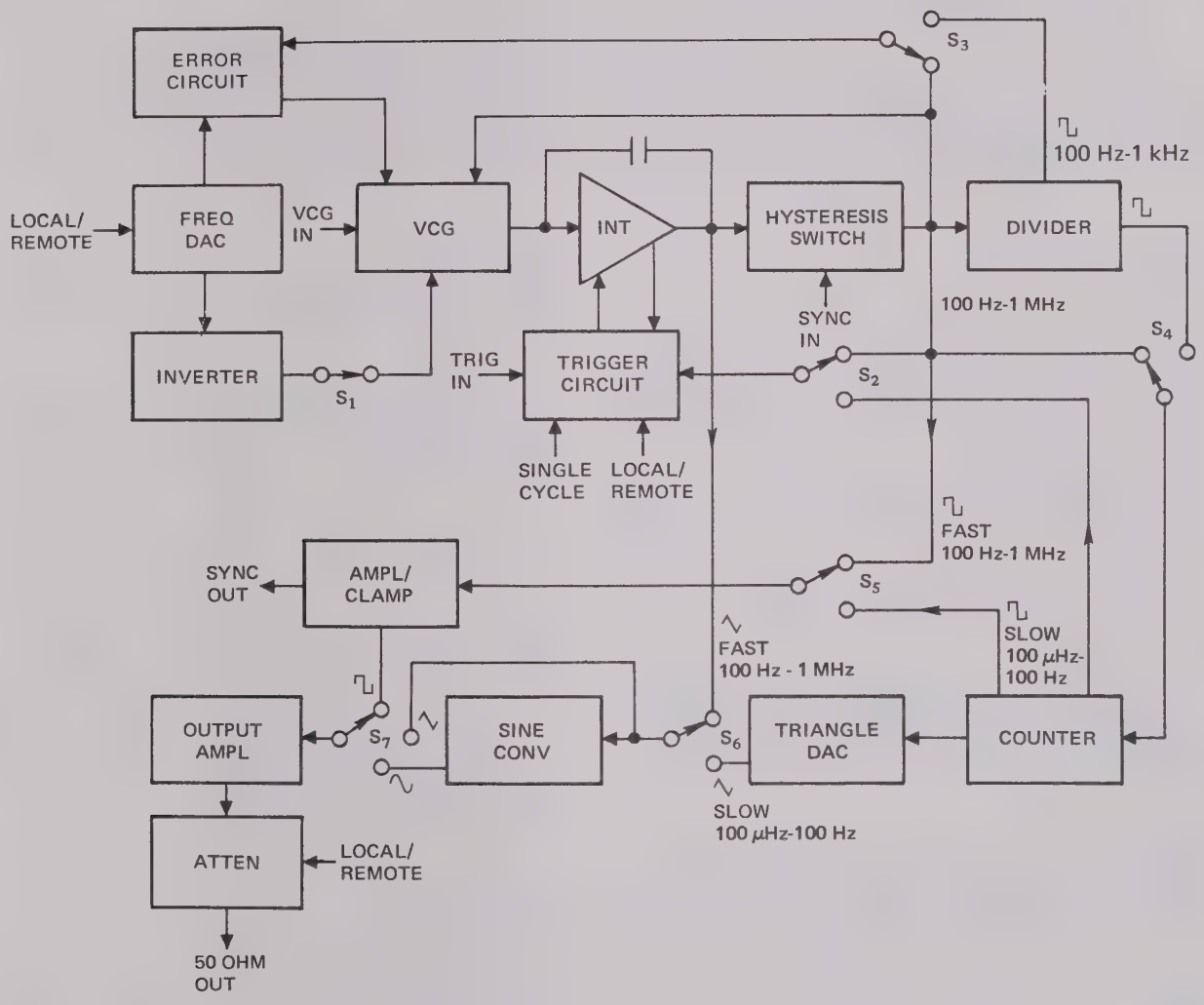


Figure 1-1. Simplified Block Diagram

triangle waveform at 0.001 of the generator frequency over the range of 100 μHz to 100 Hz.

For frequencies from 100 Hz down to 100 μHz, the divider is progressively switched into the counter input through S_4 as described previously for the error-sensing circuit.

Sine conversion, function selection, amplification, and output attenuation are executed in the same manner as described for the high-frequency range.

SPECIFICATIONS

VERSATILITY

Waveforms
Sine ∿ , square ⊓ , and triangle ∿ .

Dynamic Frequency
100 μHz (2.77 hr) to 1 MHz (1 μsec) in 10 ranges.

Outputs
∿ , ⊓ , ⩘ selectable and digitally variable from 0.001 to 10 V p-p into a 50-ohm load in 4 ranges with 3-digit resolution (0.002 to 20 V p-p open circuit).

NOTE

Output may be shorted without damage to instrument.

VCG—Voltage-Controlled Generator
Over 1000:1 frequency ratio with 0-volt to 5-volt input signal. Input impedance is 10 kΩ.

OPERATIONAL MODES

Trigger Mode
Generator may be triggered to produce single cycles on command or gated to produce any discrete number of cycles by applying a + gate signal to trigger input for the length of the desired burst.
Input impedance is 10 k.
Plus gate required is +5 volts to +50 volts.

Auto Cal Mode
Generator has an automatic control loop to maintain the ouput frequency for high accuracy and stability.

Manual Cal Mode
Generator is manually calibrated using the control loop but is then returned to the open-loop condition when momentary switch is released so that it may be swept or triggered.

Search Mode
Generator frequency control within the selected range is transferred from the digital controls to a single turn analog control for convenient manual sweeping of the entire range.

Sync In
Within the upper 4 ranges, the instrument may be frequency synchronized to an external signal of approximately 1 V p-p that is within 1% of the free-running frequency. The induced sine distortion will be less than 1%.

Sync Out
A fixed amplitude square wave is brought out at 1 k impedance for syncing scopes or other equipment.

HORIZONTAL PRECISION

Auto Cal Mode
Frequency accuracy from programmed input or front-panel selector switches is ±(0.02% of programmed frequency, +1 digit); ±(0.01% +1 digit) @25 ±5°C. Closed-loop stability is within ±0.005% of programmed frequency at constant temperature for a 24-hour period. Loop will settle to within the accuracy specified within 1 msec minimum and 3 sec maximum, depending on frequency and range.

Manual Cal Mode
Instrument can be calibrated to an accuracy of ±0.02% of programmed frequency and triggered or frequency modulated about this accurate center frequency.
Open-loop stability is within 0.05% of setting for 8-hour period at constant temperature.

VCG Bandwidth
1 MHz.
Slew rate 100% of range per μsec.

VCG Linearity
Frequency vs input voltage—best straight-line method.
±0.1% 100 μHz-10 kHz.
±1% 10 kHz-100 kHz.
±3% 100 kHz-1MHz

VERTICAL PRECISION

Amplitude change with frequency is less than 0.1 db to 100 kHz and 0.5 db to 1 MHz.

Peak-to-Peak Voltage Accuracy % of Program (Up to 10 kHz)
1 V to 10 V range ±(0.3% +5 mV); ±(0.1% +5 mV) @25 ±5°C.
0.1 V to 1 V range ±(1% + 1 mV).
10 mV to 100 mV range ±(1% + 0.1 mV).
1 mV to 10 mV range ±(1% + 0.1 mV).

Amplitude Symmetry
All waveforms are symmetrical about ground ±2% of full range; ±1% @ 25 ±5°C.
Trigger start-stop point will be 0 V ±1% of output amplitude program; ±0.1% @ 25 ±5°C.

PURITY

Sine Wave Distortion
Less than:
1% 100 μHz to 10 kHz; 0.5% @ 25 ±5°C
1% 10 kHz to 100 kHz
3% 100 kHz to 1 MHz

Triangle Linearity
Greater than:
99% 1 Hz to 100 kHz
95% 100 kHz to 1 MHz

Square Wave Rise and Fall Time
Less than 100 nsec.

ISOLATION

Output signal can be raised above ground up to 250 V. Caution must be taken as all exposed BNC connectors are at raised potential.

ENVIRONMENTAL

Temperature
Specifications apply at 0°C to 50°C except where noted otherwise.

REMOTE CONTROL SPECIFICATIONS

Configuration A (Standard)
True or Logic "1" = 0 volt ±1 volt.
Note: 0 volt sinks approximately 1.5 mA.
False or Logic "0" = +2 volts to +10 volts.
Note: Open circuit voltage is approximately +2 volts.

Configuration B
True or Logic "1" = 0 volt ±1 volt.
Note: 0 volt sinks approximately 1.5 mA.
False or Logic "0" = −2 volts to −10 volts.
Note: Open circuit voltage is approximately −2 volts.

Configuration C
True or Logic "1" = −2 volts to −10 volts.
Note: −2 volts sinks approximately 1.5 mA.
False or Logic "0" = 0 volt to −1 volt.
Note: Open circuit voltage approximately +0.7 volts.

Configuration D
True or Logic "1" = +2 volts to +10 volts.
Note: +2 volts sinks approximately 1.5 mA.
False or Logic "0" = 0 volt to +1 volt.
Note: Open circuit voltage approximately −0.7 volts.

Program Transition Time
1 msec.

MECHANICAL

Weight
24 lb, 34 lb shipping.

Power
105 Vac to 125 Vac or 210 Vac to 250 Vac, 50 Hz to 400 Hz.
50 watts of power required.

NOTE
Precision specifications apply over 10% to 100% of selected range.

SECTION 2
OPERATING INSTRUCTIONS

INSTALLATION

Conversion for 230-Volt Line Power

The Model 157 is shipped from the factory with the power transformer connected for 115-volt line power, unless otherwise specified. To convert the transformer primary for 230-volt operation, remove the bottom cover and place the conversion switch (located on the underside of the Mother Board) in the 230 position. The 1-ampere fuse (rear panel) should be replaced with a ½-ampere fuse after conversion. (Refer to **Replacement of Circuit Boards** in Section 4 for information on removing the bottom cover.)

Rack Mounting

The Model 157 is equipped with four rubber feet when ordered for bench use and with rail-attaching hardware when ordered for use in a standard 19-inch rack. If rack mounting of a bench model is desired, two rack mount adapters, Wavetek part No. 210-367, can be used as follows:

1. Remove the six flathead machine screws that retain the left and right side rails to the left and right handles, respectively.
2. Install one rack mount adapter on each side of the Chassis Assembly with the six roundhead screws supplied with the adapters, using the tapped holes previously occupied by the flathead screws.
3. Secure the Model 157 in the desired rack position with standard rack mounting hardware.

INSPECTION

The following procedures should be performed to assure the user that the instrument has arrived at its destination in proper operating condition. Complete calibration and checkout instructions are provided in Section 4 for determining if the instrument is within electrical specifications.

Visual Inspection

After carefully unpacking the instrument, visually inspect the external parts for damage to knobs, dials, indicators, surface areas, etc. If damage is discovered, file a claim with the carrier who transported the instrument.

Operating Inspection

NOTE

Refer to **Installation** paragraph for 115-volt or 230-volt line power instructions.

The procedural steps in this paragraph provide a quick checkout of instrument operation. If electrical deficiencies exist, refer to the *Warranty* in the front of the manual. The following test equipment, or equivalent, is recommended for performing the electrical inspection. (Refer to Table 2-1 and Figure 2-1 for operating control descriptions.)

Name	Manufacturer	Model
Oscilloscope	Tektronix	454

1. Depress POWER pushbutton.
2. Connect oscilloscope through 50-ohm terminator to 50Ω OUTPUT connector.
3. Depress AUTO CAL pushbutton.
4. Depress ⊓⊔ pushbutton.
5. Set FREQUENCY dials for 1000.00 Hz.
6. Set TERMINATED P-P AMPLITUDE dials for 10.00 V.
7. Observe oscilloscope display to determine the general correctness of the signal output for Steps 8 through 16.
8. Depress the TRIG pushbutton and observe that the output is +5 Vdc.
9. Press SINGLE CYCLE pushbutton several times. Output should go to −5 Vdc for 0.5 milliseconds and back to +5 Vdc each time the pushbutton is pressed.
10. Depress the SEARCH pushbutton and rotate the MAN CAL/SEARCH control from maximum counter-clockwise to maximum clockwise positions. Output should respond to frequency control from less than 100 Hz to more than 1 kHz over the control range.
11. Depress the AUTO CAL pushbutton. Depress the ⊓⊔ , ∿ , and ∿ , in turn. Output should respond with indicated waveform.
12. Step the most-significant-decade FREQUENCY dial from 10 through 1. Output should change frequency in 100 Hz increments from 1 kHz to 100 Hz.

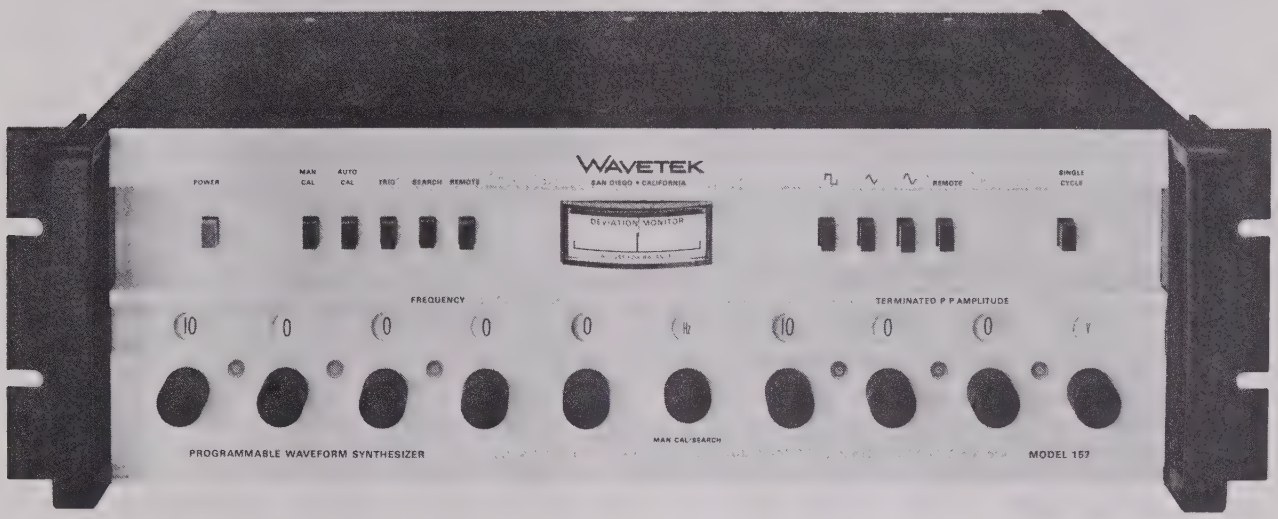
Table 2-1. CONDENSED OPERATING DATA

Name	Description
CONTROLS	
POWER pushbutton	Applies power to unit when depressed.
MAN CAL pushbutton*	Closes frequency-error loop so that **MAN CAL/SEARCH** control and nullmeter can be used to calibrate frequency output for VCG operation (momentary engage, opens loop when released).
AUTO CAL pushbutton*	Closes frequency-error loop for fixed-frequency operation.
TRIG pushbutton*	Opens frequency-error loop and enables trigger circuit for pulsed operation.
SEARCH pushbutton*	Opens frequency-error loop so that **MAN CAL/SEARCH** control can be used to sweep selected frequency range. Disconnects digital frequency control.
REMOTE pushbutton*	Transfers automatic calibration and trigger control to remote connector.
⊓ pushbutton**	Selects square-wave output.
∿ pushbutton**	Selects triangle-wave output.
∿ pushbutton**	Selects sine-wave output.
REMOTE pushbutton**	Transfers function control to remote connector.
SINGLE CYCLE pushbutton	Provides one-cycle of the selected output when the **TRIG** pushbutton is depressed (momentary engage).
FREQUENCY dials	Programs decimal frequency selection.
Most-Significant Decade	Numbered decimally from 1 through 10 with **R** being the remote-control transfer, or a logical "0" if not remotely programmed.
Next Four Decades	Numbered decimally from 0 through 9 with **R** being the remote-control transfer, or a logical "0" if not remotely programmed.
Range Dial	Lettered from μHz (one position), mHz (three positions), Hz (three positions), through kHz (three positions, and **R** for remote.

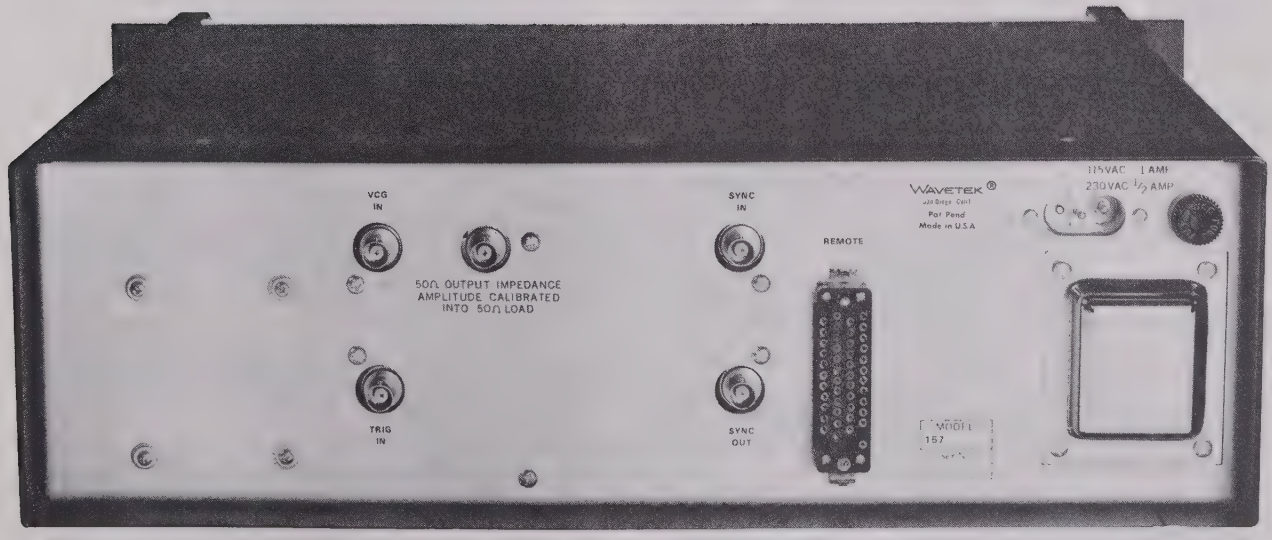
Table 2-1 CONT.

Name	Description
TERMINATED P-P AMPLITUDE dials	Programs decimal amplitude selection.
Most-Significant Decade	Numbered decimally 1 through 10 with **R** being the remote-control transfer, or a logical "0" if not remotely programmed.
Next Two Decades	Numbered decimally from 0 through 9 with **R** being the remote-control transfer, or a logical "0" if not remotely programmed.
Range Dial	Lettered from mV (three positions) to V (one position) and **R** for remote.
INDICATORS	
Nullmeter	Operates in conjunction with **MAN CAL** and **SEARCH** pushbutton and during **Checkout and Calibration**.
FREQUENCY decimal-point lamps	Illuminate to indicate position of decimal point during manual operation—pilot light only if range dial is in **R** position.
TERMINATED P-P AMPLITUDE	Illuminate to indicate position of decimal during manual operation—pilot light only if range dial is in **R** position.
CONNECTORS	
50Ω OUTPUT IMPEDANCE AMPLITUDE CALIBRATED INTO 50Ω LOAD	Output for selected function/amplitude waveform.
VCG IN	Analog input for frequency modulation of programmed frequency.
TRIG IN	Input for pulse to produce tone bursts of desired duration.
SYNC IN	Input for synchronizing output frequency with external signal.
SYNC OUT	Output for sychronizing external device with synthesizer.
REMOTE	Input for contact-closure or logic level programming from remote source.

NOTE: Pushbutton switches marked * or ** are mechanically linked. Depressing any one in a set releases any other previously depressed pushbutton in the same set.



Front View



Rear View

Figure 2-1. Operating controls, indicators, and connectors

13. Step the FREQUENCY range dial through its range from kHz to μHz. The output should respond with a 10:1 frequency change for each step. *Note*: If the top frequency ranges respond correctly, it should not be necessary to wait for the prolonged time period required for the bottom two or three ranges.
14. Repeat Step 13, but while holding in the MAN CAL and the AUTO CAL pushbuttons. The null meter pointer should not peg, but the pointer may deflect significantly in the top range.
15. Set the FREQUENCY dials for 1000.00 Hz and step the most-significant-decade dial of the TERMINATED P-P AMPLITUDE dials from 10 to 1. The output should respond in increments of 1 V p-p from 10 V to 1 V.
16. Set the TERMINATED P-P AMPLITUDE dials for 10 V and step the range dial through its range from V to mV. The output should respond in increments of 10:1 for each step.

MANUAL OPERATION

The following subparagraphs describe the operation of the Model 157, using the front-panel controls. Data for programming from a remote location are contained under the paragraph entitled **PROGRAMMING**.

Operating Controls

After the initial step of depressing the POWER pushbutton, as described below, *1-hour warmup* is required for the Model 157 output parameters to be within specifications.

Figure 2-1 illustrates the controls, indicators and connectors on the Model 157, and Table 2-1 describes them.

Fixed-Frequency Operation

This type of operation provides a fixed-frequency output within the specified tolerances for closed-loop operation.

1. Depress the POWER pushbutton.
2. Connect the external device (50-ohm impedance) to the 50 Ω OUTPUT IMPEDANCE AMPLITUDE CALIBRATED INTO 50 Ω LOAD connector.
3. Set the FREQUENCY digital dials to the desired frequency number.
4. Set the FREQUENCY range dial to the desired range setting.

NOTE

Horizontal precision tolerances listed under **SPECIFICATIONS** in Section 1 are obtained when operating within the 10 percent to 100 percent of range settings (i.e., from 10.000 Hz to 100.000 Hz).

5. Set the TERMINATED P-P AMPLITUDE digital dials to the desired output amplitude number.
6. Set the TERMINATED P-P AMPLITUDE range dial to the desired range setting.

NOTE

Vertical precision tolerances listed under **SPECIFICATIONS** in Section 1 are obtained when operating within the 10 percent to 100 percent of range settings (i.e., from 1.00 mV to 10.00 mV), and into a precise 50-ohm load. A 0.1 percent error in load resistance will cause a 0.05 percent error in output voltage.

7. Depress the AUTO CAL pushbutton.
8. Depress the desired function pushbutton: ∿ , ⊓ , or ⩘ .

Frequency Modulation With Manual Calibration Operation

This type of operation permits the selected output function to be frequency modulated with an external ac input signal, or the selected output frequency to be changed to another frequency with an external dc input level.

1. Perform Steps 1 through 6 of the **Fixed-Frequency Operation.**
2. Connect the external voltage source to the VCG IN connector and set the voltage to zero.

NOTE

If the voltage source cannot be set to zero, the VCG IN connector should be terminated with a resistance equal to the internal resistance of the source used to drive the VCG. If the signal voltage can be set to zero, then the terminating resistance can be eliminated and the driving source connected to the unterminated VCG IN connector.

3. While depressing the MAN CAL pushbutton, null the front-panel meter with the MAN CAL/SEARCH control. Release the MAN CAL pushbutton.
4. Set the external voltage source for the desired output. Frequency change will be 20 percent of range maximum per volt of VCG input.

Tone Burst Operation

This type of operation provides a fixed-frequency, tone-burst output within the specified tolerance for the manual

calibration mode. An output will occur for each input pulse; the duration of the burst is equal to the duration of the input pulse and the time required to complete the last cycle.

1. Perform Steps 1 through 6 of the **Fixed-Frequency Operation.**
2. While depressing the MAN CAL pushbutton, null the front-panel meter with the MAN CAL/SEARCH control.
3. Depress the TRIGGER pushbutton.
4. Connect an external pulse source to the TRIG IN connector.

NOTE

If manual single-cycle operation is desired, skip this step and proceed to Step 5.

5. Press SINGLE CYCLE pushbutton. One-cycle tone burst of the selected frequency and amplitude will be produced each time the SINGLE CYCLE is pressed.

NOTE

If external pulsing is desired, skip this step and proceed to Step 6.

6. Set the external pulse generator to produce a +5-volt to +50-volt pulse, having the desired repetition frequency and pulse duration.

NOTE

If the time interval of one cycle of the programmed frequency is greater than the duration of the input pulse, a one-cycle tone burst will be produced.

Frequency Search Operation

The search mode provides rapid analog frequency search (over an approximate 20:1 range), thereby reducing the time that would otherwise be spent in setting the digital dials. Although there are several uses for this mode, the operational procedures are basically the same. The following steps describe the search mode as a quick measurement of the resonant frequency of a narrow passband filter.

1. Perform Steps 1 through 6 of the **Fixed-Frequency Operation** except connect the Model 157 output to the filter and the filter output to an oscilloscope.
2. Depress the SEARCH pushbutton.
3. Rotate the MAN CAL/SEARCH knob until the sharp peak is seen on the oscilloscope display.
4. Now set the FREQUENCY dials (first two most-significant decades) until the front-panel meter nulls. The frequency digital readout will be within ±5 percent. If greater readout accuracy is desired, depress the AUTO CAL pushbutton and set the three least-significant decade dials for optimum oscilloscope display.

Remote Transfer Operation

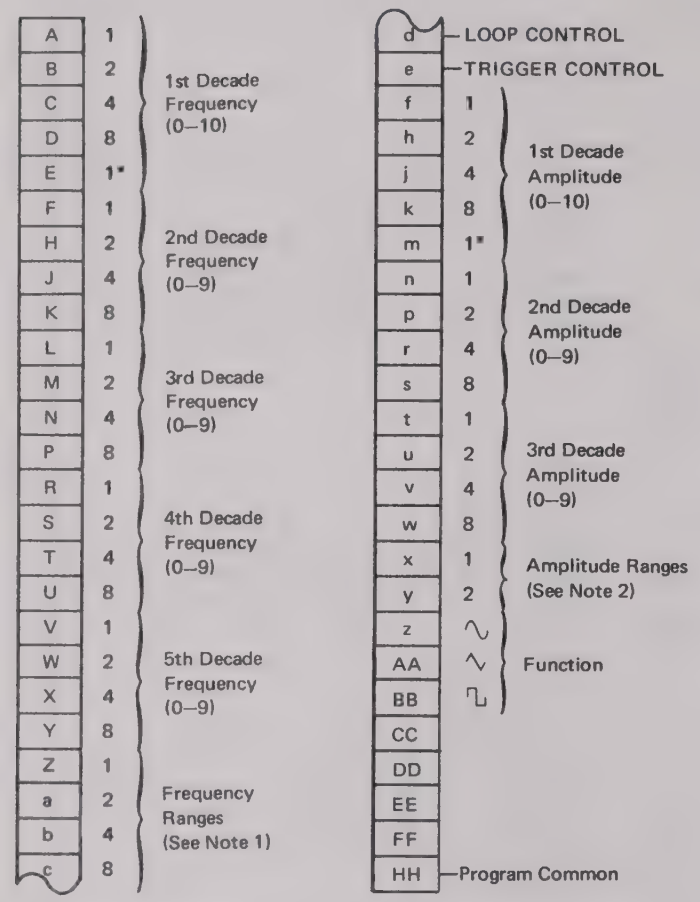
Transfer of operation to a remote switching or contact-closure function is described below. The instructions are for the transfer of all transferable control functions; however, any combination of remote and local control can be selected. A detailed description of the contact-closure or logic-level electrical requirements are contained under the following **PROGRAMMING** instructions.

1. Set all FREQUENCY dials, including the range dial, to R.
2. Set all TERMINATED P-P AMPLITUDE dials, including range dial, to R
3. Depress the generator mode REMOTE pushbutton.
4. Depress the function mode REMOTE pushbutton.
5. Connect the external command cable to the REMOTE connector.
6. If required, connect the external voltage device line to the VCG IN connector.
7. If required, connect the external pulse generator line to the TRIG IN connector.
8. Connect SYNC IN and/or SYNC OUT connectors to remote lines, as required.
9. Depress the POWER pushbutton.

PROGRAMMING

Instructions for programming are given in Figure 2-2. The modified BCD code in the frequency and amplitude five-line bytes consists of a 1* that carries a decimal weight of 1. This coding occurs only for the most-significant decade (MSD) of the frequency or amplitude selection lines. Programming a "1" on the 1* line in any combination with the associated BCD group, other than the decimal 9, is a *not allowed* condition.

The Model 157 requires static logic levels. If programming is by computer, a controller must be connected between the computer and the instrument to provide the static conditions.



To program "10", the code is 8, 1, and 1.

NOTE 1

PROGRAM	FREQ RANGE
0	1000 μHz
1	10 mHz
2	100 mHz
3	1000 mHz
4	10 Hz
5	100 Hz
6	1000 Hz
7	10 kHz
8	100 kHz
9	1000 kHz

NOTE 2

PROGRAM	AMPL RANGE
0	10 mV
1	100 mV
2	1000 mV
3	10 V

PROGRAM VOLTAGE

Refer to **Remote Control Specifications** in Section 1.

REAR BNC CONNECTORS

50-ohm amplifier output.
VCG input, Trig in, Sync in, Sync out.

Figure 2-2. Remote Programming Connector Pins

SECTION 3
CIRCUIT DESCRIPTION

INTRODUCTION

This section presents theory of operation as an aid to the user in understanding and maintaining the Model 157 Programmable Waveform Synthesizer. The block diagram in Section 5 graphically illustrates the functional features of the Model 157 and should be referenced when reading the theoretical discussion. In addition to the block diagram, references are made throughout the text to specific board schematic diagrams in Section 5 and simplified diagrams are used in this section to supplement the text for more complicated circuits. The theory in Section 3 is divided into six discussional parts:

1. Programming the various generator characteristics.
2. The 100 Hz to 1 MHz generation.
3. The 100 μHz to 100 Hz generation.
4. Sine conversion.
5. Tone-burst generation.
6. Power-supply characteristics.

PROGRAM SELECTION

Generator Mode Selection

The generator-mode selection circuit is graphically shown in the block diagram and detailed on sheet 1 of the Chassis Assembly schematic diagram and also the VCG/Integrator Board schematic diagram. When one of the pushbuttons in the generator mode row is depressed, the following circuit functions occur.

REMOTE switching disables the MAN CAL/SEARCH voltage input to the first VCG amplifier and transfers control of error amplifier output relay K5 to a remote connector pin so that either closed-loop or open-loop mode can be programmed.

AUTO CAL switching disconnects the MAN CAL/SEARCH voltage input to the first VCG amplifier and connects the error amplifier output to this amplifier so that the basic generator operates in closed-loop.

MAN CAL switching (momentary) connects the nullmeter across the error amplifier output and the output into the first VCG amplifier input so that the generator frequency may be accurately set with the MAN CAL/SEARCH control prior to analog control operation.

SEARCH switching connects the nullmeter between the inverter output and the manual calibration/search input to the first VCG amplifier. Also, the error amplifier output is disconnected from the first VCG amplifier input, which enables the VCG to sweep the basic generator over any single frequency range with one turn of the MAN CAL/SEARCH control.

TRIG switching turns on driver Q8 on the VCG/Integrator Board which energizes relay K6. The normally-open contact of K6 closes, causing emitter follower Q9 to turn off, and the generator subsequently enters the standby condition. (Refer to the description of tone-burst generation for additional details.)

Function Selection

The function-mode selection circuit is graphically shown in the block diagram and detailed on sheet 1 of the Chassis Assembly schematic diagram and the Function Board schematic diagram. When a specific pushbutton in this row is depressed, the following circuit functions occur:

REMOTE switching disconnects the front-panel function pushbuttons and connects the pins on the remote connector to the sine, square, and triangle relay drivers on the Function Board.

⊓ switching energizes relay K3 on the Function Board to provide the square waveform to the Output Amplifier Board.

∧ switching energizes relay K2 on the Function Board to provide the triangle waveform to the Output Amplifier Board.

∿ switching energizes relay K1 on the Function Board to provide the sine waveform to the Output Amplifier Board.

Frequency Selection

The frequency-selection circuit is shown in the block diagram as the first-decade driver/ladder through the fifth-decade driver/ladder, the range capacitors, the range switch, and the range matrix. The digital switch details are shown on sheet 1 of the Chassis Assembly Board schematic diagram; the first and second decade of the frequency digital-to-analog converter (DAC) in the Error Amplifier Board schematic diagram; the third through fifth decade of the frequency DAC in the Crystal One-Shot Board schematic diagram; the range capacitor/relay switching circuit in the VCG/Integrator Board schematic diagram; and the frequency-range diode matrix on the Range Matrix Board schematic diagram.

Frequency number selection of the first decade switch is converted from binary logic to an analog voltage that is proportional to the BCD coding by the first decade DAC shown on the Error Amplifier Board schematic diagram. The BCD code, in this case, is modified by a "1*" that has a binary weight of 2^0. All of the other frequency decade DACs have an unmodified BCD input. Figure 3-1 is a simplified diagram of a single binary input for one of the decades—all others function similarly.

Driver Q_1 is biased *off* by the +6-volt dc connection and relay K_1 is in its normally-open state, resulting in zero volts across R_1 and R_2 and zero contribution to the current in the summing nodes of the inverter and error amplifiers. When a logical "1" appears at the binary input to Q_1, the driver is turned *on* and the relay is energized. This connects 12 volts across R_1 and R_3 and the correct contribution to the summing nodes. Resistances R_1 and R_3 represent the network associated with the Q_1 driver, but R_2, R_4, R_5, and R_6 represent all of the other resistance in the frequency DAC. Similarly, relays K_2 and K_3 represent their associated circuits with driver/relay stages. Thus any parallel combination of resistance can be achieved by programming to provide an analog current input to the VCG that is proportional to the selected frequency.

The frequency range selector output is converted from binary logic to relay-closure functions by the diode matrix shown in the Range Matrix schematic diagram. The 8421 BCD coding from the FREQUENCY range selector switch, or remote programming, is decoded into decimal format shown at the right of the schematic diagram. Table 3-1 shows the correlation between these decimal numbers and the resulting relay closures.

Relays K4 through K6 on the Function Board perform the fast-slow switching function. Relays K5, K6, K7, and K8 on the Range Matrix Board are paralled by relays K4, K3, K2,

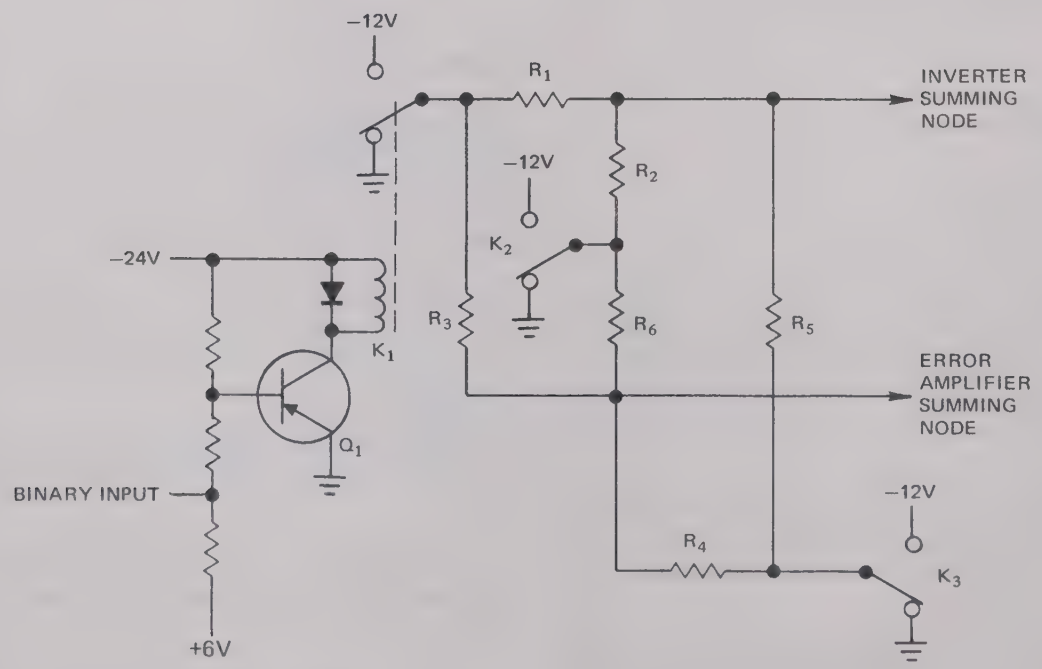


Figure 3-1. Frequency Selection, Simplified Diagram

Table 3-1. FREQUENCY RANGE CODING

Board Desig	Relay Desig	Digital Program 0	1	2	3	4	5	6	7	8	9
A11	K1	1	0	0	0	0	0	0	0	0	0
A11	K2	0	1	0	0	0	0	0	0	0	0
A11	K3	0	0	1	0	0	0	0	0	0	0
A11	K4	0	0	0	1	1	1	0	0	0	0
A11	K5	1	1	1	1	0	0	1	0	0	0
A11	K6	0	0	0	0	1	0	0	1	0	0
A11	K7	0	0	0	0	0	1	0	0	1	0
A11	K8	0	0	0	0	0	0	0	0	0	1
A10	K4	1	1	1	1	0	0	1	0	0	0
A10	K3	0	0	0	0	1	0	0	1	0	0
A10	K2	0	0	0	0	0	1	0	0	1	0
A10	K1	0	0	0	0	0	0	0	0	0	1
A9	K4-6	0	0	0	0	0	0	1	1	1	1

and K1 on the VCG/Integrator Board, respectively, which switch in the correct capacitance for the programmed range. (Refer to description of basic generator for additional details.) The relays on the Range Matrix Board also provide circuit switching for the divider flip-flops that connect to the crystal one-shot circuit and to the low-frequency generator. (Refer to the descriptions of the closed-loop basic generator and the low-frequency generator for additional details.)

The frequency range selector (SW6) also applies power to the proper decimal-place indication lamp (L1, L2, or L3) as shown on sheet 1 of the Chassis Assembly schematic diagram.

Amplitude Selection

The amplitude-selection circuit is shown in the block diagram as the signal attenuator and the range attenuator. The digital switch details are shown on sheet 1 of the Chassis Assembly schematic diagram; the three decades of the amplitude attenuation on the Output Amplifier Board schematic diagram; and the range/attenuation circuit (A5) on sheet 2 of the Chassis Assembly schematic diagram.

The amplitude-number selection circuitry is similar to the frequency-number DAC circuit previously described.

The amplitude-range selection circuit, contained on the Output Attenuator Board, consists of a 20-db attenuator and a 40-db attenuator, either or both of which are switched into the path of the output waveform by the binary program. Table 3-2 is a truth table of the binary code expanded to list associated circuit functions.

Table 3-2. ATTENUATOR RANGE CODING

A5-6	A5-7	Range (max)	20-db	40-db
0	0	10 mV	X	X
1	0	100 mV		X
0	1	1000 mV	X	
1	1	10 V		

NOTE: X denotes attenuator in circuit.

BASIC GENERATOR

The basic generator is shown in block diagram as the first and second voltage-controlled generator amplifiers, the integrator, and the hysteresis switch. This generator produces both triangle and square waveforms over a 100 Hz to 1 MHz range in open-loop operation. For closed-loop operation, the divide-by-ten stages, the one-shot stage, the frequency-converter circuit, and the error amplifier are connected between the output of the hysteresis switch and the input of the first VCG.

Open-Loop Operation

When power is applied, the high-gain characteristic of the integrator causes this device to produce an output voltage that is equal to one of the threshold levels of the hysteresis switch. The switch assumes one of its binary states and regeneration proceeds in the following manner.

If the bistable output of the hysteresis switch is negative, the diode connecting the second VCG amplifier to the integrator (D_2 in Figure 3-2) is zero biased by the diode connecting the second VCG amplifier to the hysteresis switch (D_3). With this condition, current proportional to the frequency program flows from the integrator summing node to the first VCG amplifier and the positive slope of the triangle is formed at the integrator output. When the output level reaches +2.5 volts, the hysteresis switch reverses state to provide a positive output. This zero biases diode D_3 to permit current to flow from the second VCG amplifier into the integrator summing node. This current is precisely twice the value of the current flowing into the first VCG amplifier; therefore, a net current flows that is equal in value, but in the opposite direction, to the current produced by the negative output of the hysteresis switch. The net current causes the integrator to produce the negative slope of the triangle at its output. When the integrator output reaches −2.5 volts, the hysteresis switch again reverses states and the cycle is repeated.

The output voltages of the two VCG amplifiers are always equal in value, but opposite in polarity, and directly proportional to the frequency program.

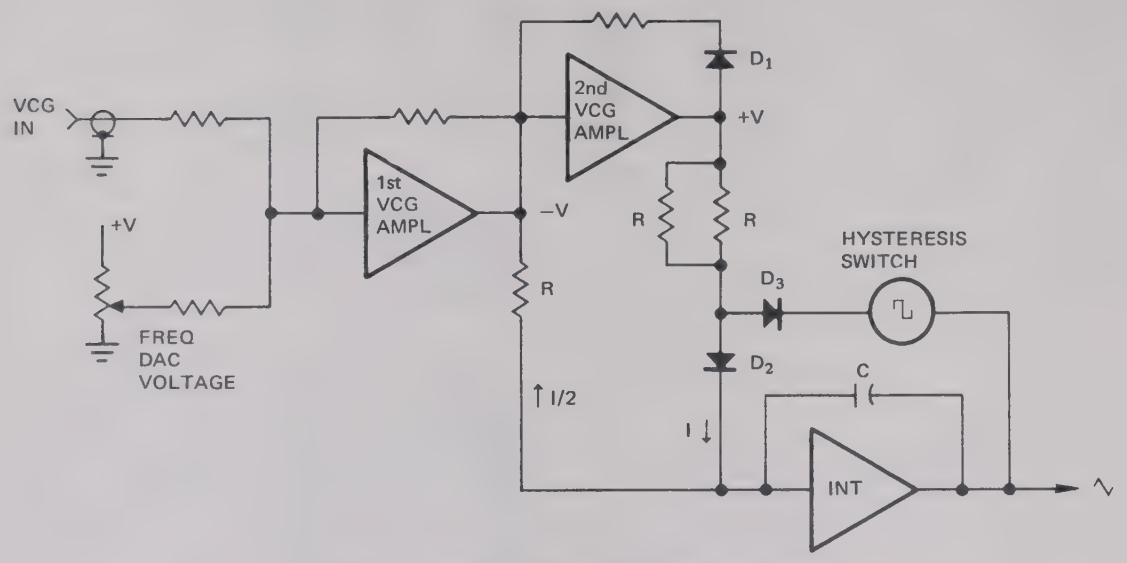


Figure 3-2. Open-Loop Operation, Simplified Diagram

If an external voltage is applied to the VCG IN connector, the voltage levels at the outputs of the VCG amplifiers will change proportionally, thereby providing frequency modification or modulation.

On the VCG/Integrator schematic diagram, the first VCG amplifier is the Q1/IC1 stage; the second VCG amplifier, the Q3/IC3 stage; the diode switch, the IC2 stage; and the integrator, the Q14/IC4 stage. The hysteresis switch, on the Function Board schematic diagram, consists of a complementary Schmitt trigger coupled to a bistable switch. Stage Q22/Q23/Q24 form one half of the Schmitt trigger, and stage Q25/Q26/Q27 form the other half. Similarly, Q28/Q29 are one half of the bistable switch and Q30/Q31 the other half. The single-ended 1 volt peak-to-peak square-wave output of the bistable switch is the VCG diode switching voltage.

Closed-Loop Operation

Frequency accuracy of 0.01 percent is obtained by connecting an error-sensing circuit into a feedback loop around the basic generator. With either the AUTO CAL or MAN CAL pushbutton depressed, the output of the error amplifier is connected to the input of the first VCG amplifier through the relay closure shown in the block diagram.

The error-sensing circuit consists of the three switched divide-by-ten stages; the one-shot stage, and the pulsed reference current switching circuit.

The crystal one-shot circuit operates over a 10:1 frequency range from 1 kHz to 100 Hz. The frequency dividing stages are switched between the hysteresis switch output and the one-shot input for the range from 1 kHz to 1 MHz by the range matrix (previously discussed). Since the three divide-by-ten stages are cascaded, a total of 0.001 F is available which, of course, is sufficient to reduce the basic generator top range of 1 MHz to the one-shot top range of 1 kHz.

The output of each divide-by-ten stage, shown in the Range Matrix Board schematic diagram, is coupled to the error-sensing circuit and to the low-frequency generator. This discussion will be limited to the error-sensing circuit; the low-frequency generator will be discussed later.

Dual flip-flop stages IC1/IC2, IC3/IC4, or IC5/IC6 are logically connected so that each paired dual flip-flop provides a divide-by-ten output. When relay K5 is digitally programmed, the output to the one-shot circuit is the frequency of the basic generator, closure of K6 contacts, 0.1 F; closure of K7 contacts, 0.01 F; and closure of K8 contacts, 0.001 F.

The crystal one-shot circuitry is shown in the upper half of that schematic diagram in Section 5. A free-running crystal-controlled oscillator (Y1/Q1) produces an accurate 4 MHz sinusoidal output. Potentiometric amplifier Q2-Q4 provides buffering for the oscillator. The buffered output is applied to the trigger input of a JK flip-flop which is configured logically with thirteen other flip-flops to

produce an accurate 400-microsecond rectangular output 400 microseconds after a square-wave input is received from the divide-by-ten circuits.

The Q5/Q6 input stage couples the incoming signal into a differentiator (C8/R12). The differentiated pulse presets the upper flip-flop (IC7A) in the IC7 stage. The set output of this flip-flop enables the IC1A flip-flop. The count begins with the first oscillator cycle and, after 400 microseconds. IC7B is set, producing a high state at pin 9 (see Figure 3-3 for PRF timing). After 400 microseconds IC7B is reset, which sets IC7A to disable the trigger input to IC1A.

This cycling is repeated for each square-wave input. Thus, the output of the one-shot circuit is a rectangular waveform, having a duty cycle proportional to the pulse reoccurrence time and, hence, the frequency of the basic generator.

The offset +6-volt regulator (IC2), the PRF switching circuit (Q1/Q2/IC3), and the error amplifier subassembly are shown in the upper half of the Error Amplifier Board schematic diagram. Figure 3-4 is a simplified diagram of this same circuit.

The summing node of the error amplifier is the point where a comparison is made to tell the generator if it is running too fast or too slow.

The program is converted to a precise current flowing out of this summing node. If this current is not balanced perfectly by an interrupted current from the +6 V reference through R15 and R16, then the output of the error amplifier will tell the generator to run faster or slower so that the pulse rate from the crystal one shot will be corrected, which brings about a balance at the summing node.

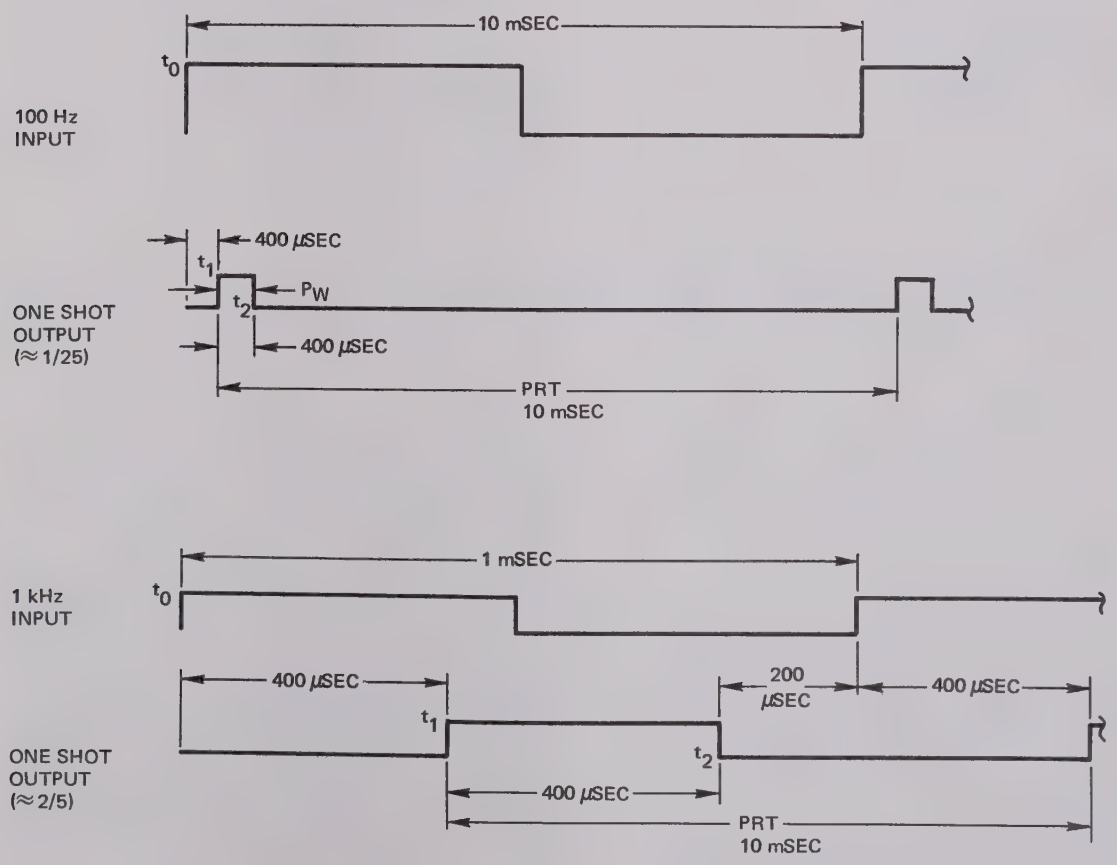


Figure 3-3. One-Shot Pulse Period Ratios for Minimum and Maximum Frequencies

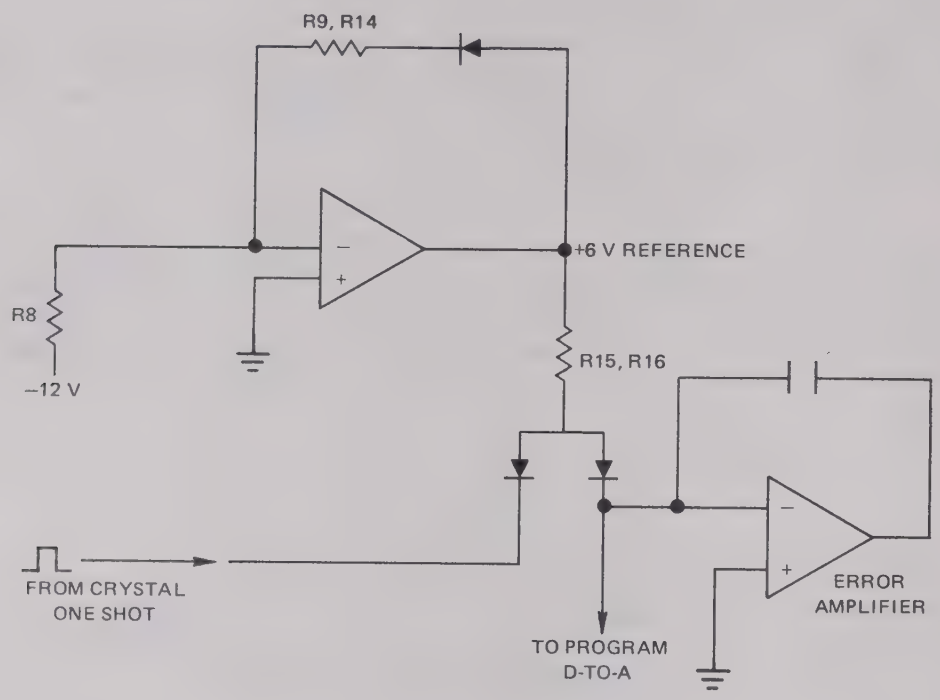


Figure 3-4. Pulsed Reference Current Switching Circuit, Simplified Diagram

LOW-FREQUENCY GENERATOR

Triangle and square waveforms are digitally generated in the frequency ranges covering the band from 100 μHz to 100 Hz. This circuitry consists of the divide-by-ten stages, cyclic counter, and triangle DAC shown in the block diagram.

Slow Square-Wave Generation

The cyclic counter is logically configured to count from 6 to 506 and then back down to 6 again to produce square-wave outputs that are 0.001 F of the input frequency from the basic generator. The divide-by-ten states are progressively switched between the output of the basic generator and the input to the cyclic counter so that the low-frequency range (100 Hz) of the basic generator may be extended down to the 100 μHz low-frequency range of the instrument. This operation is essentially the same as previously described for providing the correct frequency input to the error-sensing circuit.

The simplified logic diagram for the counter appears in Section 5. The parallel trigger inputs to each flip-flop are not shown, but enter directly without being gated. All other inputs to the flip-flops are controlled by NOR gates which require *all* inputs to be false to produce a true output. When a true output from a NOR gate occurs, both

the set and reset inputs are enabled and the flip-flop will change state with the next clock pulse into its trigger input terminal.

When the last flip-flop in the counter is set at the No. 506 clock-pulse input, the logic gating causes the next clock-pulse input to begin sequentially resetting the flip-flops, starting with the highest order flip-flop. Thus after No. 506, the counter output steps down until No. 6 is reached and the cycle is repeated.

Two binary (square-wave) outputs are taken from the cyclic counter as square waveforms and connected to other boards. The No. 256 count is coupled to the trigger circuit on the VCG/Integrator Board (described later in the tone-burst generator theory), and the No. 506 count is sent to the square-wave amplifier/clamp on the Function Board.

As shown in the Function Board schematic diagram, the *slow* ⊓⊔ is applied to the amplifier/clamp circuit that provides the 5-volt peak-to-peak square-wave output. Stages Q5/Q6 and Q9/Q10 consist of a differential amplifier driving a compound configuration to provide the collector supply for the positive excursion of the square-wave output. Stages Q7/Q8 and Q11/Q12 are the complementary equivalent of the circuitry just described and provide the negative-excursion of the square-wave output.

Slow Triangle Wave Generation

The triangle DAC, shown in the block diagram as the 9-bit digital-to-analog converter, produces the digitally-generated *slow* triangle waveform over the 100 μHz to 100 Hz frequency ranges of the instrument.

The triangle DAC consists of the nine diode-bridge switching circuits (IC25 through IC33), the +6-volt regulator (IC22), the −6-volt regulator (IC23), and the summing amplifier (IC24/Q3/Q4) shown on the Counter Board schematic diagram. The counter and DAC are capable of binary outputs up to 2^9 (512_{10}), but are logically configurated to provide a count of 500.

Figure 3-5 is a simplified diagram of the triangle DAC. The binary inputs from the counter cause the nine diode-bridges to switch on in their logical binary sequence. Since all of these bridges are connected in parallel, the resulting output from the summing amplifier is a triangle waveform at 0.001 F of the basic generator frequency.

The slow triangle output is connected to the slow/fast relay switching circuit for application to the sine converter (discussed later) and the function-selection relay-switching circuits (discussed previously).

SINE CONVERSION

The selected, slow or fast, triangle waveform is transformed into a sine waveform by the sine converter shown in the block diagram (Section 5, drawing 157-600). In the Function Board schematic diagram, this circuit comprises IC1 through IC4. Figure 3-6 is a simplified diagram of the sine conversion circuit.

The sine converter is composed of four diode bridges in IC1 through IC3 which behave as soft clippers. Each diode bridge operates on the triangle wave in a way to shape it into a trapezoidal current wave. Each trapezoid is cut from the triangle at a different level, or phase angle. All of these trapezoidal waves are summed in the node of the operational amplifier IC4. The phase angle and weight of each cut is chosen so that the sum of all comes out to be a low distortion sine wave. The phase continuity of the signal is then restored with the inverter IC5.

TONE BURST GENERATION

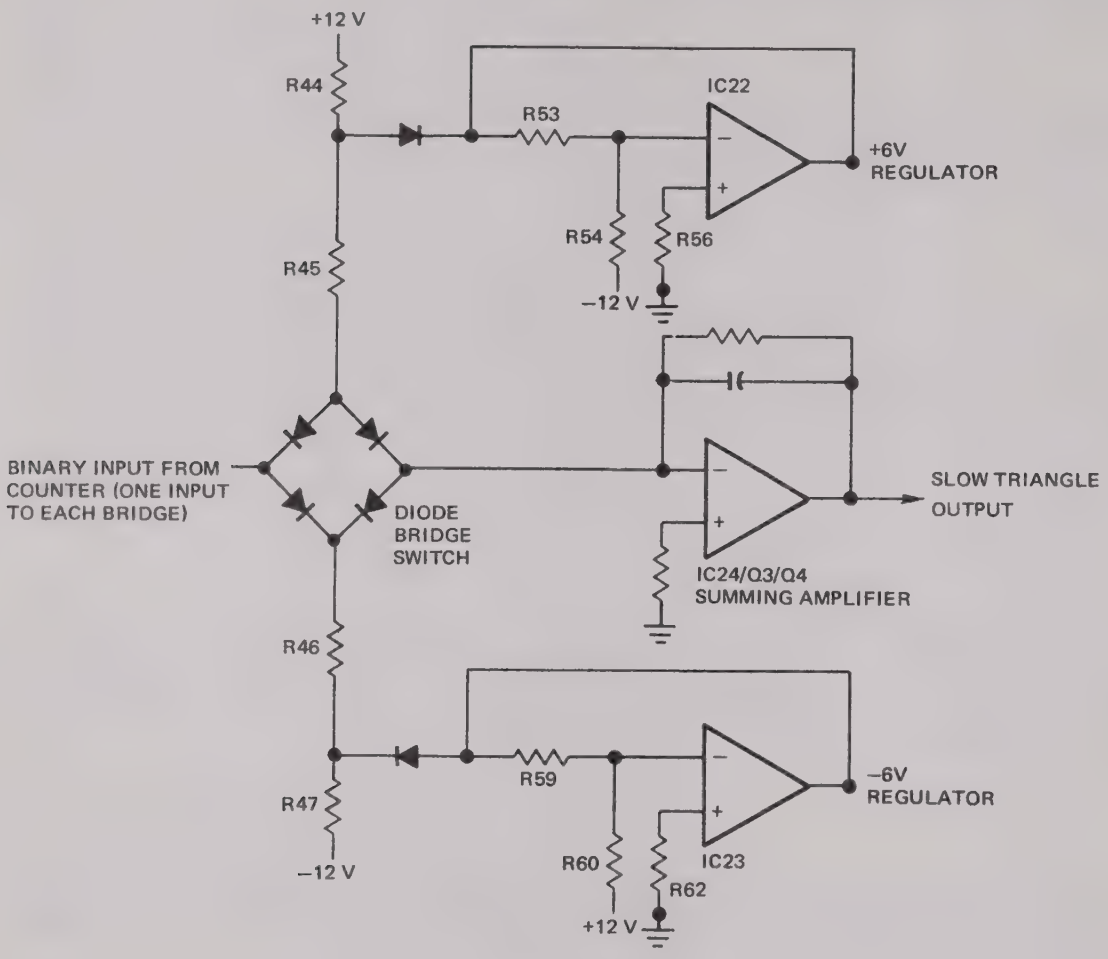
When the TRIG pushbutton is depressed, or a logical "1" is programmed during remote operation, driver Q8 on the VCG/Integrator Board draws current through the coil of relay K6, causing the normally-open contact of this relay to close (Figure 3-7).

Prior to the closing of the K6 contact, emitter follower Q9 is turned on by the +12-volt path through CR5 and associated resistors, and flip-flop IC6 is clamped in the set state by approximately +3 volts at the preset input. The set state of IC6 disables the standby circuit and the integrator provides its programmed output.

Turn off of Q9, however, removes the set state clamp from the flip-flop, and the next square-wave clock input causes the flip-flop to assume the reset state. The set output, "Q", is now a positive voltage sufficient to turn on grounded-base amplifier Q12 which, in turn, enables emitter follower Q11. The feedback path around the integrator is now a short circuit around the programmed range capacitance through the Q13/Q11 path and the integrator is in the "standby state"—no output.

With the trigger function programmed, a trigger input causes the flip-flop to resume the preset state to disable the standby circuit. Figure 3-8 is the timing chart for this operation. The generator output triangle resumes immediately at the programmed frequency, and the square wave starts at the triangle peak as previously described.

At the end of the trigger duration, the flip-flop is reset to the standby state, but the tone burst continues until the integrator output compares with the integrator input



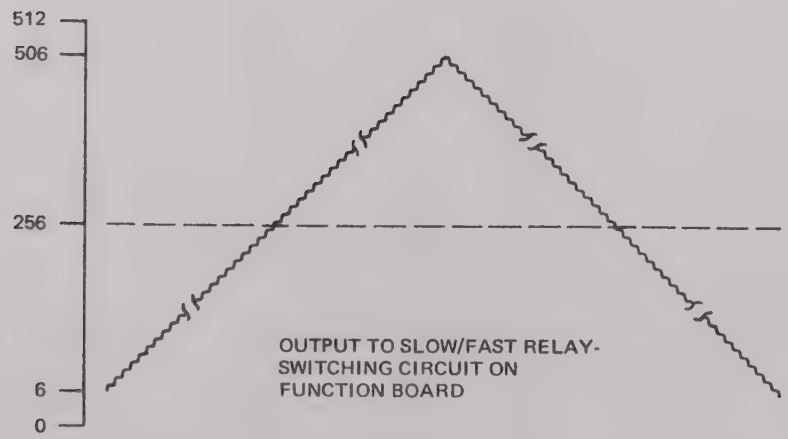


Figure 3-5. Slow-Triangle Digital-to-Analog Converter, Simplified Diagram

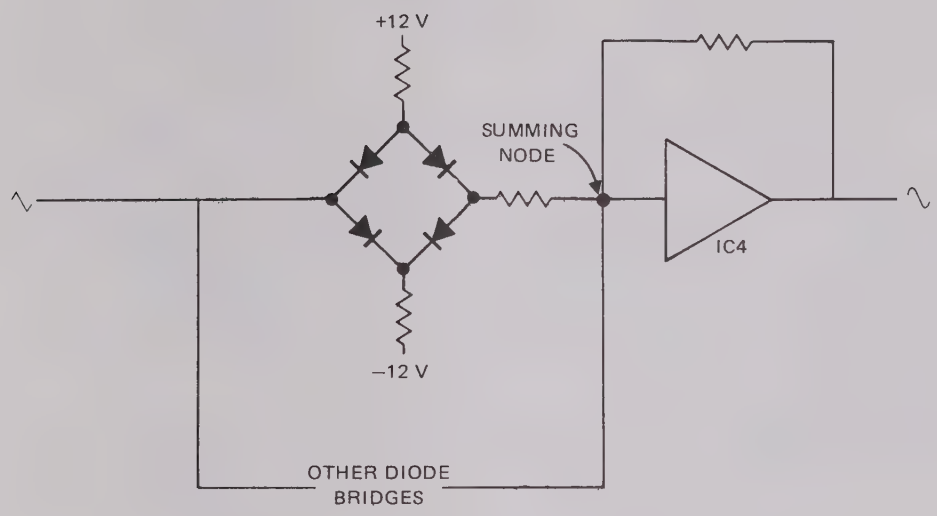


Figure 3-6. Sine Conversion Circuit, Simplified Diagram

(triangle wave reaches zero-crossing on the positive-going edge of waveform as shown by the *time to complete last cycle* period in the tone-burst timing chart). The standby circuit holds the generator in the standby condition until another trigger input is received, or another mode is programmed.

POWER SUPPLY

Regulated operating voltages for the solid-state devices of the Model 157 are –24, +12, –12, +6, and +3.6 volts dc. Each voltage is regulated by an operational amplifier controlling a series-regulator transistor. Figure 3-9 is a simplified diagram that illustrates the complete function for the regulated –24-volts dc.

The 115 ac line voltage is connected through fuse F1 and a choke-input RF filter to the POWER pushbutton on the front panel. When this pushbutton is depressed, alternating current flows through the paralleled primary windings of transformer T1. (If line voltage is 230-volts ac, the primaries can be series-connected at SW11.

Although there are six secondary windings for T1, as shown on sheet 2 of the Chassis Assembly schematic diagram, only one winding is shown for clarity—the other circuits are similar. The secondary winding is connected across a conventional diode-bridge full-wave rectifier, consisting of diodes CR14 through CR17. Capacitor C21 filters the ac ripple. The unregulated voltage is applied to the collector of series-regulator Q3, which is controlled by the operational amplifier, consisting of integrated circuit IC3 and transistors Q19 through Q20. The reference voltage is provided by a pair of transistors connected to form a Zener diode (Q22 and Q23). The operational amplifier inverts its input to control the series regulator. If the –24-volt output rises, the forward bias on Q3 is decreased, which causes the collector-emitter current to decrease, and the output returns to the correct level. Conversely, if the –24-volt output decreases, the forward bias on Q3 is increased and its collector-emitter current is increased.

Figure 3-10 is a simplified diagram of the +6-volt regulation circuit. Transistor Q14 is the series regulator. The controlling operational amplifier consists of differential amplifier Q16/Q18 and emitter follower Q15.

The unregulated voltage is applied to the collector of the series regulator. The –24-volt dc input is coupled through R46 into the inverting input of the first stage of the operational amplifier, differential amplifier Q16/Q18. The inverted output of Q16 is direct coupled to the base of emitter follower Q15 which supplies the bias for the series regulator. Resistor R45 is the feedback resistor and transistor Q17 is the current limiter for series regulator Q14.

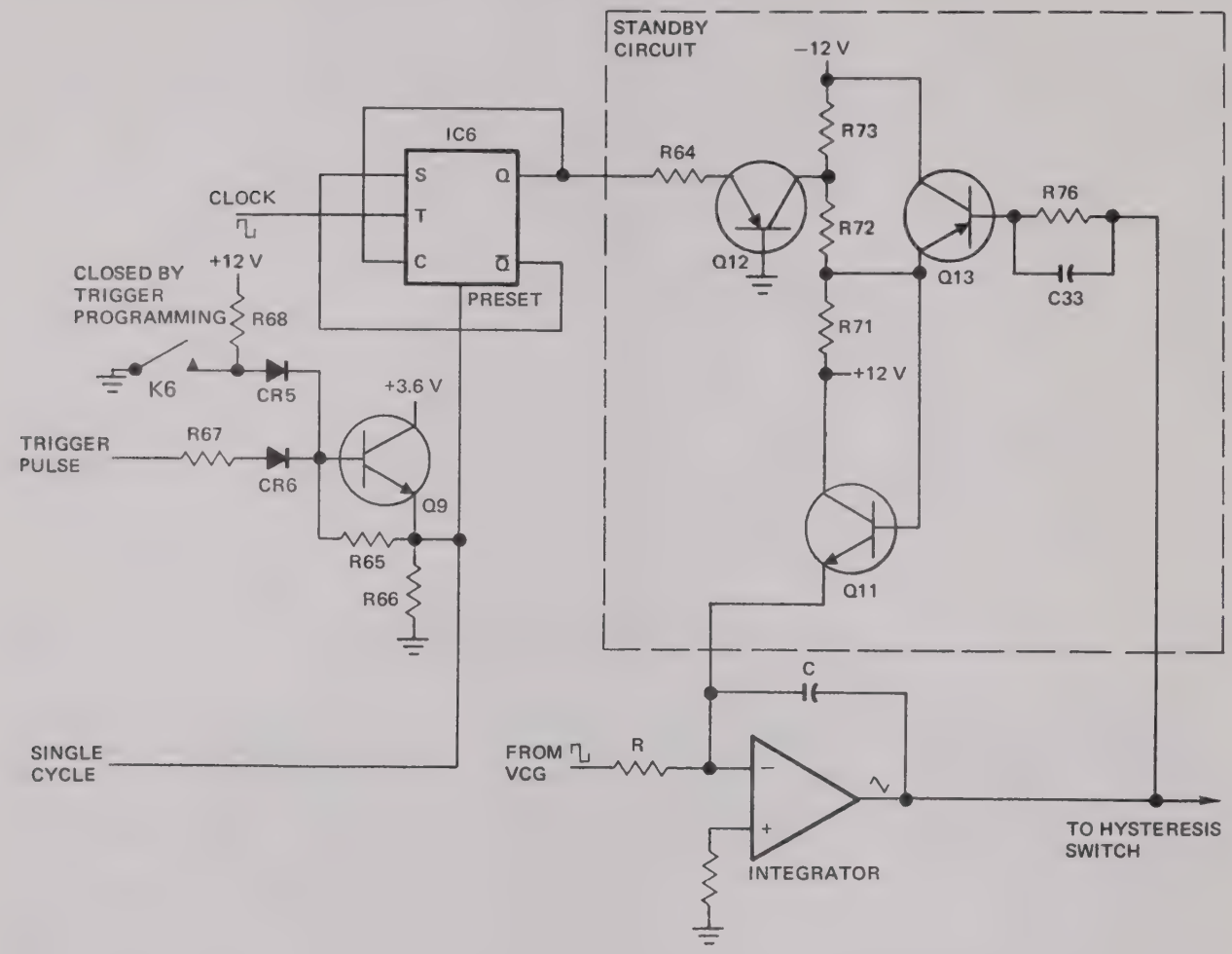


Figure 3-7. Trigger Gating Circuit, Simplified Diagram

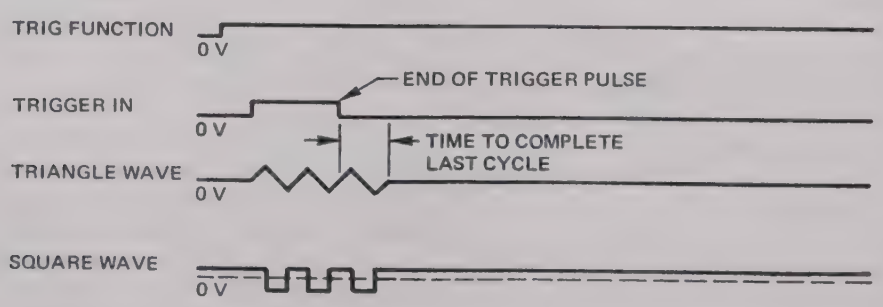


Figure 3-8. Tone Burst Timing Chart

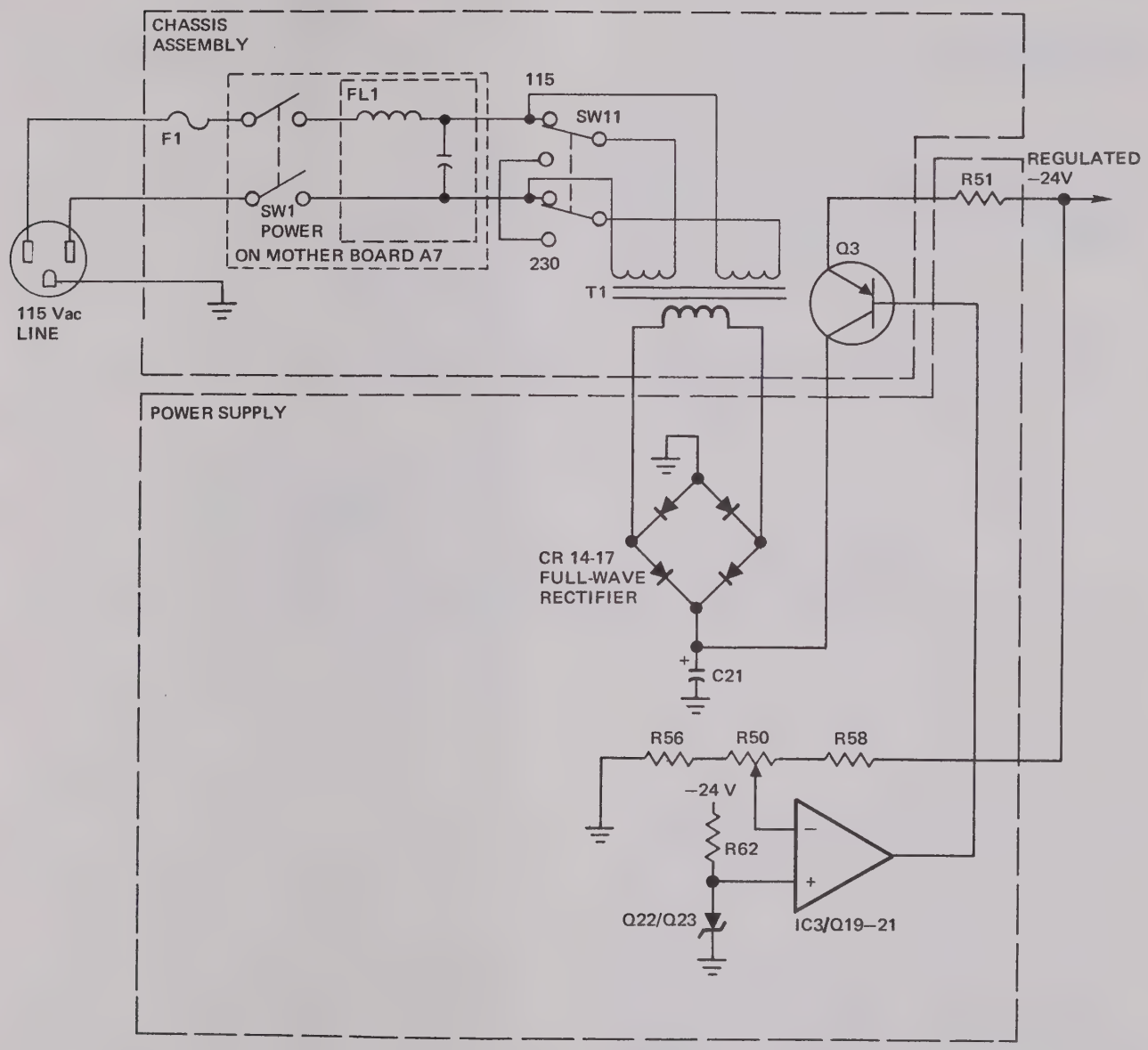


Figure 3-9. Negative 24-volt Regulation, Simplified Diagram

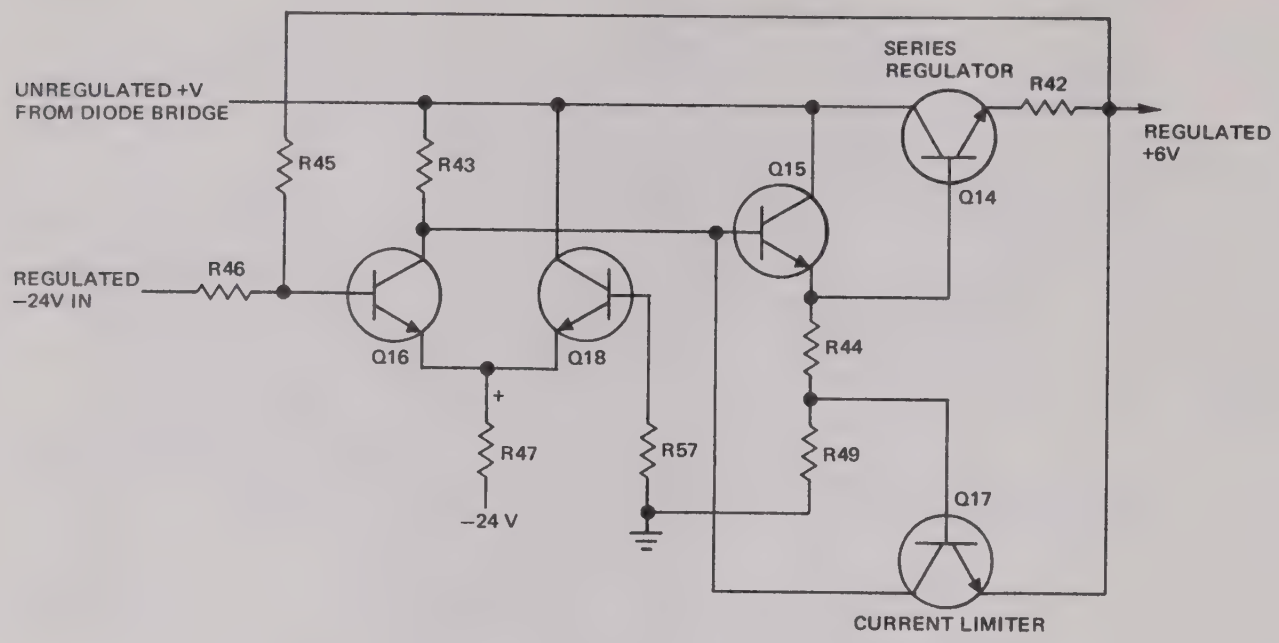


Figure 3-10. Operational Amplifier Regulator, Simplified Diagram

SECTION 4
MAINTENANCE

INTRODUCTION

This section provides instructions for testing, adjusting, calibrating and troubleshooting the Model 157. The instructions are directed to the experienced electronics technician or field engineer. Wavetek maintains a factory-repair department for those customers not possessing the necessary personnel or test equipment to maintain the instrument. If the instrument is returned to the factory for repair, a detailed description of the specific problem should be attached to facilitate the turnaround time.

NOTE

The Model 157 requires one hour warm-up for stabilization before output parameters are within specified tolerances. Prepare for this in advance of any planned checkout, calibration, or troubleshooting programs. For amplitude checks, use a precise 50Ω load. A 0.1% error in load resistance will cause a 0.05% error in the output voltage level. Because several characteristics are temperature sensitive, the instrument should be covered as much as possible during calibration. A lucite cover with access holes is recommended.

RECOMMENDED TEST EQUIPMENT

Table 4-1 contains a list of recommended test equipment for maintenance. Any test equipment having equal accuracies may be substituted for those listed.

Table 4-1. RECOMMENDED TEST EQUIPMENT

Name	Manufacturer	Model
Oscilloscope	Tektronix	543B
Plug-In Unit	Tektronix	W, 1A7 or 1A5
Electronic Counter (6 Digit)	——	——
Extender Board	Wavetek	157-111
Distortion Analyzer	Hewlett-Packard	332A or 334A
Digital Voltmeter (3½ Digit)	——	——
Function Generator	Wavetek	180

CHECKOUT AND CALIBRATION

Circuit board test point and adjustment locations for the following calibration procedures are shown in figures 5-1 through 5-8. Refer to the troubleshooting procedures when a check or calibration indicates incorrect performance.

NOTE

The entire calibration procedure should be read before using to become acquainted with control settings, test equipment connections, and calibration points.

Preliminary Procedure

1. Set generator mode for REMOTE.
2. Press the POWER pushbutton.
3. Wait 1 hour for warm-up.

NOTE

Setting any rotary digital switch to R is the same as setting it to 0.

Power Supply Regulation

1. Connect the voltmeter high input to TP2 and the low input to TP1 on the power supply board. Adjust R7 for +12 Vdc ±10 mV.
2. Move voltmeter high input to TP3 and check for −12 Vdc ±10 mV.
3. Move voltmeter high input to TP4 and check logic V_{cc} for +2.8 Vdc ±200 mV.
4. Move voltmeter high input to TP5 and check logic common for −0.7 Vdc ±100 mV.
5. Connect the voltmeter high input to TP7 and the low input to TP6. Adjust R50 for +24 Vdc ±50 mV.

CAUTION

Care must be taken not to short TP8 to the top of R62 (−24 V), as this will cause Q18 in the +6 Vdc supply to short out. (The supply will go to +9 to +11 V.)

6. Move voltmeter connection to TP8 and check for +6 Vdc ±200 mV.

Frequency, Amplitude and Trigger Controls

1. Observe the triangle output of the 157 on an oscilloscope terminated with a 50Ω load.
2. Check the frequency ranges for proper ranging.
3. Check each FREQUENCY digit dial for proper frequency change.

NOTE

For the 10 Hz range and below, use ∿ function and adjust oscilloscope to observe slow frequencies. On 10 mHz and 1000 μHz ranges, the stepping of the "digital triangle" will be observed.

4. Check each amplitude digit dial for proper amplitude change.
5. Check the amplitude range dial for proper amplitude ranging.
6. Check the SEARCH mode to insure that the 157 searches at least 15:1.
7. Put the 157 in the trigger mode and check for proper operation of the SINGLE CYCLE switch.
8. Connect an external trigger into the TRIG IN BNC and check for proper triggering.

Square Wave Offset and Amplitude

1. Connect the voltmeter with precise 50Ω terminator to the 50Ω OUTPUT connector on rear panel.

NOTE

Terminator resistance must be ±0.1% of 50Ω.

2. Set the 157 as follows:

Mode	AUTO CAL
Function	REMOTE
FREQUENCY Dials	100.00 mHz
TERMINATED P-P AMPLITUDE Dials	10.00 V

3. Adjust R10 on the output amplifier board for 0 V ±2 mV.
4. Set output function to ⊓ .
5. With DVM set to measure negative half-cycle of square wave, adjust R34 on the function board for −5 V ±5 mV.
6. With DVM set to measure positive half-cycle of square wave, adjust R51 for +5 V ±5 mV.
7. Track the amplitude digits from 10.00 to 1.11 to insure that positive and negative peak amplitudes are within specification.

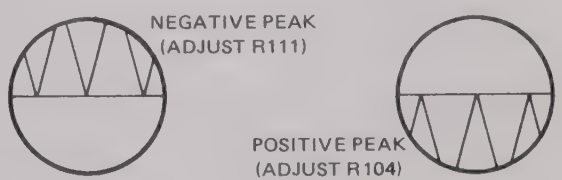
Setting	Allowable Peak Voltage
10.00	5.0075 to 4.9925
8.88	4.4465 to 4.433
7.77	3.891 to 3.879
6.66	3.3355 to 3.324
5.55	2.780 to 2.7695
4.44	2.2245 to 2.2155
3.33	1.669 to 1.661
2.22	1.1135 to 1.1065
1.11	0.5580 to 0.5520

8. Set the frequency dials as shown and check the positive and negative peak amplitudes (indicates proper attenuator operation).

Amplitude (mV)	Allowable Peak Voltage (mV)
1000.0	0.506 to 0.495
100.00	0.0506 to 0.0495
10.000	0.0051 to 0.0049

Fast Triangle Amplitude

1. Connect the oscilloscope with precise 50Ω terminator to the 50Ω OUTPUT connector. Set controls for voltage-comparison measurement of 10 V peak-to-peak range and 10 mV/cm sensitivity.
2. Set the FREQUENCY dials for 100.00 Hz and output function for ⊓ . Display two cycles on scope.
3. Display the negative half-cycle of the square wave with the peak at a convenient reference line on the graticule.
4. Set the output function to ∿ , and adjust R111 on the function board until the triangle peak is within 5 mV of the square wave peak.
5. Set the output function at ⊓ and display the positive half-cycle of the square wave peak at the reference line.
6. Set the output function to ∿ and adjust R104 so that the triangle peak is within 5 mV of the square wave peak.



Time Symmetry and VCG Null

1. Set generator as follows:

Mode	All buttons in out position
Function	⊓
FREQUENCY Dials	R1.000 kHz

2. Set oscilloscope for 0.1 ms/cm. Adjust generator output for one cycle on the graticule using the MAN CAL/SEARCH knob.

3. Momentarily short TP9 (ground) on the VCG/integrator board to TP10 (first VCG summing node) and observe the display. Adjust R16 until connecting and disconnecting the short results in minimum change in the falling edge.
4. Set the FREQUENCY dials for R01.00 kHz and function to ∿ . Adjust R46 for straight slopes on the triangle waveform. Adjust MAN CAL/SEARCH knob for one cycle of display.
5. Set the FREQUENCY dials for 1000.00 Hz and output function for ⊓ . Set the sweep magnifier of the oscilloscope to X 100. Adjust R19 for best time symmetry by changing from positive to negative triggering on scope.
6. Set the FREQUENCY dials for R1.000 kHz and the oscilloscope sweep magnifier to X 10. Adjust R79 for best time symmetry.
7. Set the FREQUENCY dials for R01.00 kHz and adjust R80 for best time symmetry. (It is normal for this waveform to be jittery.)
8. Repeat steps 5 through 7 for optimum adjustment.
9. Set the FREQUENCY dials for 1.0000 kHz and observe that the time symmetry of the square wave is within 0.1% (1 cm at sweep magnifier of X 100). If this is correct, steps 1 through 8 have been done to the proper precision.

Frequency Alignment

NOTE

Perform the following meter adjustment while simultaneously holding the MAN CAL and AUTO CAL pushbuttons depressed.

1. Set the FREQUENCY dials for 1000.00 Hz and adjust R21 on the VCG/integrator board for a null indication on the front panel meter (center of meter).
2. Set the FREQUENCY dials for 100.00 Hz and adjust R1 for a null indication.
3. Repeat steps 1 and 2 until optimum adjustment is obtained.
4. Set FREQUENCY dials to 1000.00 Hz and adjust R99 on the error amplifier board for minimum meter deviation as dials are changed to 990.00 Hz and back to 1000.00 Hz.
5. Set the FREQUENCY dials to 10.0000 kHz and check for a close-to-null indication on the front panel meter.
6. Set the FREQUENCY dials to 100.000 kHz and adjust C3 for a null indication.
7. Set the FREQUENCY dials for 1000.00 kHz and adjust C4 for smallest deviation from null as MSD (Most Significant Digit) is stepped through its range.

NOTE

Meter indication in step 7 normally will vary widely over this range, but should not peg.

Closed Loop Frequency Adjustment

1. Connect a counter to the 50Ω output connector using a 50Ω termination. Set the counter to read period or frequency (minimum 6 digits).
2. Set the generator mode for AUTO CAL.
3. Set the FREQUENCY dials for 1000.00 Hz and adjust R43 on the error amplifier board for 1000.0 μs (1000.00 Hz) on the counter.
4. Set the FREQUENCY dials for 100.00 Hz and adjust R20 for 10000 μs (100.00 Hz) on the counter.
5. Repeat steps 3 and 4 until optimum adjustment is obtained.

Sine Conversion and Slow Sine

1. Set the generator to AUTO CAL mode, ∿ output function, and 100.00 Hz. Connect the distortion analyzer and oscilloscope to the 157 as shown in figure 4-1 and adjust R15, R6, R68, and R93 of the function board for minimum distortion.

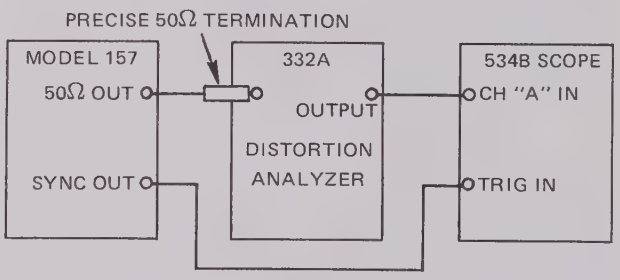


Figure 4-1. Distortion Test Setup

2. For slow sine adjustment, change the frequency to 100.000 Hz and adjust R70 and R65 of counter for minimum distortion.
3. Remove the generator output from the distortion analyzer and connect to the oscilloscope.
4. Set the oscilloscope controls for voltage comparison measurement of 10 V p-p range and 10 mV/cm sensitivity.
5. Set the FREQUENCY dials for 100.00 Hz and function to ⊓ .
6. Display the negative half-cycle of the square wave with the peak at a convenient reference line on the graticule.

NOTE

The figures below indicate sine and square wave amplitude relationships and do not necessarily show precise comparisons.

7. Set the function to ∿ ; observe the relative position of the negative half-cycle of the sine to that of the square.

8. Observe the positive half-cycle of the sine wave.
9. Set the function to ⊓, observe the relative position of the positive half-cycle of the sine wave in relation to the positive half-cycle of the square.

10. Adjust R21 on the function board so that the positive and negative sine half-cycles are equally balanced relative to the square wave.

11. Adjust R23 on function board so negative and positive half-cycles are at the same amplitude as the square wave half-cycles.
12. Repeat steps 10 and 11 until the sine half-cycles are within specifications.

Trigger

1. Connect the oscilloscope to the 50Ω OUTPUT using a 50Ω terminator.
2. Set the 157 controls as follows:

Mode	TRIG
Function	∿
FREQUENCY Dials	1000.00 Hz
TERMINATED P-P AMPLITUDE Dials	10.00 V

3. Adjust R74 on the VCG/integrator board for minimum peak dc voltage change as the FREQUENCY dials are stepped down through the range to 100.00 Hz (may change ±50 mV).

NOTE
Approximately 5 mV change is normal.

Frequency Loop Final Calibration

1. Connect the counter to the 50Ω output using a 50Ω termination. Set the counter to read period.
2. Set the generator mode to AUTO CAL, output function to ⊓ and FREQUENCY dials to 1000.00 Hz. Carefully adjust R43 on the error amplifier board for 1000.0 μs.
3. Set the FREQUENCY dials for R90.00 Hz and carefully adjust R119 for 11111.1 μs (90.00 Hz).
4. Set the FREQUENCY dials for R99.00 Hz and carefully adjust R20 for 10101.0 μs (99.00 Hz).
5. Repeat steps 3 and 4 until optimum setting is obtained.
6. Repeat steps 2 and 4 until optimum setting is obtained.
7. Now check the Closed Loop Frequency Adjustment and repeat if necessary.

VCG Input Sensitivity

1. Set the generator mode to REMOTE, FREQUENCY dials to R00.00 Hz, function to ⊓ and connect 50Ω OUTPUT to a frequency or period counter.
2. Connect the precision dc voltage source to the VCG IN connector on the rear panel with +5.000 Vdc applied.
3. Adjust R69 on VCG board for 1000.0 μs or 1000.0 Hz.

Remote Check

Check all remote functions in the same way front panel controls are checked.

TROUBLESHOOTING

Basic Techniques

Troubleshooting the Model 157 requires no special techniques. The following are a few reminders of basic electronics fault isolation.

1. Check control settings carefully: many times an incorrect control setting, or a knob that has loosened on its shaft, will cause a false indication of a malfunction.
2. Check associated equipment connections: make sure that all connections are properly connected to the correct connector.
3. Perform the checkout procedures: many out-of-specification indications can be corrected by recalibrating the instrument.
4. Visually check the interior of the instrument. Look for such indications as broken wires, charred components, loose leads, etc.

Troubleshooting Chart

Table 4-2 provides a list of some of the more probable symptoms of malfunctions and their remedies. Locate the indicated symptom listed in Column 1 of the chart and follow the corresponding procedures. When a fault has been localized to a specific stage by checking the parameters given for the major test points, check the dc operating voltages at solid-state device junction/pins. Check associated passive elements with an ohmmeter (power off) before replacing a suspected semiconductor element.

Table 4-2. TROUBLESHOOTING CHART

Symptom	Probable Cause	Board
Automatic calibration is less accurate than open loop.	Damaged crystal or logic failure in crystal one shot.	Crystal One Shot
	Damaged error amplifier	Error Amplifier
Automatic calibration less accurate than open loop on some ranges only.	Logic failure in range-divider stages.	Range Matrix
Generator operates on some ranges only.	Damaged range matrix or relay.	Range Matrix
No ∿ or ∿ output on upper four ranges.	Failure in triangle-wave buffer stage.	VCG/Integrator
Poor time symmetry or open-loop accuracy.	Miscalibration or failure in VCG circuitry.	VCG/Integrator
Problems on lower six or upper four ranges only.	Failure in slow-fast relay stages.	VCG/Integrator
No output on lower six ranges only.	Failure in counter logic or clock buffer.	Counter
⊓⊔ output on lower six ranges only or distorted ∿ and ∿ on lower six ranges only.	Failure in waveform DAC.	Counter
No output.	Failure in power supply .	Power Supply
No ∿ output.	Defective function relay stage.	Function
No ∿ output.	Defective function relay stage.	Function
No ⊓⊔ output.	Defective function relay stage.	Function
Asymmetrical ⊓⊔ amplitude.	Square-wave clamp faulty.	Function
Distorted or no ∿ signal to sine function relay.	Failure in sine converter.	Function
∿ and ∿ produce 0 Vdc output.	Trigger circuit failure.	VCG/Integrator
Positive or negative half cycles of output waveform only.	Output amplifier at fault.	Output Amplifier
Incorrect output waveform amplitude.	Failure in output attenuator.	Output amplifier
Output amplitude incorrect by one or more decades.	Failure in final attenuator.	Attenuator

REPLACEMENT OF CIRCUIT BOARDS

Refer to the Chassis Assembly illustration in Section 5 as a visual aid to the following procedural instructions.

Replacing Plug-In Boards

Plug-in boards in the Model 157 are color-coded from front to rear relative to the last digit of the part number:

PN Digit	Color	Board
1	Brown	Extender
2	Red	Output Amplifier
3	Orange	Function
4	Yellow	VCG/Integrator
5	Green	Range Matrix
6	Blue	Error Amplifier
7	Violet	One-shot
8	Gray	Power Supply
9	White	Counter

To remove any of these boards, proceed as follows:

1. Remove the three machine screws holding the top cover at the rear of the unit and slide the top cover out of the front retaining lip.
2. Remove the four machine screws holding the calibration shield over the plug-in boards and remove the shield.
3. Pull straight up on the plug-in board that you wish to remove.
4. If the board is to be checked operationally with the unit, remove the extender board and put it in the position of the board to be checked. Then plug this board into the top connectors of the extender board.

Replacing Left Switch Board

To replace the Left Switch Board, proceed as follows:

1. Remove top cover.
2. Remove the two machine screws retaining the POWER pushbutton switch to the subpanel.
3. Remove the one machine screw holding this switch to the Left Switch Board.
4. Disconnect the 18 Amphenol pin connections.
5. Remove the four machine screws that secure the Left Switch Board to the subpanel and lift out the Left Switch Board.
6. Replacement is essentially the reverse of removal.

Replacing Right Switch Board

To replace the Right Switch Board, proceed as follows:

1. Remove top cover.
2. Disconnect the 11 Amphenol pin connections.
3. Remove the four machine screws that secure the Right Switch Board to the subpanel and lift out the Right Switch Board.
4. Replacement is essentially the reverse of removal.

Replacing Attenuator Board

To replace the Attenuator Board, proceed as follows:

1. Remove top cover.
2. Remove all plug-in boards.
3. Unsolder the BNC connections (three leads), the two coaxial connections (four leads), and the five single-conductor leads at the bottom of the board.
4. Remove the four machine screws retaining the Attenuator Board to the rear panel and remove the board.
5. Replacement is essentially the reverse of removal.

REPLACEMENT OF ROTARY SWITCHES, DIALS, OR DECIMAL LAMPS

The decimal lamps in the Model 157 are operated at one-half of the manufacturer's voltage rating. The rated MTBF for this lamp, continuously operated in this mode, is 20 million hours which precludes normal lamp failure during the life of the instrument. However, if a lamp or rotary switch item is inadvertently damaged, proceed as follows to replace the defective item:

1. Remove top and bottom covers.
2. Spring out bail from bottom of chassis.
3. Remove all front-panel knobs by loosening their set screws.
4. Remove the three hex nuts, lockwashers, and flat washers that hold the Mother Board to each side rail.
5. Remove the six machine screws from each side rail.
6. Disconnect the eight Amphenol pin connectors at the front edges of the Mother Board.
7. Pull off the side rails, starting at the top edge.
8. Remove the two No. 6 machine screws on each handle.
9. Loosen all machine screws on the top and bottom of the handles.
10. Pull the extrusion off, being careful to guide the Amphenol pin leads through their slots at the bottom left and right ends of the subpanel.
11. Lamps can now be removed by unsoldering two leads and pulling the lamp out from the rear.
12. Rotary switches can now be removed since access for unsoldering leads from top (Left or Right Switch Board removed, as required) and bottom of switch wafers is now available. Access to the switch-shaft locking nut is obtained by pulling off the switch dial.
13. Replacement is essentially the reverse of removal.

SECTION 5
DATA PACKAGES

INTRODUCTION

This section contains data packages for the Model 157 Programmable Waveform Synthesizer. Each data package is a quick-access document that contains maintenance data arranged for the user's convenience in simultaneously viewing the schematic diagram and supporting data. Each data package includes a part location illustration, replaceable parts list, voltage, waveform and logic data and a schematic diagram. The voltage, waveform and logic data are numerically coded to correspond with numbers on the schematic diagram and part location illustration. Also included in this section are block diagrams, logic diagrams and mechanical drawings that are helpful in understanding and maintaining the equipment.

ORDERING PARTS

When ordering spare parts, please specify part number, circuit reference, board, serial number of unit and, if applicable, the function performed.

ARRANGEMENT

Figures 5-1 through 5-8 of this section locate test points and internal adjustments used during calibration and test procedures.

Drawings and data packages are arranged in this section in the following order:

CROSS REFERENCE FOR DRAWING NUMBERS

Drawings	Old Number	New Number
Outline	157-603	
Block Diagram	157-600	
Counter Logic Diagram	157-602	
Chassis Assembly	157-000	0102-00-0302
Chassis Schematic	157-200	0004-00-0043
Chassis Parts List	*	1101-00-0043
Mother Board Assembly	157-010	0101-00-0092
Mother Board Parts List	*	1100-00-0092
Extender Board Assembly	157-011	0101-00-0093
Extender Board Parts List	*	1100-00-0093
Output Amplifier Assembly	157-012	0101-00-0094
Output Amplifier Schematic	157-212	0103-00-0094
Output Amplifier Parts List	*	1100-00-0094
Function Board Assembly	157-028	0101-00-0104
Function Board Schematic	157-228	0103-00-0104
Function Board Parts List	*	1100-00-0104
VCG Board Assembly	157-014	0101-00-0095
VCG Board Schematic	157-214	0103-00-0095
VCG Board Parts List	*	1100-00-0095

Drawings	Old Number	New Number
Range Matrix Assembly	157-015	0101-00-0096
Range Matrix Schematic	157-215	0103-00-0096
Range Matrix Parts List	*	1100-00-0096
Error Amplifier Assembly	157-016	0101-00-0097
Error Amplifier Schematic	157-216	0103-00-0097
Error Amplifier Parts List	*	1100-00-0097
Crystal One Shot Assembly	157-017	0101-00-0098
Crystal One Shot Schematic	157-217	0103-00-0098
Crystal One Shot Parts List	*	1100-00-0098
Power Supply Assembly	157-018	0101-00-0099
Power Supply Schematic	157-218	0103-00-0099
Power Supply Parts List	*	1100-00-0099
Counter Board Assembly	157-019	0101-00-0100
Counter Board Schematic	157-219	0103-00-0100
Counter Board Parts List	*	1100-00-0100
Left Switch Board Assembly	157-020	0101-00-0101
Left Switch Parts List	*	1100-00-0101
Right Switch Board Assembly	157-021	0101-00-0102
Right Switch Board Parts List	*	1100-00-0102
Attenuator Board Assembly	157-024	0101-00-0103
Attenuator Board Parts List	*	1100-00-0103

*Same as Assembly Number

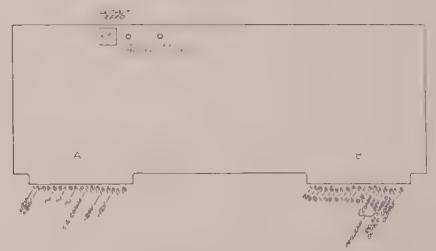


Figure 5-1. Output Amplifier

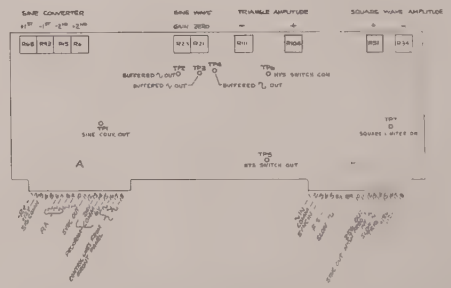


Figure 5-2. Function Board

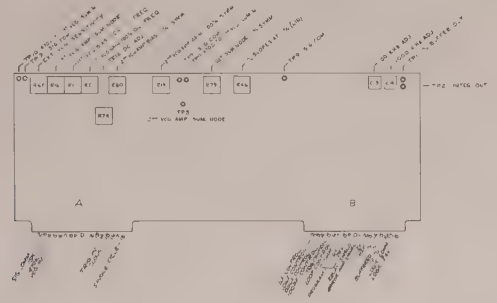


Figure 5-3. VCG/Integrator

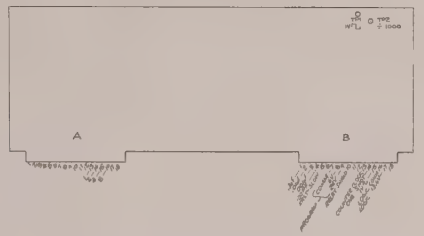


Figure 5-4. Range Matrix

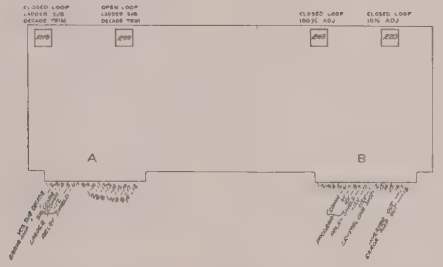


Figure 5-5. Error Amplifier

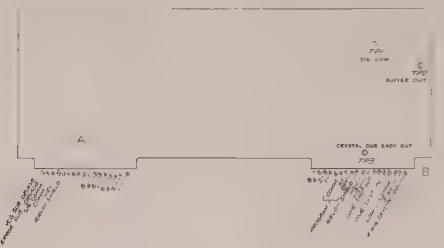


Figure 5-6. Crystal One Shot

Figure 5-7. Power Supply

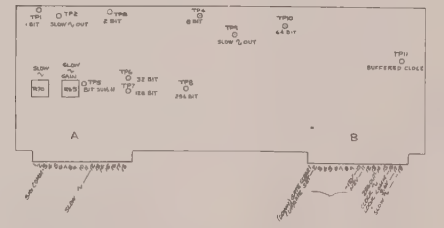
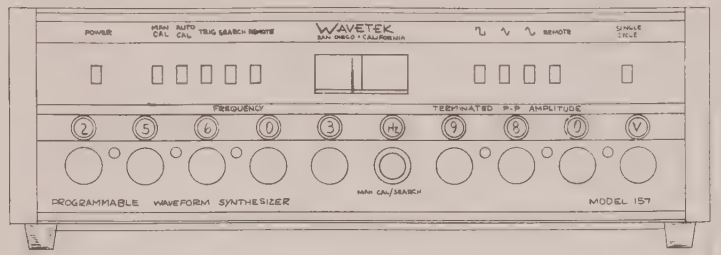
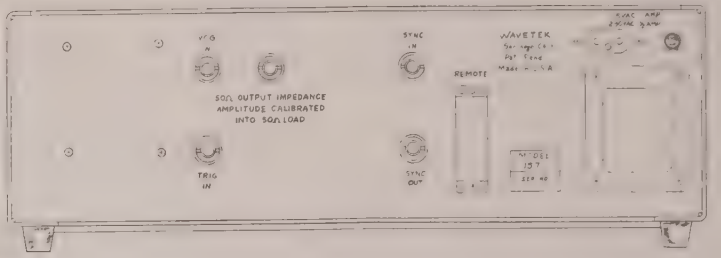
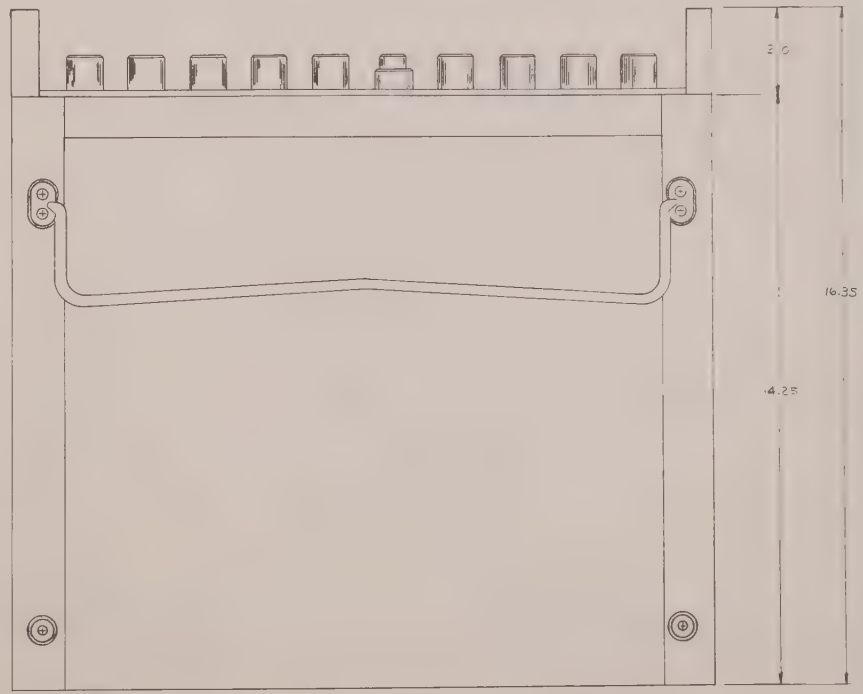


Figure 5.8. Counter Board







rev	ecn	by	date	app.
B	ECN 1233	RL	4/1/89	
A	Re-draw Rear Panel	PGS	12-13-88	

tolerance unless otherwise specified
XXX ± .010
XX ± .030

WAVETEK san diego, calif.

scale 3/4 | by BECHLAND | date 11/19/88 | app.

material: N/A

title: OUTLINE DRAWING

finish: N/A

model no: 157 | dwg no: 0157-603 | rev: B

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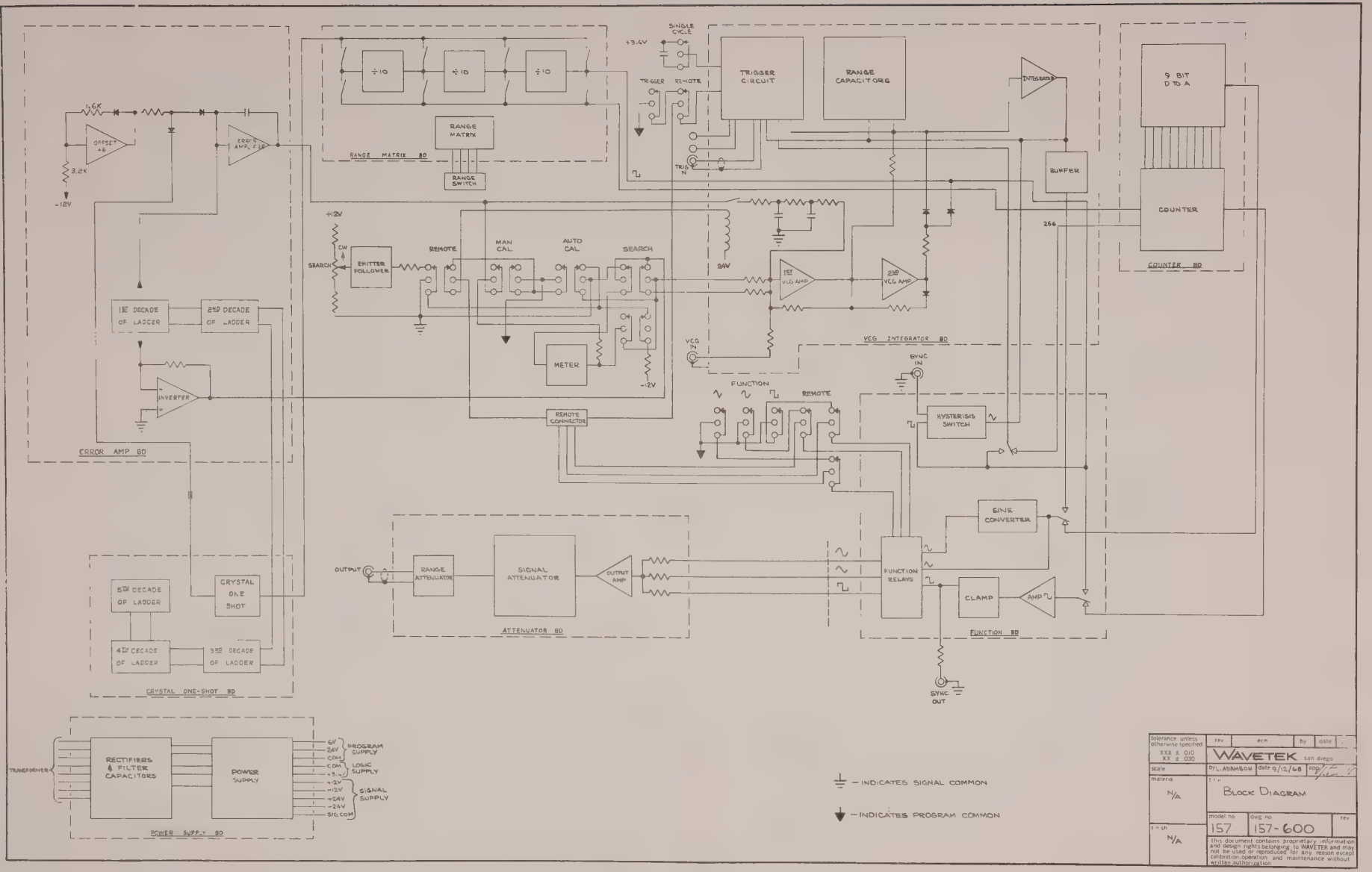

tolerance unless otherwise specified XXX ± 010 XX ± 030	rev.	ecn	by	date
	WAVETEK san diego			
scale	by L. ADAMSON	date 9/12/68	app.	
materia N/A	title BLOCK DIAGRAM			
finish N/A	model no 157	dwg no 157-600	rev	

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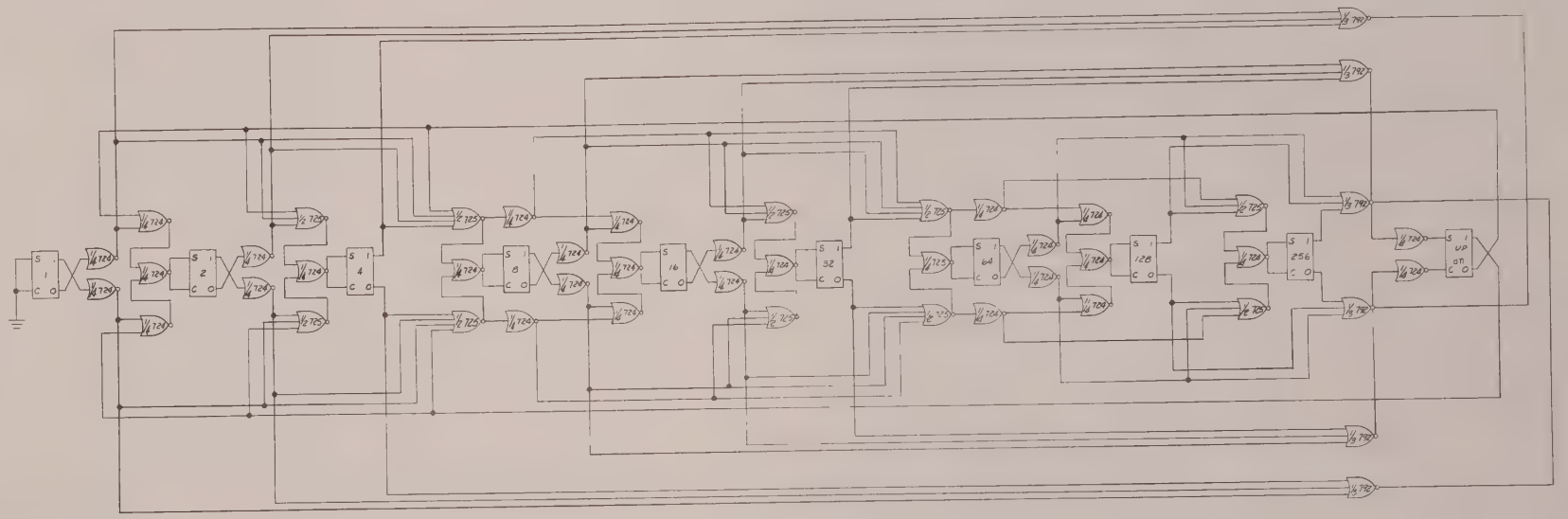

tolerance unless otherwise specified .XXX ± .010 XX ± .030	rev	ecn	by	date	app
scale N/A	WAVETEK san diego, calif.				
material N/A	drawn E. HOLLAND date 10/20/68 approved				
finish N/A	title COUNTER LOGIC DIAGRAM				
	model no. 157	dwg no. 157-602			rev

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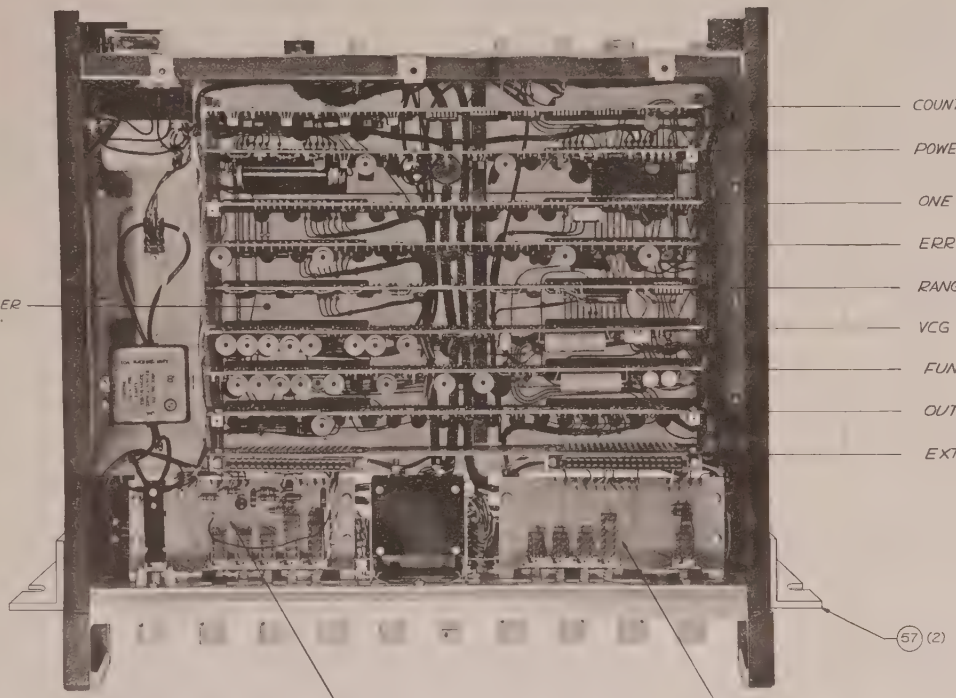

R	ECN 2115		DC	3048D	
Q	# 1575	1614	S	1570	
P	ECN 1527		RO	3277	
N	DOC # 1460		DC	9176	
M	ECN 1233		RO	9/15/70	
L	ECN 809		BA	8-31-70	
K	ECN 748		BA	8-21-70	
J	ECN 811		BA	8-21-70	
H	ECN 757		UD	7-10-70	
G	ECN 680		BA	5/6/70	
F	ECN 562		BA	3/1/70	
E	ECN 440		BA	1/9/70	
D	ECN 411		S	11/17/69	
C	295		NG	10/30/69	
B	289		NG	10/15/69	
A	285		BA	10/8/69	
rev	ecn		by	date	app

tolerance unless otherwise specified
XX ± 010
XX ± 030

WAVETEK san diego calif

scale N/A

by BOCHICHIO date 7-9-69 app

material N/A

finish N/A

title CHASSIS ASSY (TOP)

model no 157

dwg no SHEET 1 OF 7 0102-00-0302

rev R

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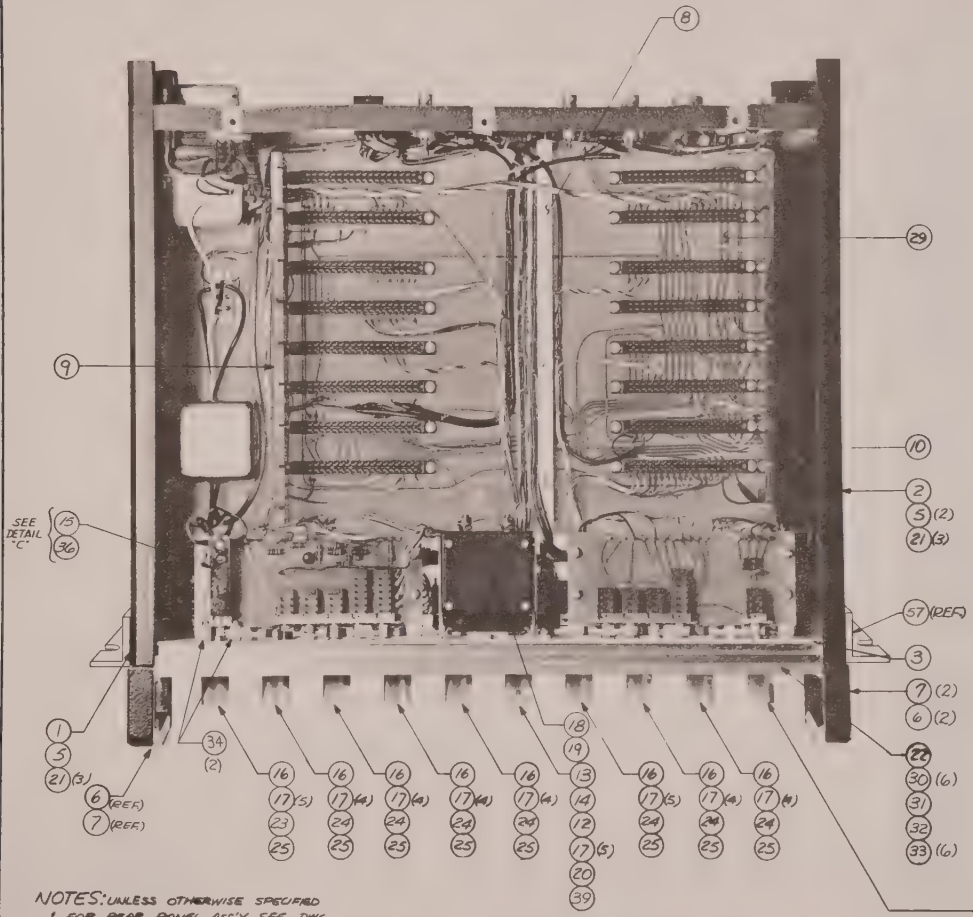
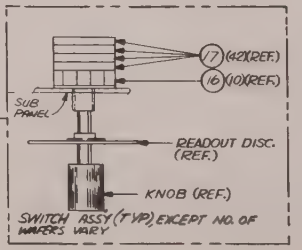
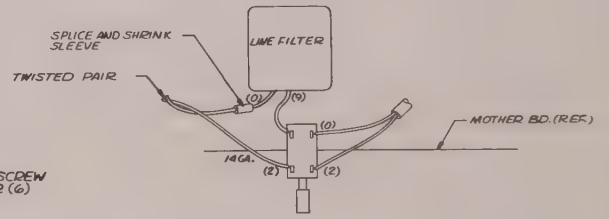


NOTES: UNLESS OTHERWISE SPECIFIED
1 FOR REAR PANEL ASS'Y SEE DWG 0102-00-0302 SHT #3



SWITCH ASSY (TYP), EXCEPT NO. OF WAFERS VARY



DETAIL "C"
POWER SWITCH WIRING

SEE SHEET 1

N	DOC #1460	DC	9/74	
M	ECN 1233	EA	4/74	
L	ECN 709	BA	3/73	
K	ECN 778	BA	2/73	
J	ECN 811	BA	1/73	
H	ECN 757	VP	10/72	
G	ECN 680	BA	9/72	
F	ECN 562	TM	7/72	S
rev	ecn	by	date	app

tolerance unless otherwise specified: XXX ± .010, XX ± .030
scale N/A
material N/A
finish N/A

WAVETEK san diego, calif
by BOCHICHIO date 7-8-69 appd

title: CHASSIS ASS'Y
model no. 157
dwg no. SIM # 379 0102-00-0302
rev 2

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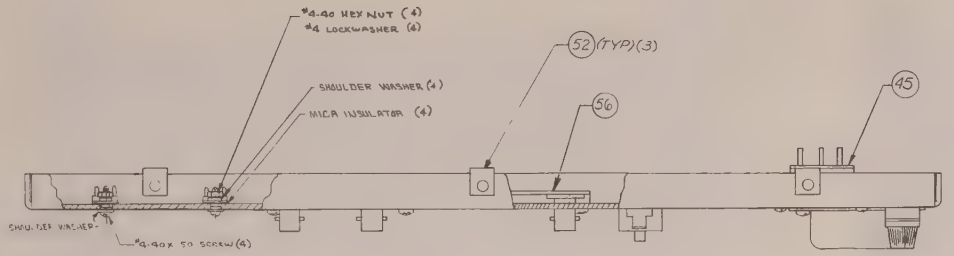
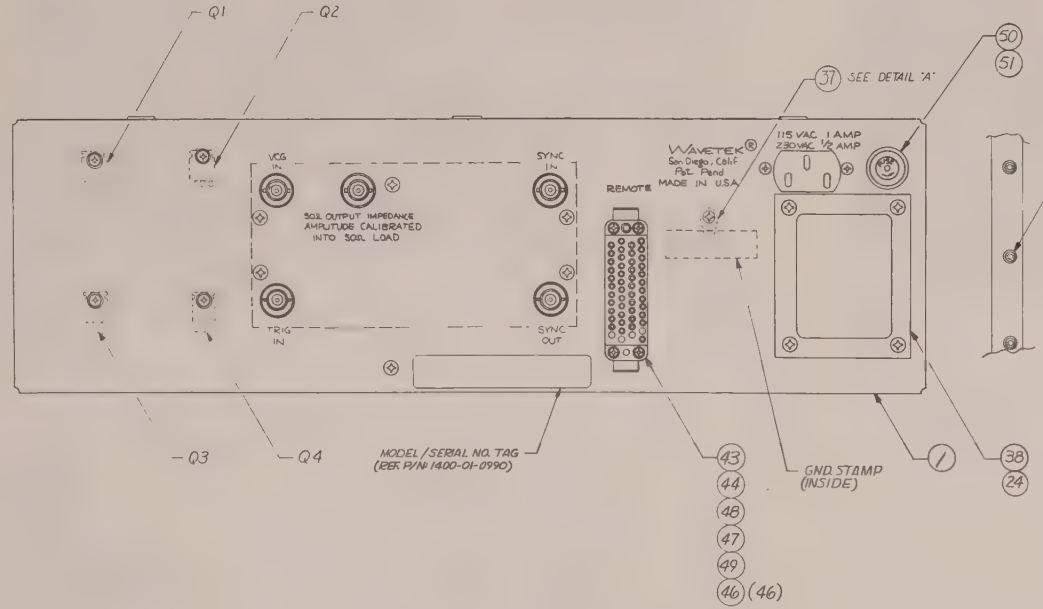
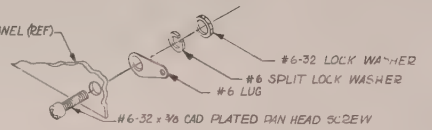




"ALL CONNECTORS TO BE MECHANICALLY SECURE TO CHASSIS PRIOR TO SOLDERING GROUND WIRES TO LUGS"



DETAIL "A"
GROUND LUG HARDWARE STACKUP

1. ALL TIPS MOUNTED USING (2) TIP SHOULDER WASHERS, AND MICA INSULATOR.

SEE SHEET 1

rev	ecn	by	date	app.
N	DCN 1460	QC	8/14	
M	ECN 1433	RD	7/1/	
L	ECN 901	BA	5.	
K	ECN 798	BA	12-4	
J	ECN 311	BA	9/1	
H	ECN 757	[illegible]	[illegible]	
G	ECN 680	[illegible]	5/15/	
F	ECN 562	[illegible]	11/13	3

tolerance unless otherwise specified: XXX ± .010, XX ± .030
scale: FULL
material: N/A
finish: N/A

WAVETEK san diego, calif

by: BOCHEHIO date: 6-26-69 app:

title: ASS'Y REAR PANEL

model no. 157 | dwg no. SHT 3 OF 7 | rev .R

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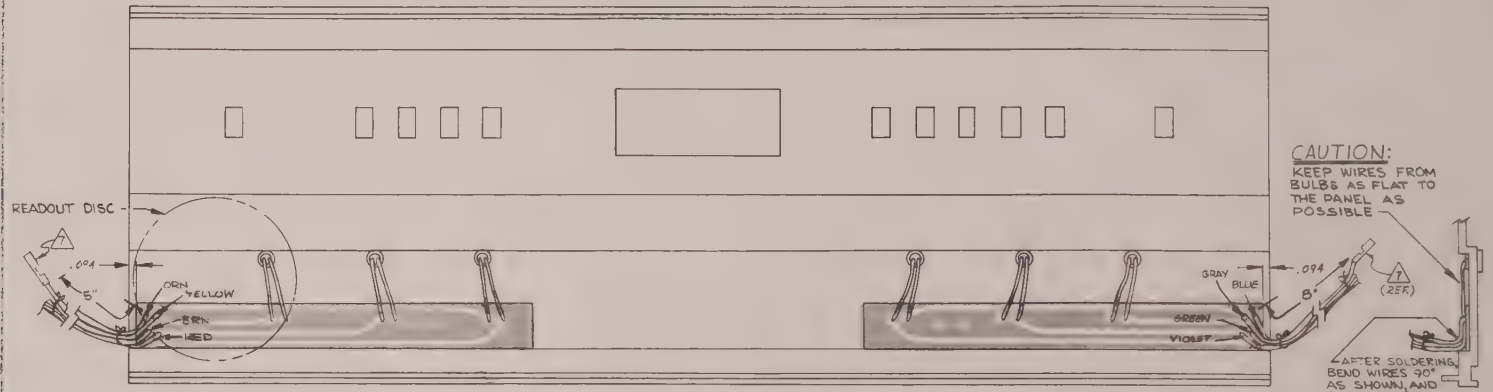
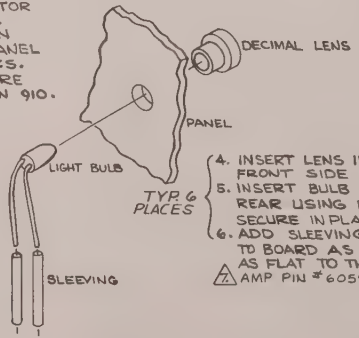


REAR VIEW OF FRONT PANEL (1400-00-2959)

1. SECURE BOARDS WHERE SHOWN USING EASTMAN 910
2. MAKE UP, & SOLDER IN PLACE, CONNECTOR HARNESSES AS SHOWN, USING AMP PINS. ANGLE WIRES TOWARD CORNER AS SHOWN THEN BEND PERPENDICULAR TO THE PANEL TO AVOID CATCHING ON READOUT DISCS.
3. AFTER SOLDERING & BENDING, SECURE THE WIRES TO THE BOARDS USING EASTMAN 910.



4. INSERT LENS INTO PANEL FROM THE FRONT SIDE (PRESS FIT).
5. INSERT BULB INTO LENS FROM THE REAR USING RUBBER CEMENT TO SECURE IN PLACE.
6. ADD SLEEVING TO LEADS AND SOLDER TO BOARD AS SHOWN KEEPING THE LEADS AS FLAT TO THE PANEL AS POSSIBLE.

7 AMP PIN # 60598-8 ON END OF EACH WIRE (8 PLACES)

	A	ECN 895	NG	9/68

WAVETEK San Diego, Calif.

NONE	L. ADAMSON	9/11/68
NOTED	CHASSIS ASSY (FRONT PANEL)	
N/A	157	0102-00-0302 R

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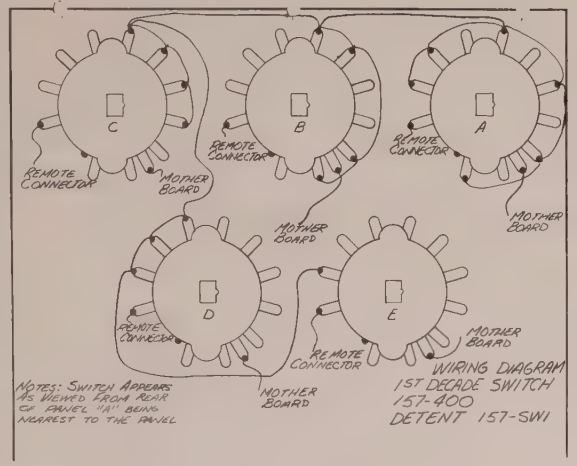
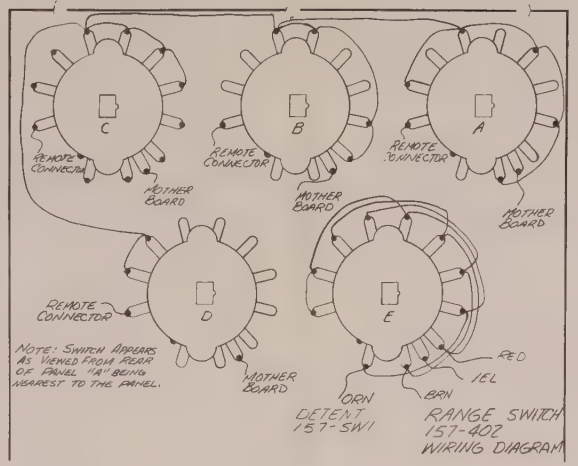
REV	ECN	BY	DATE	APP

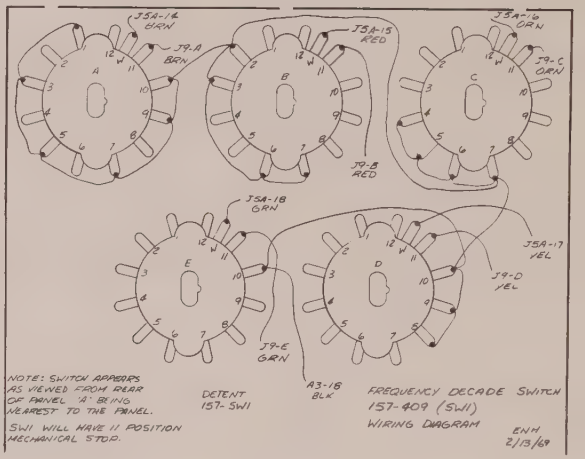


WIRING DIAGRAM
1ST DECADE SWITCH
157-400
DETENT 157-SW1



DETENT
157-SW1

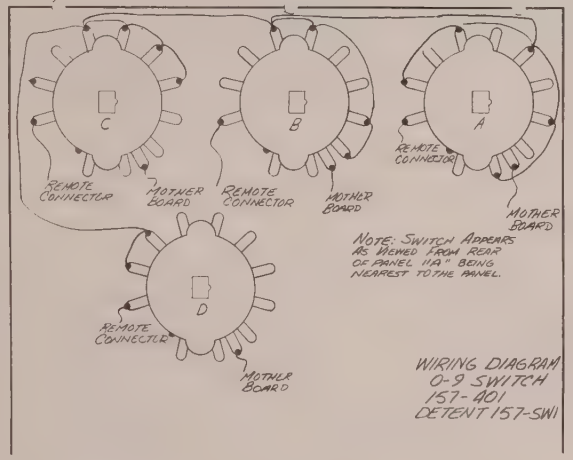
RANGE SWITCH
157-402
WIRING DIAGRAM



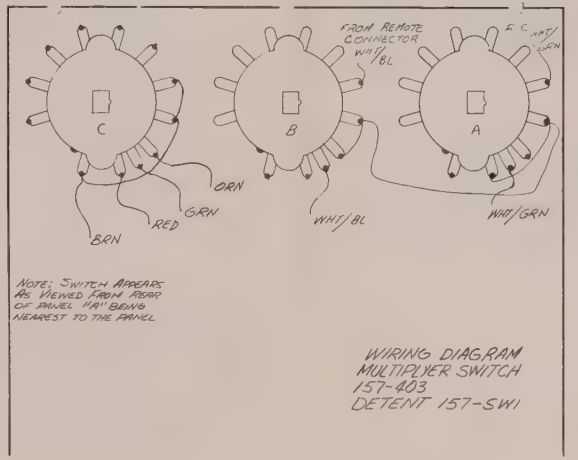
DETENT
157-SW1

FREQUENCY DECADE SWITCH
157-409 (SW1)
WIRING DIAGRAM

ENH
2/13/69



WIRING DIAGRAM
0-9 SWITCH
157-401
DETENT 157-SW1



WIRING DIAGRAM
MULTIPLYER SWITCH
157-403
DETENT 157-SW1

NOTE UNLESS OTHERWISE SPECIFIED

REMOVE ALL BURRS AND BREAK SHARP EDGES	DRAWN J. COOPER	DATE 1-11-71	WAVETEK SAN DIEGO • CALIFORNIA
MATERIAL	PROJ ENGR		TITLE CHASSIS ASSY (DETENT)
FINISH WAVETEK PROCESS	RELEASE APPROV		
	TOLERANCE UNLESS OTHERWISE SPECIFIED XXX ± .010 ANGLES ±1° XX ± .030		
	DO NOT SCALE DWG		MODEL NO 157 DWG NO 0102-00-0302 REV R
	SCALE		CODE IDENT 23338 SHEET 5 OF 7

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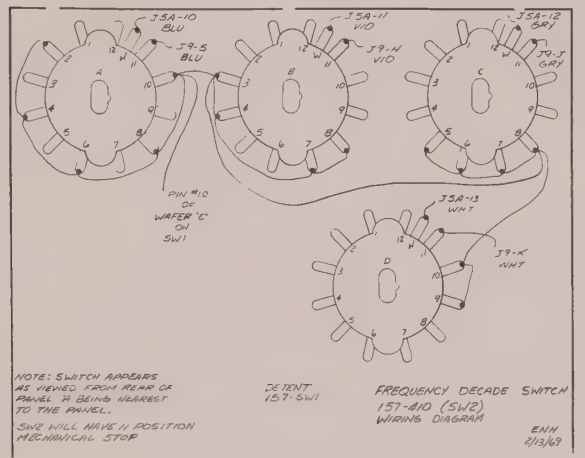
REV	ECN	BY	DATE	APP

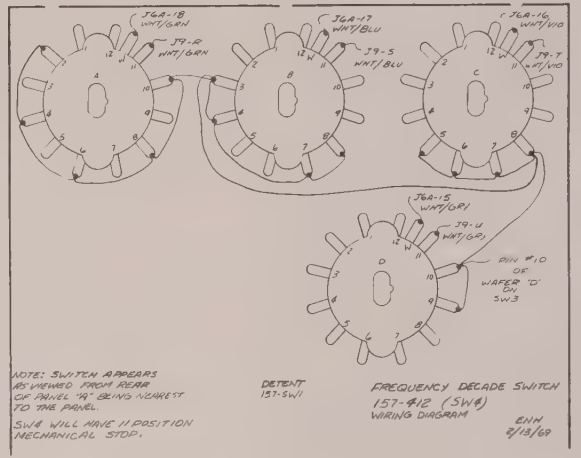


NOTE: SWITCH APPEARS AS VIEWED FROM REAR OF PANEL 'A' BEING NEAREST TO THE PANEL.
SW2 WILL HAVE 11 POSITION MECHANICAL STOP

DETENT
157-SW1

FREQUENCY DECADE SWITCH
157-410 (SW2)
WIRING DIAGRAM

ENH
2/13/69

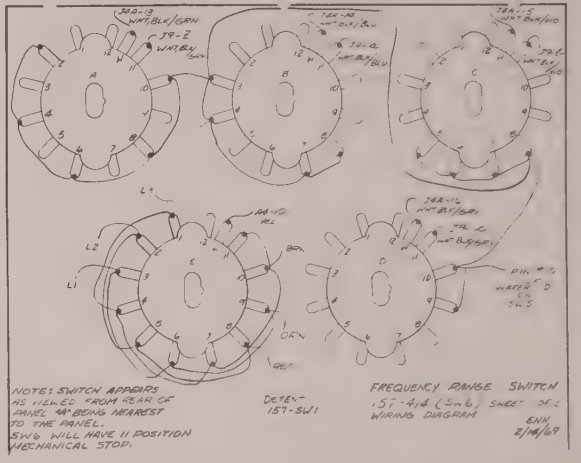


NOTE: SWITCH APPEARS AS VIEWED FROM REAR OF PANEL 'A' BEING NEAREST TO THE PANEL.
SW4 WILL HAVE 11 POSITION MECHANICAL STOP.

DETENT
157-SW1

FREQUENCY DECADE SWITCH
157-412 (SW4)
WIRING DIAGRAM

ENH
2/13/69

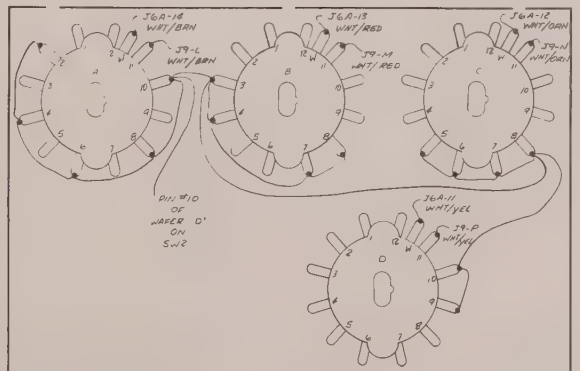


NOTE: SWITCH APPEARS AS VIEWED FROM REAR OF PANEL 'A' BEING NEAREST TO THE PANEL.
SW6 WILL HAVE 11 POSITION MECHANICAL STOP.

DETENT
157-SW1

FREQUENCY RANGE SWITCH
157-414 (SW6) SHEET 1 OF 2
WIRING DIAGRAM

ENH
2/14/69

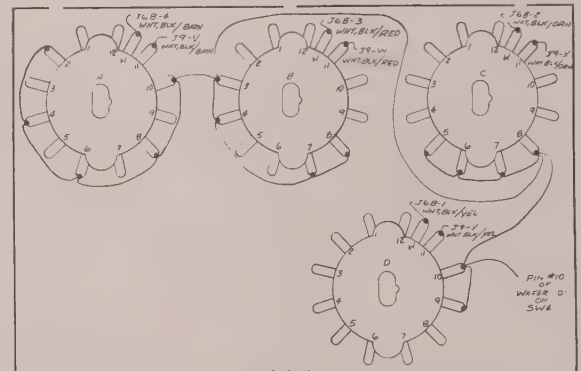


NOTE: SWITCH APPEARS AS VIEWED FROM REAR OF PANEL 'A' BEING NEAREST TO THE PANEL.
SW3 WILL HAVE 11 POSITION MECHANICAL STOP.

DETENT
157-SW1

FREQUENCY DECADE SWITCH
157-411 (SW3)
WIRING DIAGRAM

ENH
2/13/69

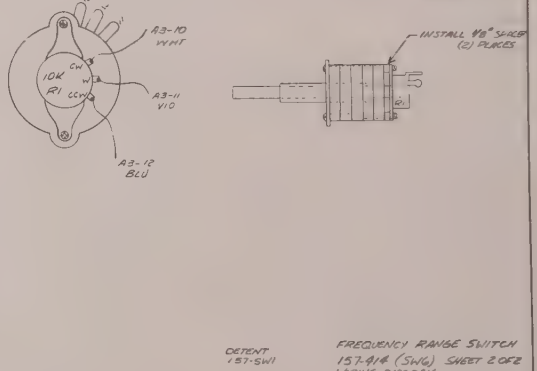


NOTE: SWITCH APPEARS AS VIEWED FROM REAR OF PANEL 'A' BEING NEAREST TO THE PANEL.
SW5 WILL HAVE 11 POSITION MECHANICAL STOP.

DETENT
157-SW1

FREQUENCY DECADE SWITCH
157-413 (SW5)
WIRING DIAGRAM

ENH
2/13/69



DETENT
157-SW1

FREQUENCY RANGE SWITCH
157-414 (SW6) SHEET 2 OF 2
WIRING DIAGRAM

ENH
2/14/69

NOTE: UNLESS OTHERWISE SPECIFIED

REMOVE ALL BURRS AND BREAK SHARP EDGES	DRAWN D. COOPER	DATE 1/1177	WAVETEK SAN DIEGO, CALIFORNIA
MATERIAL	PROJ ENGR		TITLE: CHASSIS ASSY (DETENT)
	RELEASE APPROV		
FINISH WAVETEK PROCESS	TOLERANCE UNLESS OTHERWISE SPECIFIED XXX ± .010 ANGLES ± 1° .XX ± .030		
	DO NOT SCALE DWG		MODEL NO 157 / DWG NO 002-00-0302 / REV R
	SCALE	CODE IDENT 23338	SHEET 6 OF 7

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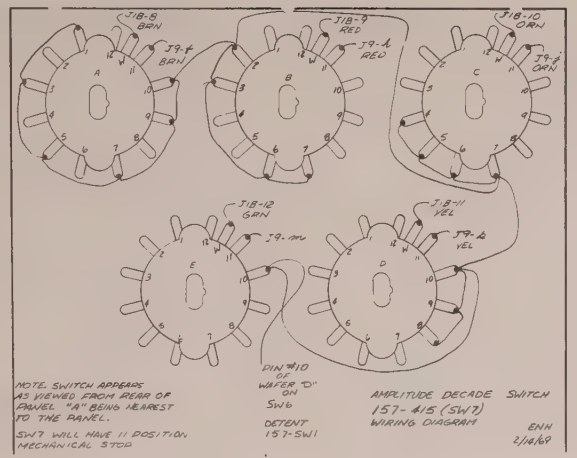
REV	ECN	BY	DATE	APP



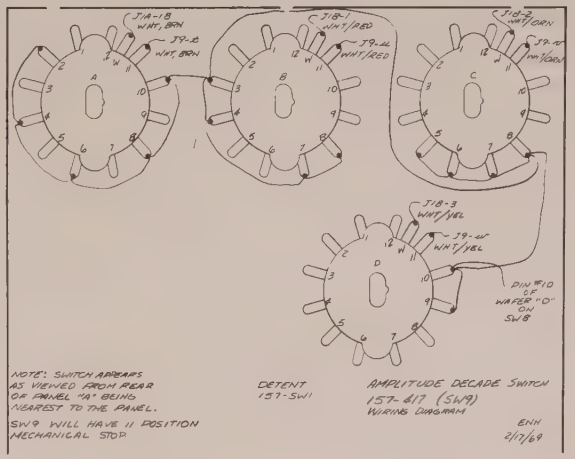
NOTE: SWITCH APPEARS AS VIEWED FROM REAR OF PANEL "A" BEING NEAREST TO THE PANEL.

SW7 WILL HAVE 11 POSITION MECHANICAL STOP

DETENT 157-SW11

AMPLITUDE DECADE SWITCH
157-815 (SW7)
WIRING DIAGRAM

ENH
2/18/69



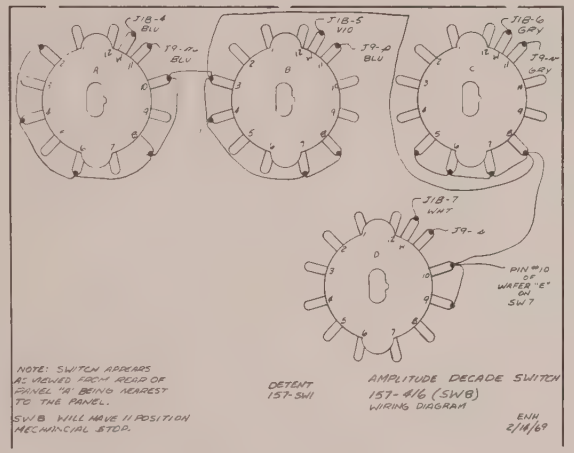
NOTE: SWITCH APPEARS AS VIEWED FROM REAR OF PANEL "A" BEING NEAREST TO THE PANEL.

SW9 WILL HAVE 11 POSITION MECHANICAL STOP.

DETENT 157-SW11

AMPLITUDE DECADE SWITCH
157-817 (SW9)
WIRING DIAGRAM

ENH
2/17/69



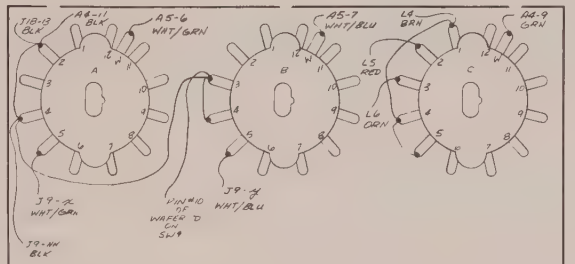
NOTE: SWITCH APPEARS AS VIEWED FROM REAR OF PANEL "A" BEING NEAREST TO THE PANEL.

SW8 WILL HAVE 11 POSITION MECHANICAL STOP.

DETENT 157-SW11

AMPLITUDE DECADE SWITCH
157-816 (SW8)
WIRING DIAGRAM

ENH
2/18/69



NOTE: SWITCH APPEARS AS VIEWED FROM REAR OF PANEL "A" BEING NEAREST TO THE PANEL

SW10 WILL HAVE 5 POSITION MECHANICAL STOP.

AMPLITUDE RANGE SWITCH
157-818 (SW10)
WIRING DIAGRAM

ENH
2/17/69

NOTE UNLESS OTHERWISE SPECIFIED

OVE ALL BURRS BREAK SHARP EDGES	DRAWN D. COOPER	DATE 1/17/77	WAVETEK SAN DIEGO, CALIFORNIA
RIAL	PROJ/ENG'R		TITLE CHASSIS ASSY (DETENT)
	RELEASE APPROV		
FINISH WAVETEK PROCESS	TOLERANCE UNLESS OTHERWISE SPECIFIED XXX ± 010 XX ± 030 ANGLES 1		
	DO NOT SCALE DWG		MODEL NO 157 / DWG NO 0102-00-0302 / REV R
	SCALE		SCDH IDENT 23338 / SHEET 7 OF 7

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REV	ECN	BY	DATE	APP

REFERENCE DESIGNATORS	PART DESCRIPTION	ORIG-MFGR-PART-NO	MFGR	WAVETEK NO.	QTY/PT
NONE	SCHEMATIC,INSTRUMENT	0004-00-0043	WVTK	0004-00-0043	1
NONE	ASSYDRWG,STD CHASSIS	0102-00-0302	WVTK	0102-00-0302	1
38	TRANSFORMER	157-500	WVTK	1204-00-0480	1
36	STANDOFF	013-003-2	WVTK	1400-00-2503	2
22	PANEL,FT FROM:1400-00-2952	157-500B	WVTK	1400-00-2959	1
6	PANEL,REAR	157-301G	WVTK	1400-00-2980	1
5	PANEL,SUB	157-302E	WVTK	1400-00-2993	1
11	BRACE	157-304	WVTK	1400-00-3003	4
9	GUIDE,BD,LEFT	157-312A-1	WVTK	1400-00-3010	1
10	GUIDE,BD,RIGHT	157-312A-2	WVTK	1400-00-3020	1
8	BAR,SUPT	157-313	WVTK	1400-00-3033	1
14	KNOB,SMALL,BLACK FROM:1400-00-3061	157-316BLACK	WVTK	1400-00-3079	1
13	KNOB,CO-AX,BLACK,ANO FROM:1400-00-3091	157-317B	WVTK	1400-00-3109	1
5	SPACER,BD	157-324A	WVTK	1400-00-3123	2
57	EAR,R/M,BLACK FROM:1400-00-3131	157-328	WVTK	1400-00-3149	2
23	DISCS,READOUT	157-306	WVTK	1400-00-3179	2

WAVETEK PARTS LIST | TITLE: STD CHASSIS | ASSEMBLY NO: 1101-00-0043 | PAGE: 1 | REV: U

REFERENCE DESIGNATORS	PART DESCRIPTION	ORIG-MFGR-PART-NO	MFGR	WAVETEK NO.	QTY/PT
4	BAIL, HANDLE	1400-00-8353	WVTK	1400-00-8353	2
NONE	I.D. LABEL	1400-00-9100	WVTK	1400-00-9100	1
NONE	ID LABEL SPECIAL INSTRUMENT	1400-01-0990	WVTK	1400-01-0990	1
31	L LIGHT	157-122	WVTK	1700-00-0189	1
32	R LIGHT	157-123	WVTK	1700-00-0190	1
83	CONN	280277-2	AMP	2100-02-0023	1
84	CONN KIT	7-282863-9	AMP	2100-02-0083	1
85	RECEPT,POWER	EAC-301	SMCFT	2100-03-0005	1
37	BOLDER LUG	1485-6	SMITH	2100-04-0025	2
66	PIN,FEMALE,CONN	42985-4	AMP	2100-05-0013	46
67	CONN	200380-4	AMP	2100-07-0004	1
68	CONN	200390-4	AMP	2100-07-0005	1
69	LOCK SPRING	201926-1	AMP	2100-07-0006	2
33	LAMP	20M820A67-AB15	ILPRD	2400-02-0007	6
50	FUSE,250V,1A,FB	312001	LITFU	2400-05-0007	1
51	FUSE HOLD	031.1653/031.1686	SCHUR	2400-05-0012	1
19	METER	50-1A5112-MEM	GE	2400-06-0001	1

WAVETEK PARTS LIST | TITLE: STD CHASSIS | ASSEMBLY NO: 1101-00-0043 | PAGE: 3 | REV: U

REFERENCE DESIGNATORS	PART DESCRIPTION	ORIG-MFGR-PART-NO	MFGR	WAVETEK NO.	QTY/PT
55	PWR CORD	0-7788-008-GY	PACRD	6001-80-0005	1

WAVETEK PARTS LIST | TITLE: STD CHASSIS | ASSEMBLY NO: 1101-00-0043 | PAGE: 5 | REV: U

REFERENCE DESIGNATORS	PART DESCRIPTION	ORIG-MFGR-PART-NO	MFGR	WAVETEK NO.	QTY/PT
	FROM:1400-00-3161				
28	DISCS,READOUT FROM:1400-00-3161	157-307	WVTK	1400-00-3189	6
26	DISCS,READOUT FROM:1400-00-3161	157-308	WVTK	1400-00-3199	1
39	DISCS,READOUT FROM:1400-00-3161	157-309	WVTK	1400-00-3209	1
80	SHIELD,TOP	157-341	WVTK	1400-00-3210	1
24	END BELL	157-500-EB	WVTK	1400-00-3224	1
25	KNOB,BLACK FROM:1400-00-3231	201-339-2	WVTK	1400-00-3249	9
1	RAIL,SIDE,LEFT,BLK FROM:1400-00-3311	210-341D	WVTK	1400-00-3329	1
2	RAIL,SIDE,RIGHT,BLK FROM:1400-00-3341	210-342D	WVTK	1400-00-3359	1
30	LENS,DECIMAL	210-356	WVTK	1400-00-3378	6
81	COVER,TOP,BLACK FROM:1400-00-3392	211-311A	WVTK	1400-00-3399	1
42	COVER,BOTTOM,BLACK FROM:1400-00-3411	211-312A	WVTK	1400-00-3429	1
7	HANDLE,BLACK FROM:1400-00-7921	1400-00-7929	WVTK	1400-00-7929	2

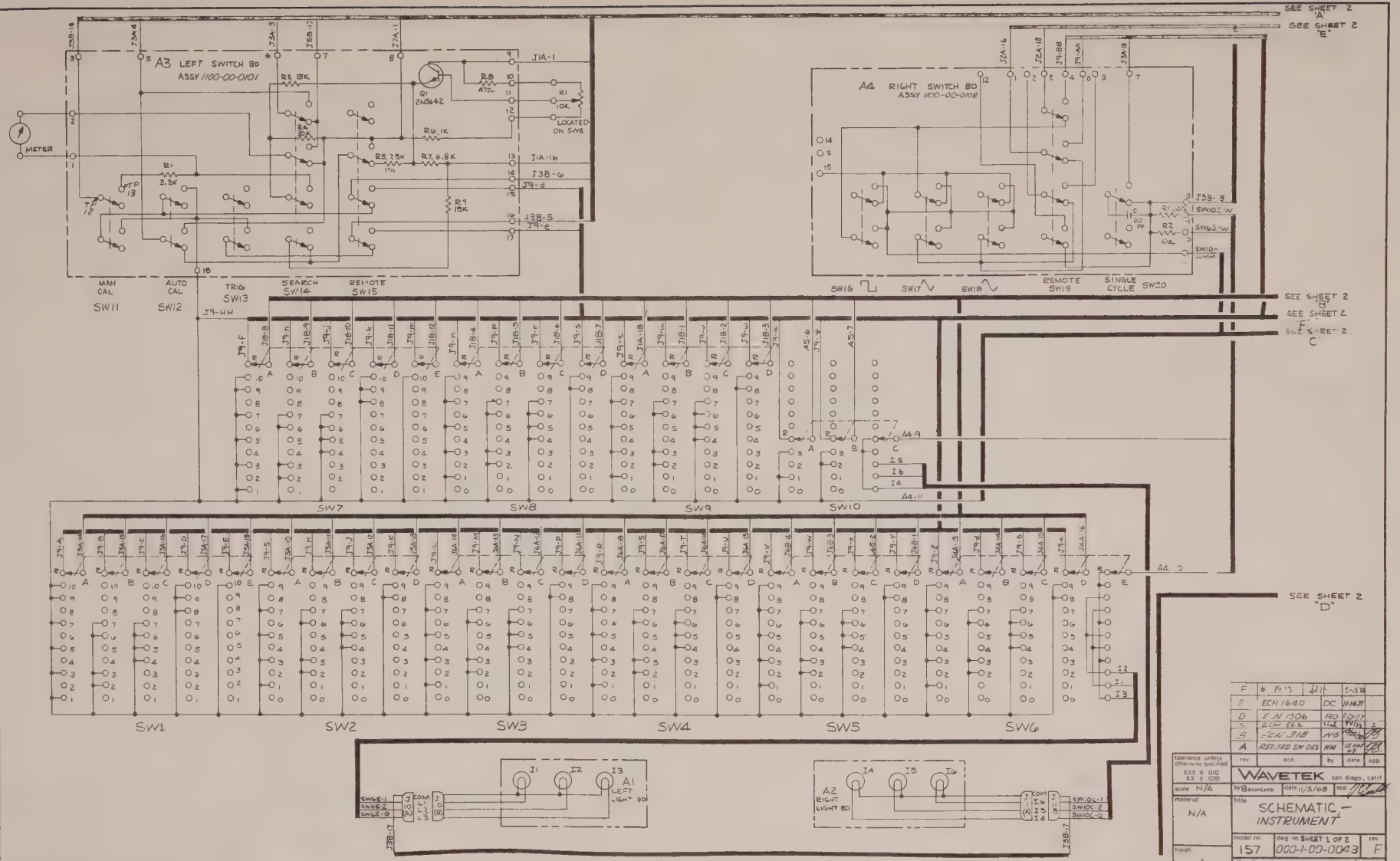
WAVETEK PARTS LIST | TITLE: STD CHASSIS | ASSEMBLY NO: 1101-00-0043 | PAGE: 2 | REV: U

REFERENCE DESIGNATORS	PART DESCRIPTION	ORIG-MFGR-PART-NO	MFGR	WAVETEK NO.	QTY/PT
38	SPACER .250H,.250 DIA .140 DIA THRU	8501	SMITH	2800-04-0003	2
35	BAIL W/FEET,15 1/2	BAIL 15 1/2	BUNEY	2800-08-0001	1
52	CLIP NUT	SL210-06-2	SHRLK	2800-09-0011	3
21	INSERT,BANC,LOC	T81D100-632	BOOTS	2800-09-0013	12
18	METER BKT,	4149K16778	GE	2800-12-0002	1
53	WASHER,SHOULDER	2660	SMITH	2800-26-0001	8
20	POT,CONT,10K FROM:4600-01-0305	4609-71-0315	WVTK	4609-71-0315	1
Q1	TRANS	TIP29	TI	4902-00-0290	1
Q3 Q6	TRANS	TIP30	TI	4902-00-0300	2
Q2	TRANS	TIP33	TI	4902-00-0330	1
15	SWITCH ASSY PB	157-822	WVTK	5103-00-0011	1
54	BUTTON	J-52305-RED	CRL	5103-04-0004	1
16	DETENT	157-3M1	WVTK	5104-01-0006	9
17	WAFER	T-105	CTS	5104-02-0001	42
12	DETENT MOD FROM:5104-01-0006	157-327	WVTK	5104-99-0019	1

WAVETEK PARTS LIST | TITLE: STD CHASSIS | ASSEMBLY NO: 1101-00-0043 | PAGE: 4 | REV: U

NOTE: UNLESS OTHERWISE SPECIFIED

REMOVE ALL BURRS AND BREAK SHARP EDGES	DRAWN	DATE	WAVETEK SAN DIEGO, CALIFORNIA
MATERIAL	PROJ ENGR		TITLE: CHASSIS
	RELEASE APPROV		
FINISH WAVETEK PROCESS	TOLERANCE UNLESS OTHERWISE SPECIFIED .XXX ±.010 ANGLES ±1° .XX ±.030		
	DO NOT SCALE DWG		MODEL NO: 157 DWG NO: 1101-00-0043 REV: U
	SCALE		CODE IDENT: 23338 SHEET 1 OF 1



rev	ecn	by	date	app
F	# 1413	JH	5-5-88	
E	ECN 1640	DC	11-14-87	
D	ECN 1506	RO	4-27-87	
C	ECN 562	SW	9-11-85	
B	ECN 318	NB	12-14-84	
A	RELEASED SW DES	WH	12-14-84	

tolerance unless otherwise specified: XXX ± .010, XX ± .030

WAVETEK san diego, calif

scale N/A | by Bourgeois | date 11/5/08 | app

material: N/A

finish: N/A

title: SCHEMATIC — INSTRUMENT

model no: 157 | dwg no: 000-1-00-0043 SHEET 1 OF 2 | rev: F

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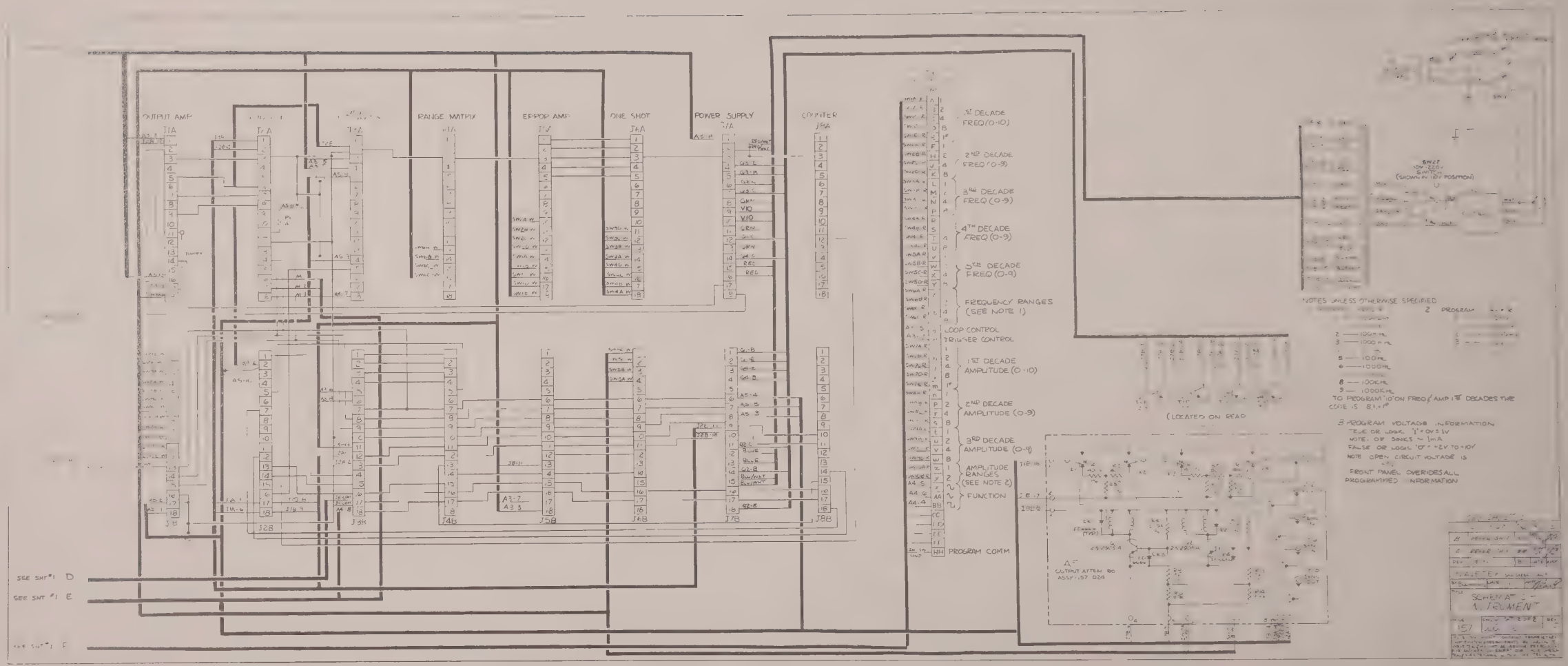
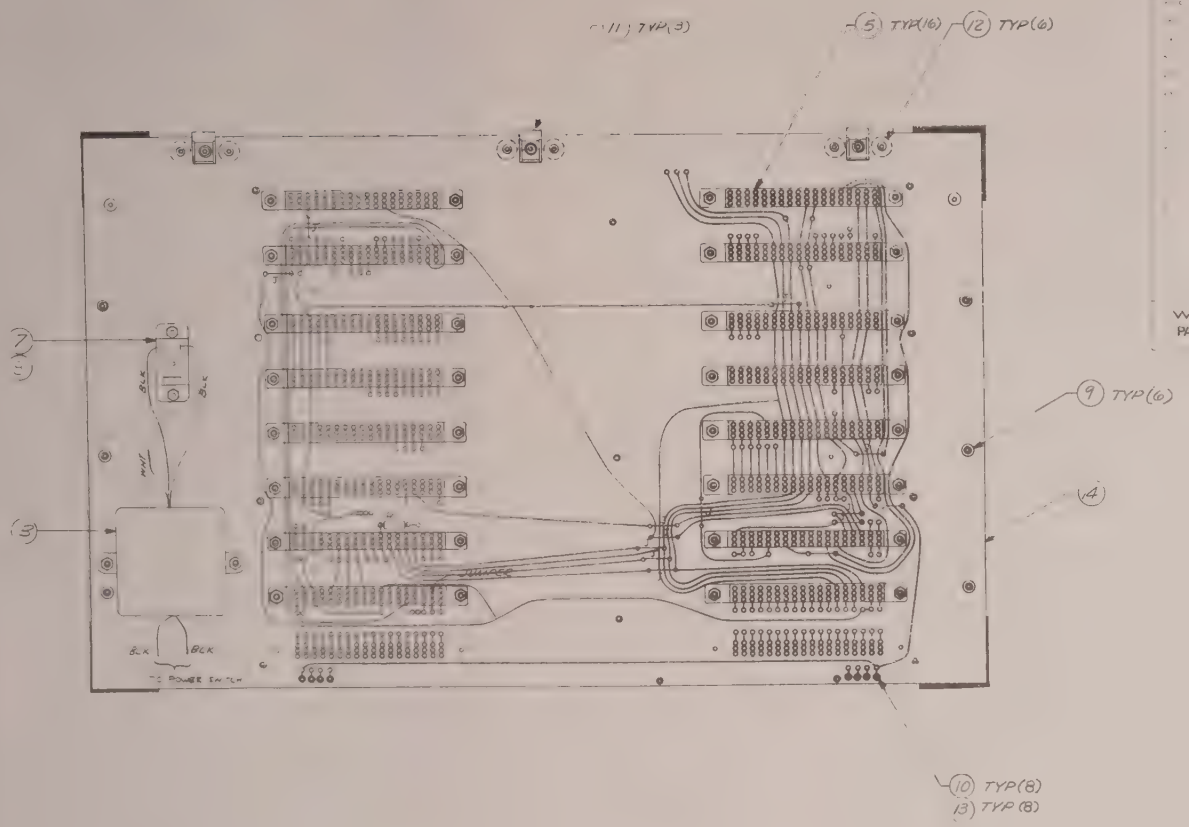

OUTPUT AMP
RANGE MATRIX
ERROR AMP
ONE SHOT
POWER SUPPLY
COUNTER
1ST DECADE FREQ (0-10)
2ND DECADE FREQ (0-9)
3RD DECADE FREQ (0-9)
4TH DECADE FREQ (0-9)
5TH DECADE FREQ (0-9)
FREQUENCY RANGES (SEE NOTE 1)
LOOP CONTROL
TRIGGER CONTROL
1ST DECADE AMPLITUDE (0-10)
2ND DECADE AMPLITUDE (0-9)
3RD DECADE AMPLITUDE (0-9)
AMPLITUDE RANGES (SEE NOTE 2)
FUNCTION
PROGRAM COMM
NOTES UNLESS OTHERWISE SPECIFIED
TO PROGRAM '10' ON FREQ & AMP 1ST DECADES THE CODE IS 8,1,0
PROGRAM VOLTAGE INFORMATION
TRUE OR LOGIC '1' = 0V TO 1V
NOTE: 0V SINKS ~ 1mA
FALSE OR LOGIC '0' = +2V TO +10V
NOTE OPEN CIRCUIT VOLTAGE IS
FRONT PANEL OVERIDES ALL PROGRAMMED INFORMATION
SEE SHT #1 D
SEE SHT #1 E
SEE SHT #1 F



WAVETEK
PARTS LIST

rev	ecn	by	date	app
	D.	E.F.N	8C9	BA	5-21-84	
	C.					
	B	-OP29		AG	4-84	
	A					

tolerance unless otherwise specified: XXX ± 010, XX ± 030

WAVETEK san diego

scale N/A | by BOCARLIND | date 7-12-84 | app

material: N/A

title: ASS'Y MOTHER B'D

model no. 157 | dwg no. 0101-00-0092 | rev D

finish: N/A

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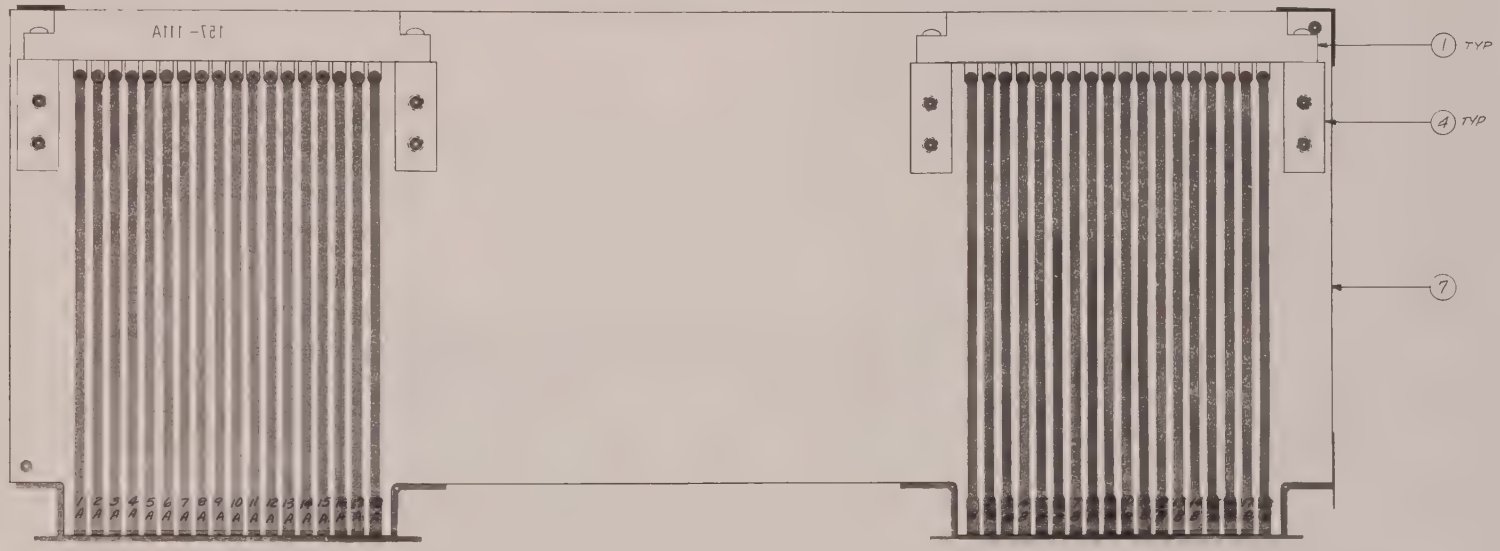

REFERENCE DESIGNATORS	PART DESCRIPTION	MFG-MFGR-PART-NO	MFGR	WAVETEK NO.	QTY
. . :	ASSEMBLY, INSTRUMENT	0000-00-0093	WVTK	0000-00-0093	1
. . .	ASSY UNMOD EXTENDER	0101-00-0093	WVTK	0101-00-0093	1
. . .	CONN. BLOCK	157-114	WVTK	1900-00-0043	4
. . .	EXTENDER	157-111	WVTK	1700-00-0093	1
. . .	CONN, EDGE CARD	252-18-30-100	CINCH	2100-03-0013	2

WAVETEK PARTS LIST	TITLE EXTENDER	ASSEMBLY NO. 1100-00-0093 PAGE: 1	REV A

A REDRAWN EH 6/7/69

tolerance unless otherwise specified	rev	ecn	by	date	app.
.XXX ± .010 .XX ± .030	WAVETEK san diego, calif				
scale 2/1	DE Hatch 6/6/69	date	appd		
material N/A	title ASS'Y EXTENDER BOARD				
finish N/A	model no. 157	dwg no. 0101-00-0093	rev A		

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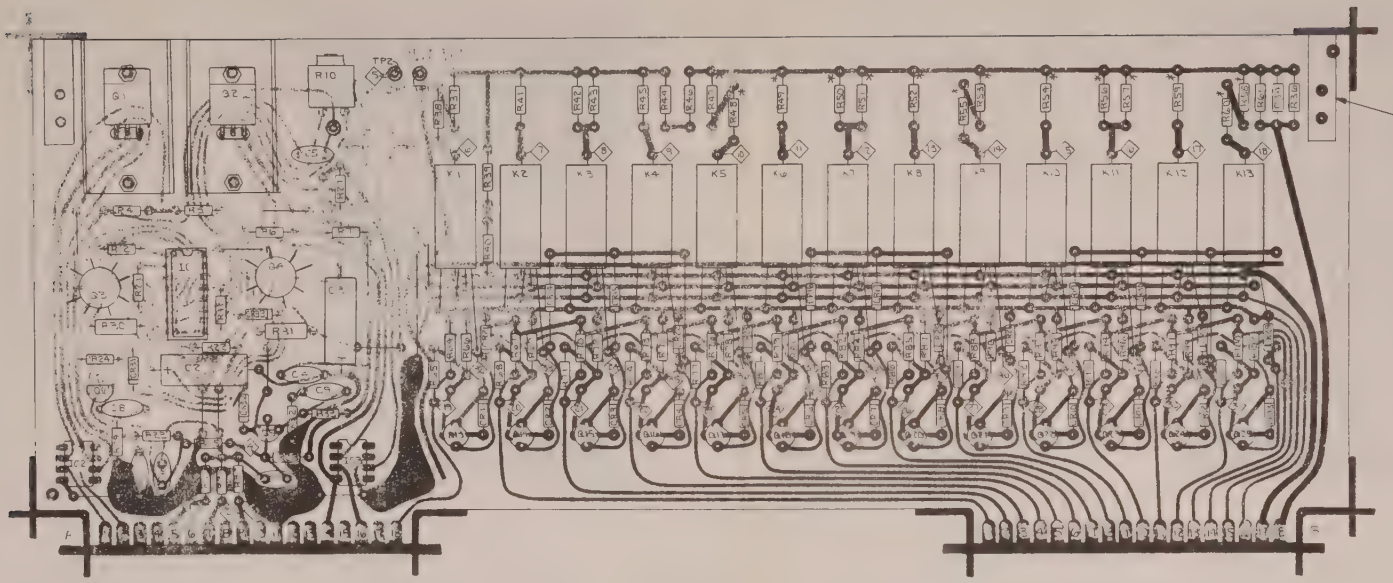


* INDICATES MATCHED SETS

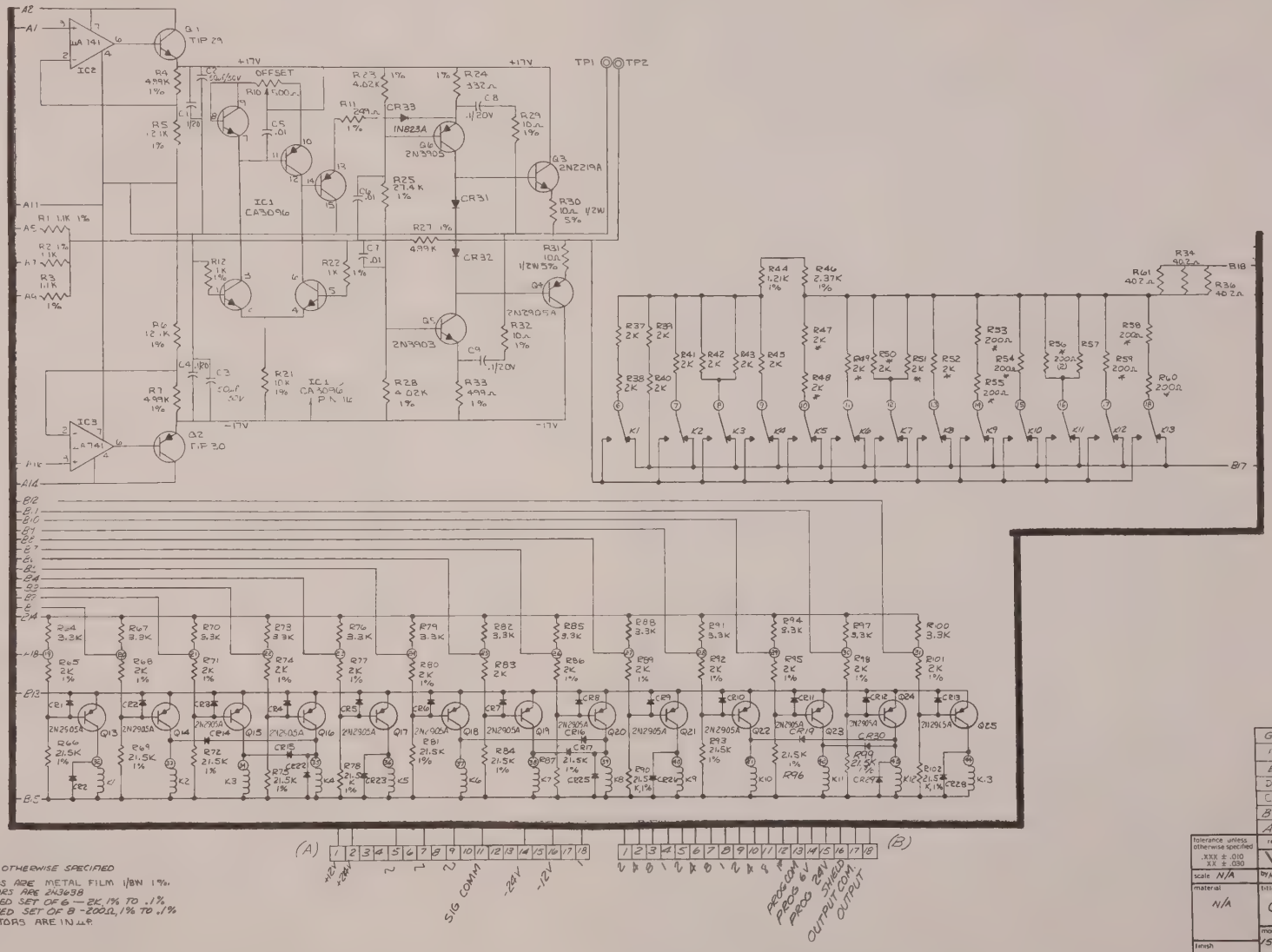
M	ECN 1236		
L	ECN 1071		
K	ECN 440		
J	ECN 768		
H	ECN 10		
G	ECN 98		
F	ECN 703		
E	REDRAWN.		
REVS	ECN'S	BY	DATE

AVETEK

ASSEMBLY

OUTPUT AMPLIFIER

SCALE 2:1 | MATERIAL SIZE A-2 | DWG NO. 0101-00-0052



NOTES UNLESS OTHERWISE SPECIFIED

1. RESISTORS ARE METAL FILM 1/8W 1%.
2. TRANSISTORS ARE 2N3638
3. * MATCHED SET OF 6 — 2K, 1% TO .1%
   MATCHED SET OF 8 -200Ω, 1% TO .1%
4. CAPACITORS ARE IN μF.

G	ECN 2115	DC	3/28/80	
F	ECN 1642	DC	11/4/77	
E	ECN 1549	RO	6-8-77	
D	ECN 1526	RO	2-14-77	
C	ECN 1071	BA	9-9-76	
B	ECN 3036 (CR28)	NG	8/11/76	
A				
rev	ecn	by	date	app

tolerance unless otherwise specified .XXX ± .010 XX ± .030

scale N/A

material N/A

finish N/A

WAVETEK san diego, calif

by M BOCHICHIO date 9-30-68 appr

title SCHEMATIC OUTPUT AMPLIFIER A CONFIGURATION

model no. 157E 170 dwg no. 0103-00-00094 rev C

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REV	ECN	BY	DATE	APP

REFERENCE DESIGNATORS	PART DESCRIPTION	ORIG-MFGR-PART-NO	MFGR	WAVETEK NO.	QTY/PT
3	ASSY DRWG,OUTPUT AMP	0101-00-0098	WVTK	0101-00-0098	1
2	SCHEMATIC,OUTPUT AMP	0103-00-0098	WVTK	0103-00-0098	1
5	CONN, BLOCK	157-315	WVTK	1400-00-3053	2
C5 C6 C7	CAP,CER,.01MF,50V	CK-103	CRL	1500-01-0310	3
C1 C4 C8 C9	CAP,CER,.1MF,20V	UK20-104	CRL	1500-01-0413	4
C2 C3	CAP,ELECT,50MF,50V	500D506G050DD7	SPRAG	1500-35-0003	2
7	OUTPUT AMP	157-112	WVTK	1700-00-0094	1
71	SKT,IC,16PIN	16-DIP	CINCH	2100-03-0022	1
9	TERM	201081	USECO	2100-05-0011	3
11	HEAT SINK	NF-207	WAKE	2800-11-0001	2
12	HEAT SINK	NR-150A	RCA	2800-11-0002	2
13	TRANSIPAD	10180	METRS	2800-11-0004	2
K1 K10 K11 K12 K13 K2 K3 K4 K5 K6 K7 K8 K9	RELAY,REED,FORM=C	P8-865	PHIPP	4500-00-0005	13
R10	POT,TRIM,500	FR-503M	AB	4600-05-0100	1
R30 R31	RES,C,1/2W,5%,10	RC20GF-100	STKPL	4700-25-0100	2
R12 R22	RES,MF,1/8W,1%,1K	RN55D-1001F	TRW	4701-03-1001	2
R21	RES,MF,1/8W,1%,10K	RN55D-1002F	TRW	4701-03-1002	1

WAVETEK PARTS LIST | TITLE OUTPUT AMP | ASSEMBLY NO. 1100-00-0098 | PAGE: 1 | REV T

REFERENCE DESIGNATORS	PART DESCRIPTION	ORIG-MFGR-PART-NO	MFGR	WAVETEK NO.	QTY/PT
R33	RES,MF,1/8,1%,499	RN55D-4990F	TRW	4701-03-4990	1
R27 R8 R7	RES,MF,1/8W,1%,4.99K	RN55D-4991F	TRW	4701-03-4991	3
R53 R54 R55 R56 R57 R58 R59 R60	RES,SET,8-200,1/8W QTY1614701-03-2000	157-501-19	WVTK	4789-00-0001	1
R47 R48 R49 R50 R51 R52	RES,SET,6-2M,1/8W QTY1614701-03-2001	157-501-20	WVTK	4789-00-0009	1
CR33	DIODE,ZENER 6.2V	1N823A	NPC	4801-01-0823	1
CR1 CR10 CR11 CR12 CR13 CR14 CR15 CR16 CR17 CR19 CR2 CR21 CR22 CR23 CR24 CR25 CR26 CR28 CR29 CR3 CR30 CR4 CR5 CR6 CR7 CR8 CR9	DIODE	FD-6666	FAIR	4807-02-6666	27
Q3	TRANS	2N2219A	NSC	4901-02-2191	1
Q13 Q14 Q15 Q16 Q17 Q18 Q19 Q20 Q21 Q22 Q23 Q24 Q25 Q4	TRANS	2N2905A	NSC	4901-02-9051	14
Q5	TRANS	2N3903	NSC	4901-03-9030	1
Q6	TRANS	2N3905	ITT	4901-03-9050	1
Q1	TRANS	TIP-29	TI	4902-00-0290	1
Q2	TRANS	TIP-30	TI	4902-00-0300	1

WAVETEK PARTS LIST | TITLE OUTPUT AMP | ASSEMBLY NO. 1100-00-0098 | PAGE: 3 | REV T

REFERENCE DESIGNATORS	PART DESCRIPTION	ORIG-MFGR-PART-NO	MFGR	WAVETEK NO.	QTY/PT
R29 R32	RES,MF,1/8W,1%,10	RN55D-10R0F	TRW	4701-03-1009	2
R1 R2 R3	RES,MF,1/8W,1%,1.1K	RN55D-1101F	TRW	4701-03-1101	3
R66	RES,MF,1/8W,1%,1.21K	RN55D-1211F	TRW	4701-03-1211	1
R5 R6	RES,MF,1/8W,1%,12.1K	RN55D-1212F	TRW	4701-03-1212	2
R101 R37 R38 R39 R40 R61 R62 R63 R65 R68 R71 R74 R77 R80 R83 R86 R89 R92 R95 R98	RES,MF,1/8W,1%,2K	RN55D-2001F	TRW	4701-03-2001	21
R107 R66 R69 R72 R75 R78 R81 R84 R87 R90 R93 R96 R99	RES,MF,1/8W,1%,21.5K	RN55D-2152F	TRW	4701-03-2152	13
R46	RES,MF,1/8W,1%,2.37K	RN55D-2371F	TRW	4701-03-2371	1
R11	RES,MF,1/8W,1%,249	RN55D-2490F	TRW	4701-03-2490	1
R25	RES,MF,1/8W,1%,27.4K	RN55D-2742F	TRW	4701-03-2742	1
R24	RES,MF,1/8W,1%,332	RN55D-3320F	TRW	4701-03-3320	1
R100 R64 R67 R70 R73 R76 R79 R82 R85 R88 R91 R94 R97	RES,MF,1/8W,1%,3.32K	RN55D-3321F	TRW	4701-03-3321	13
R24 R28	RES,MF,1/8W,1%,4.02K	RN55D-4021F	TRW	4701-03-4021	2
R38 R36 R61	RES,MF,1/8W,1%,80.2	RN55D-80R2F	TRW	4701-03-4929	3

WAVETEK PARTS LIST | TITLE OUTPUT AMP | ASSEMBLY NO. 1100-00-0098 | PAGE: 2 | REV T

REFERENCE DESIGNATORS	PART DESCRIPTION	ORIG-MFGR-PART-NO	MFGR	WAVETEK NO.	QTY/PT
IC2 IC3	IC	MA-741	FAIR	7000-07-4100	2
IC1	IC	CA-3096AE	RCA	7000-30-9600	1

WAVETEK PARTS LIST | TITLE OUTPUT AMP | ASSEMBLY NO. 1100-00-0098 | PAGE: 4 | REV T

NOTE: UNLESS OTHERWISE SPECIFIED

REMOVE ALL BURRS AND BREAK SHARP EDGES	DRAWN	DATE	WAVETEK SAN DIEGO, CALIFORNIA
MATERIAL	PROJ ENGR		TITLE: OUTPUT AMPL
	RELEASE APPROV		
FINISH WAVETEK PROCESS	TOLERANCE UNLESS OTHERWISE SPECIFIED .XXX ± .010 ANGLES ± 1 .XX ± .030		
	DO NOT SCALE DWG		MODEL NO: 7 / DWG NO: 1100-00-0094 / REV: T
	SCALE		CODE IDENT: 23338 / SHEET OF

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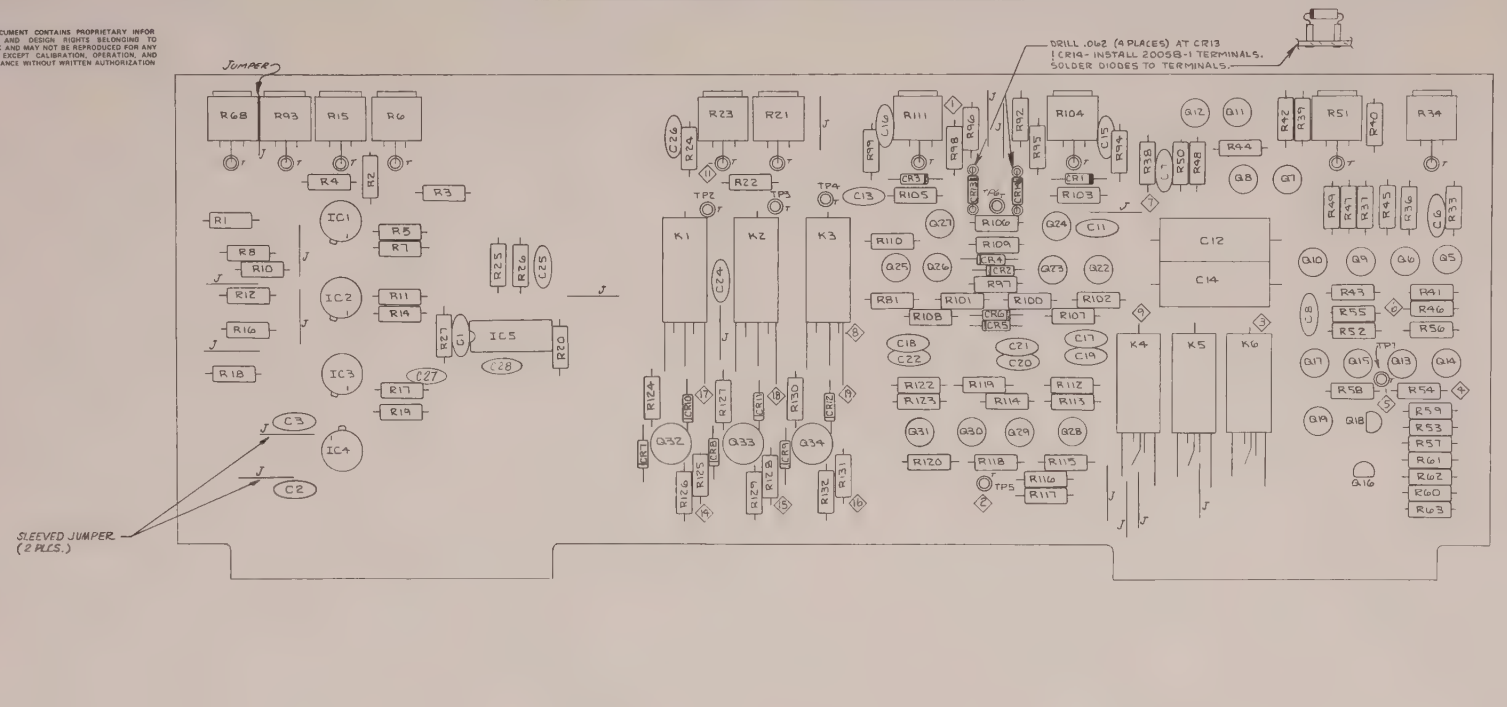
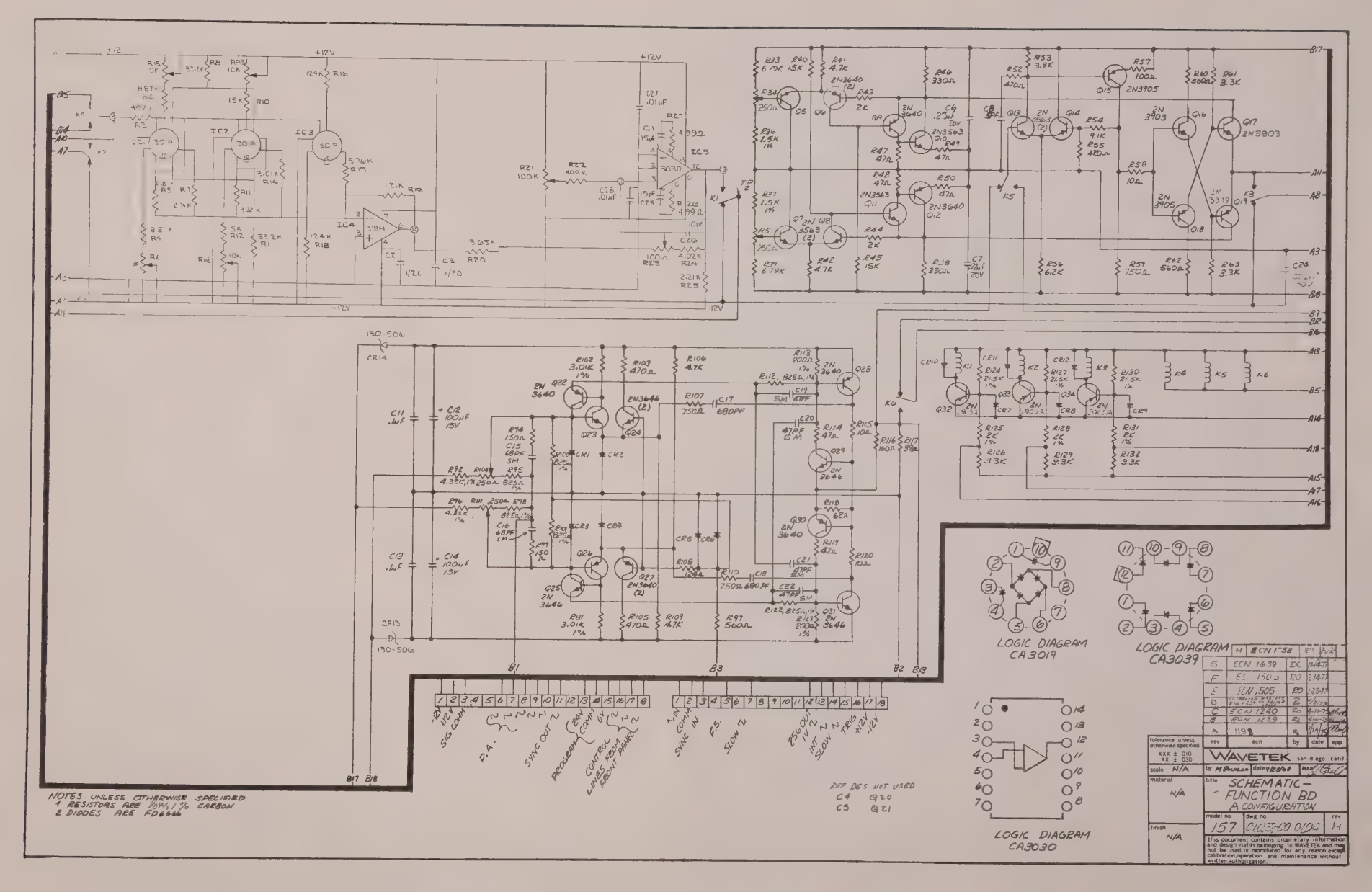
REV	ECN	BY	DATE	APP
A	1198	KD	2-4-77	
B	ECN 1239	RO	4-20-77	
C	ECN 240	RO	6-7-77	
D	ECN 1369	RO	11-11-77	
E	ECN 1505	RO	1-24-77	
F	ECN 2115	DK	5-22-87	



NOTE: UNLESS OTHERWISE SPECIFIED

REMOVE ALL BURRS AND BREAK SHARP EDGES	DRAWN ADKINS	DATE 11-13-76	WAVETEK SAN DIEGO, CALIFORNIA
MATERIAL	ENGINEER		TITLE ASSEMBLY FUNCTION BOARD A CONFIGURATION
FINISH WAVETEK PROCESS	RELEASE APPROV		
	TOLERANCE UNLESS OTHERWISE SPECIFIED XXX ± .010 ANGLES ±1° XX ± .030		
	DO NOT SCALE DWG		MODEL NO 157
	SCALE		DWG NO 0101-00-0104 REV F
			CODE IDENT 23338 SHEET 1 OF 1


+12V
-12V
IC1
IC2
IC3
IC4
IC5
3030
3018
LOGIC DIAGRAM CA3019
LOGIC DIAGRAM CA3039
LOGIC DIAGRAM CA3030
REF DES NOT USED
C4 Q20
C5 Q21
NOTES UNLESS OTHERWISE SPECIFIED
1 RESISTORS ARE 1/4W, 1% CARBON
2 DIODES ARE FD6666
WAVETEK san diego calif
SCHEMATIC — FUNCTION BD A CONFIGURATION
model no. 157
dwg no. 01C157-60 0102
rev 1H
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REV	ECN	BY	DATE	APP

REFERENCE DESIGNATORS	PART DESCRIPTION	MFGR-PART-NO	MFGR	WAVETEK NO.	QTY/PT
NONE	ASSY DRAW,FUNCTION	01U1-00-0100		01U1-00-0100	1
NONE	SCHEMATIC,FUNCTION	0103-00-0100		[illegible]	1
C5	CAP,CER,5PF,1KV	UC-050		1500-05-5011	1
C6*	CAP,CER,10PF,1KV	UC-100		1500-01-0011	1
C24 C27 C28	CAP,CER,.001UF,50V	CK-103		1500-01-0310	3
C11 C13	CAP,CER,.1UF,10V	UK10-104		1500-01-0417	2
C2 C3	CAP,CER,.01UF,100V	UK20-103		1500-01-0413	2
C1 C25	CAP,CER,15PF,1KV	UC-150		1500-01-5011	2
C8 C7	CAP,CER,.22PF,25V	MT-774		1500-02-2024	2
C17 C18	CAP,CER,.0068UF,1KV	UC-682		1500-06-8111	2
C19 C20	CAP,MICA,470PF,500V	DM19-471J		1500-18-7000	2
C15 C16	CAP,MICA,68PF,500V	DM15-680J		1500-14-8000	2
C12 C14	CAP,ELECT,100UF,16V	5300D10760I6UL7		1500-51-0101	2
*	FUNCTION	157-126		17U0-00-0104	1
*	TERM	2005B1		0100-05-0010	4
*	TERM	2010B1		0100-05-0011	10
A1 A2 A3 A4 A5 A6	HEAT,HEED,FORM-C	MS-B6A		4500-00-0005	6
R15 R6 R66 R93	POT,1M,LIN	PM-103P		[illegible]	4

WAVETEK PARTS LIST	TITLE FUNCTION	ASSEMBLY NO 1100-00-0100 PAGE: 1	REV H

REFERENCE DESIGNATORS	PART DESCRIPTION	MFGR-PART-NO	MFGR	WAVETEK NO.	QTY/PT
R102 R14 R61	RES,MF,1/8W,1%,3,01A	MF55D-3011F		4701-03-3011	3
R38 R46	RES,MF,1/8W,1%,332	MF55D-3320F		4701-03-3320	2
R11 R120 R124 R132 R53 R61 R65	RES,MF,1/8W,1%,3,32K	MF55D-3321F		4701-03-3321	7
R1 R8	RES,MF,1/8W,1%,33.2K	MF55D-3322F		4701-03-3322	2
R26	RES,MF,1/8W,1%,3,65K	MF55D-3651F		4701-03-3651	1
R[illegible]	RES,MF,1/8W,1%,39.2	MF55D-39R2F		4701-03-39R2	1
R69	RES,MF,1/8W,1%,40.2K	MF55D-4022F		4701-03-4021	1
R4	RES,MF,1/8W,1%,402	MF55D-40R2F		4701-03-4029	1
R42 R48	RES,MF,1/8W,1%,43.2K	MF55D-4322F		4701-03-4321	2
R103 R105 R52 R55	RES,MF,1/8W,1%,464	MF55D-4640F		4701-03-4640	4
R114 R117 R47 R48 R49 R50	RES,MF,1/8W,1%,46.4K	MF55D-4642F		4701-03-4642	6
R106 R107 R43 R42	RES,MF,1/8W,1%,4,75K	MF55D-4751F		4701-03-4751	4
R20 R27	RES,MF,1/8W,1%,499	MF55D-4990F		4701-03-4990	2
R60 R62 R67	RES,MF,1/8W,1%,57D	MF55D-5760F		4701-03-5760	3
R17	RES,MF,1/8W,1%,5,76K	MF55D-5761F		4701-03-5761	1
R33 R34 R50	RES,MF,1/8W,1%,6,19K	MF55D-6191F		4701-03-6191	3

WAVETEK PARTS LIST	TITLE FUNCTION	ASSEMBLY NO 1100-00-0100 PAGE: 3	REV H

REFERENCE DESIGNATORS	PART DESCRIPTION	MFGR-PART-NO	MFGR	WAVETEK NO.	QTY/PT
IC1 IC2 IC3	IC	CA-3014	RCA	7000-14-1400	3
IC5	IC,CLASS II,CA-3036 WITH 7000-30-3000	130-501-16	AVTK	7200-00-0002	1

WAVETEK PARTS LIST	TITLE FUNCTION	ASSEMBLY NO 1100-00-0100 PAGE: 5	REV H

REFERENCE DESIGNATORS	PART DESCRIPTION	MFGR-PART-NO	MFGR	WAVETEK NO.	QTY/PT
R2*	POT,1K,LIN,100M	PM-102P		4600-01-0401	
R104 R112 R36 R51	POT,1K,LIN,25W	PM-252P		4600-02-5102	
*	POT,1K,LIN,10W	PM-101P		4601-01-0102	1
*	RES,MF,1/8W,1%,1W	MF55D-1000F		4701-03-1000	
*	RES,MF,1/8W,1%,1,00K	MF55D-1001F		4701-03-1001	
*	RES,MF,1/8W,1%,1,21K	MF55D-1211F		4701-03-1211	
R105	RES,MF,1/8W,1%,12K	MF55D-1202F		4701-03-1202	1
R18 R19	RES,MF,1/8W,1%,12K4	MF55D-1242F		4701-03-1242	2
R44 R45	RES,MF,1/8W,1%,150	MF55D-1500F		4701-03-1500	2
R58 R59	RES,MF,1/8W,1%,1,5K	MF55D-1501F		4701-03-1501	2
R113 R12 R40 R43	RES,MF,1/8W,1%,15K8	MF55D-1582F		4701-03-1582	4
R110	RES,MF,1/8W,1%,16,9K	MF55D-1692F		4701-03-1692	1
* R103	RES,MF,1/8W,1%,2,00K	MF55D-2001F		4701-03-2001	1
* R131 R43	RES,MF,1/8W,1%,2K	MF55D-2001F		4701-03-2001	2
R127 R130	RES,MF,1/8W,1%,21,3K	MF55D-2132F		4701-03-2132	2
*	RES,MF,1/8W,1%,2,61K	MF55D-2611F		4701-03-2611	1
*	RES,MF,1/8W,1%,2,74K	MF55D-2741F		4701-03-2741	1

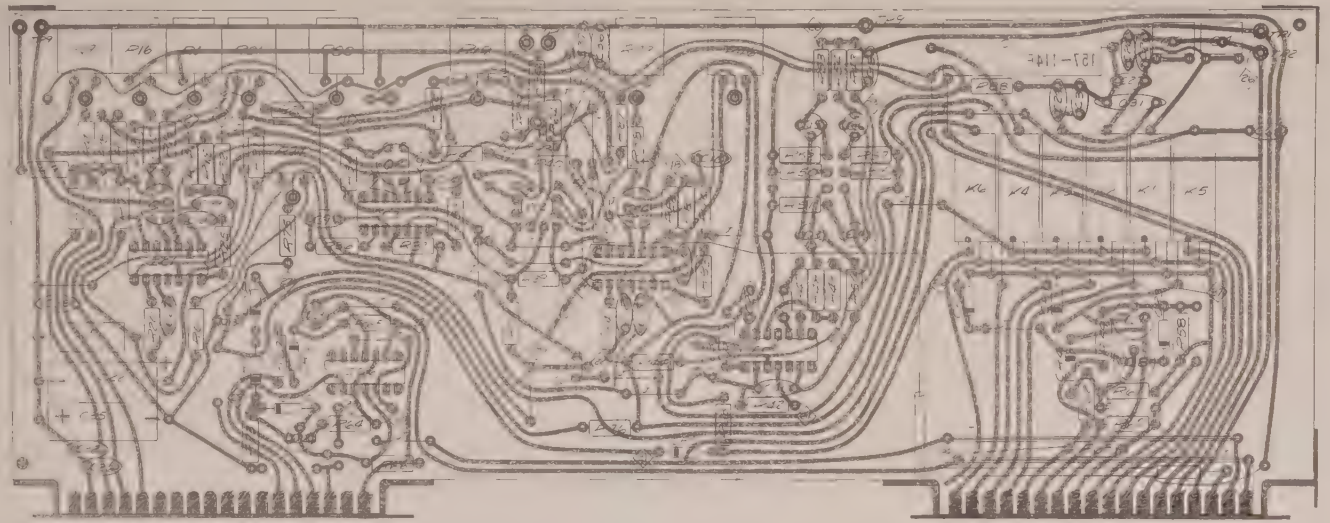
WAVETEK PARTS LIST	TITLE FUNCTION	ASSEMBLY NO 1100-00-0100 PAGE: 2	REV H

REFERENCE DESIGNATORS	PART DESCRIPTION	MFGR-PART-NO	MFGR	WAVETEK NO.	QTY/PT
R119	RES,MF,1/8W,1%,61,9	MF55D-6192F		4701-03-6192	1
R137 R139 R54	RES,MF,1/8W,1%,75K	MF55D-7502F		4701-03-7502	3
R101 R121 R122 R123 R63 R68	RES,MF,1/8W,1%,82K	MF55D-8252F		4701-03-8252	6
R2 R4 R5	RES,MF,1/8W,1%,8,87K	MF55D-8871F		4701-03-8871	3
R34	RES,MF,1/8W,1%,90K9	MF55D-9092F		4701-03-9092	1
R22	RES,MF,1/8W,1%,4,99K	MF55D-4993F		4701-03-4993	1
CR15 CR16	DIODE	1N4581		4801-01-4581	2
CR1 CR10 CR11 CR12 CR2 CR3 CR4 CR5 CR6 CR7 CR8 CR9	DIODE	FD-6000		4807-02-6000	12
Q32 Q33 Q34	TRANS	2N2058		4901-02-4051	3
Q10 Q11 Q13 Q14 Q7 Q8	TRANS	2N3563		4901-03-5630	6
Q12 Q22 Q23 Q27 Q28 Q3 Q5 Q6	TRANS	2N3906		4901-03-6000	8
Q21 Q24 Q25 Q26 Q1	TRANS	2N3946		4901-03-6000	5
Q16 Q17	TRANS	2N3903		4901-03-9030	2
Q15 Q18 Q19	TRANS	2N3963		4901-03-4050	3
IC4	IC	LM310H		7400-03-1800	1

WAVETEK PARTS LIST	TITLE FUNCTION	ASSEMBLY NO 1100-00-0100 PAGE: 4	REV H

NOTE UNLESS OTHERWISE SPECIFIED

REMOVE ALL BURRS AND BREAK SHARP EDGES		WAVETEK SAN DIEGO, CALIFORNIA	
MATERIAL	PROJENGR	TITLE PARTS LIST FUNCTION	
FINISH WAVETEK PROCESS	RELEASE APPROV	TOLERANCE UNLESS OTHERWISE SPECIFIED XXX ± .010 ANGLES ±1 XX ± .030	
	DO NOT SCALE DWG	MODEL NO 157	DWG NO 1100-00-0104 REV H
	SCALE	CODE IDENT 23338	SHEET OF



rev	ecn	by	date	app

unless otherwise specified: XXX ± 0.0, XX ± 0.10

WAVETEK san diego, ca

scale | by | date | app

material: N/A

title: ASSY 106 BOARD

finish: N/A

model no: 157

dwg no: 0.0100-0621

rev: A1

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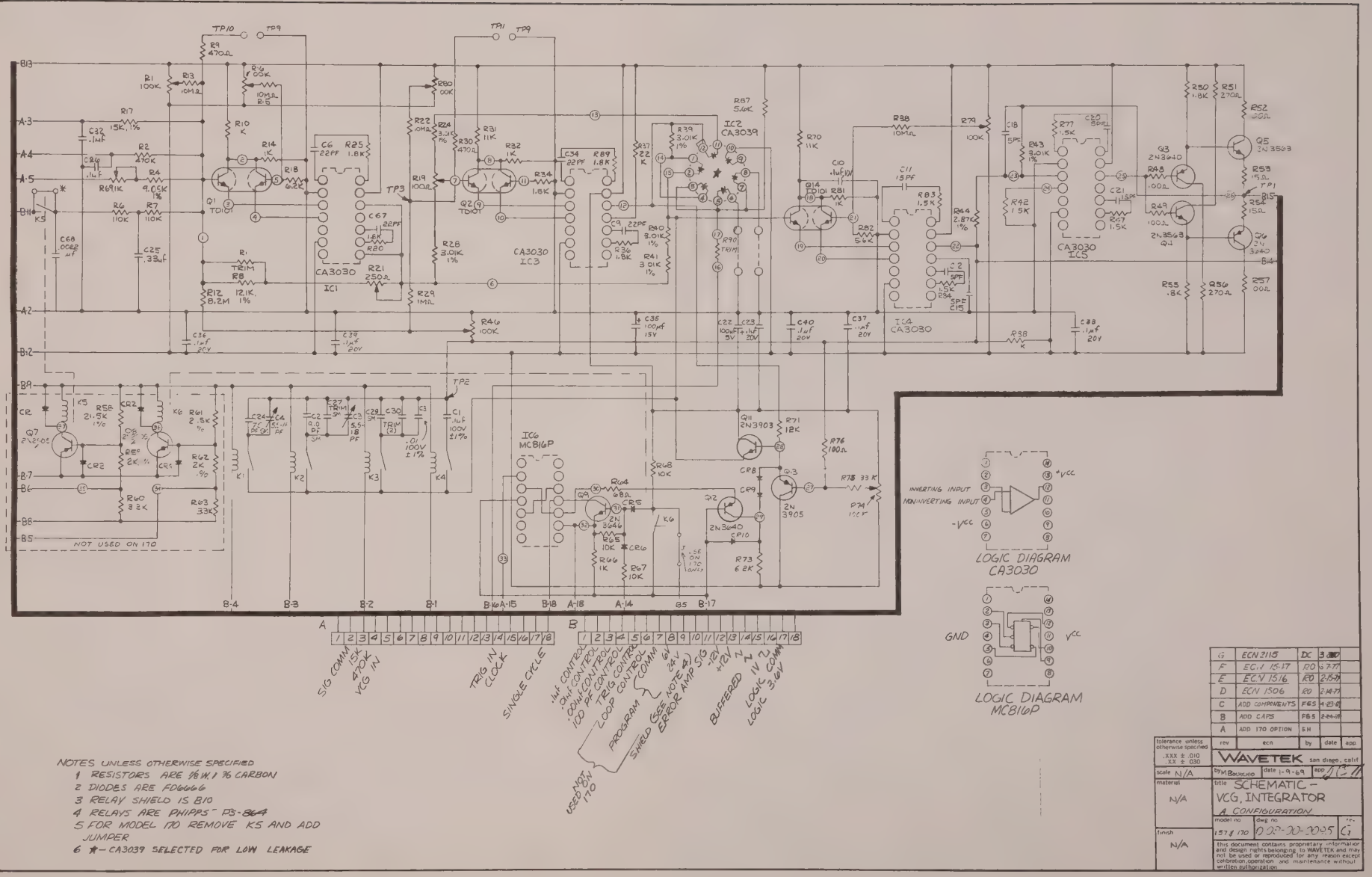

NOTES UNLESS OTHERWISE SPECIFIED

1. RESISTORS ARE 1/8 W, 1 % CARBON
2. DIODES ARE FD6666
3. RELAY SHIELD IS B10
4. RELAYS ARE PHIPPS" P3-864
5. FOR MODEL 170 REMOVE K5 AND ADD JUMPER
6. ★ — CA3039 SELECTED FOR LOW LEAKAGE

G	ECN 2115	DC	3-[illegible]	
F	ECN 1516	RO	2-17-71	
E	ECN 1516	RO	2-17-71	
D	ECN 1506	RO	2-17-71	
C	ADD COMPONENTS	FGS	1-23-70	
B	ADD CAPS	FGS	2-24-69	
A	ADD 170 OPTION	SH		

tolerance unless otherwise specified .XXX ± .010 .XX ± .030	rev	ecn	by	date	app.
		WAVETEK san diego, calif			
scale N/A	Dr'n M Balducci	date 1-9-69	app		
material N/A	title SCHEMATIC — VCG, INTEGRATOR A CONFIGURATION				
finish N/A	model no 157 & 170	dwg no 02-20-2095	rev. G		

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REV	ECN	BY	DATE	APP

REFERENCE DESIGNATORS	PART DESCRIPTION	ORIG-MFGR-PART-NO	MFGR	WAVETEK NO.	QTY/PT
NONE	ABBY DRNG, VCG	0101-00-0095	WVTK	0101-00-0095	1
NONE	SCHEMATIC, VCG	0103-00-0095	WVTK	0103-00-0095	1
C15 C18	CAP,CER,5PF,1KV	DD-050	CRL	1500-00-5011	2
C10 C26 C32	CAP,CER,,1MF,10V	UK10-104	CRL	1500-01-0412	3
C23 C36 C37 C38 C39 C40	CAP,CER,,1MF,20V	UM20-104	CRL	1500-01-0413	6
C11 C12 C20 C21	CAP,CER,15PF,1KV	DD-150	ARCO	1500-01-5011	8
C34 C6 C67 C9	CAP,CER,22PF,1KV	DD-220	ARCO	1500-02-2011	4
C68	CAP,CER,.0022,1KV	DD-222 LONG LEAD	CRL	1500-02-2201	1
C27T	CAP,MICA,36PF,500V	DM15-360J	ARCO	1500-13-6000	1
C2a	CAP,MICA,75PF,500V	DM15-750J	ARCO	1500-17-5000	1
C30T	CAP,MICA,750PF,300V	DM15-751J	ARCO	1500-17-5100	1
C2	CAP,910PF,100V,5%	DM15-911J	ARCO	1500-19-1100	1
C22 C35	CAP,ELECT,100MF,16V	500D107G016DC7	SPRAG	1500-31-0101	2
C31	CAP,POLYC,.01MF,100V	PA2R103F	ELCUB	1500-41-0300	1
C1	CAP,POLYC,,1MF,100V	PA2R104F	ELCUB	1500-41-0404	1
C25	CAP,MYLR,,33MF,200V	X663F .33MF	TMA	1500-43-3014	1
C3 C4	VARI,5.5-18PF,200V	538-006-92R	ERIE	1500-51-8000	2

WAVETEK PARTS LIST | TITLE V-C-G | ASSEMBLY NO 1100-00-0095 | PAGE: 1 | REV N

REFERENCE DESIGNATORS	PART DESCRIPTION	ORIG-MFGR-PART-NO	MFGR	WAVETEK NO.	QTY/PT
R53 R54	RES,MF,1/8W,1%,15	RN55D-15R0F	TRW	4701-03-1509	2
R20 R25 R34 R36 R50 R55 R69	RES,MF,1/8W,1%,1.78K	RN55D-1781F	TRW	4701-03-1781	7
R59 R62	RES,MF,1/8W,1%,2K	RN55D-2001F	TRW	4701-03-2001	2
R37 R58 R61	RES,MF,1/8W,1%,21.5K	RN55D-2152F	TRW	4701-03-2152	3
R51 R56	RES,MF,1/8W,1%,274	RN55D-2740F	TRW	4701-03-2740	2
R64	RES,MF,1/8W,1%,2.87K	RN55D-2871F	TRW	4701-03-2871	1
R24 R28 R39 R40 R41 R43	RES,MF,1/8W,1%,3.01K	RN55D-3011F	TRW	4701-03-3011	6
R60 R63	RES,MF,1/8W,1%,3.32K	RN55D-3321F	TRW	4701-03-3321	2
R75	RES,MF,1/8W,1%,33.2K	RN55D-3322F	TRW	4701-03-3322	1
R30 R9	RES,MF,1/8W,1%,464	RN55D-4640F	TRW	4701-03-4640	2
R82 R87	RES,MF,1/8W,1%,5.76K	RN55D-5761F	TRW	4701-03-5761	2
R18 R73	RES,MF,1/8W,1%,6.19K	RN55D-6191F	TRW	4701-03-6191	2
R66	RES,MF,1/8W,1%,68.1	RN55D-68R1F	TRW	4701-03-6811	1
R4	RES,MF,1/8W,1%,9.09K	RN55D-9091F	TRW	4701-03-9091	1
R29	RES,MF,1/4W,1%,1M	RN60D-1004F	TRW	4701-13-1004	1
R2	RES,MF,1/4W,1%,464K	RN60D-4643F	TRW	4701-13-4643	1

WAVETEK PARTS LIST | TITLE V-C-G | ASSEMBLY NO. 1100-00-0095 | PAGE: 3 | REV N

REFERENCE DESIGNATORS	PART DESCRIPTION	ORIG-MFGR-PART-NO	MFGR	WAVETEK NO.	QTY/PT
NONE	VCG	157-114	WVTK	1700-00-0095	1
NONE	TERM	2010R1	LSECO	2100-05-0011	18
K1 K2 K3 K4 K5 K6	RELAY,REED,FORM-A	P3-A90	PHIPP	4500-00-0004	6
R60	POT,TRIM,1K	FA-102W	AB	4600-01-0200	1
R1 R16 R46 R74 R79 R80	POT,TRIM,100K	FA-104W	AB	4600-01-0400	6
R21	POT,TRIM,250	FA-251W	AB	4600-02-5100	1
R19	POT,TRIM,100	FA-101W	AB	4601-01-0100	1
R13 R15 R22 R38	RES,C,1/2W,5%,10M	RC20GF-106	STKPL	4700-25-1005	4
R12	RES,C,1/2W,10%,8.2M	RC20GF-825	STKPL	4700-25-8209	1
R48 R49 R52 R57 R76	RES,MF,1/8W,1%,100	RN55D-1000F	TRW	4701-03-1000	5
R14 R32 R66 R81 R88	RES,MF,1/8W,1%,1K	RN55D-1001F	TRW	4701-03-1001	5
R65 R67 R68	RES,MF,1/8W,1%,10K	RN55D-1002F	TRW	4701-03-1002	3
R10 R31 R70	RES,MF,1/8W,1%,11K	RN55D-1102F	TRW	4701-03-1102	3
R6 R7	RES,MF,1/8W,1%,110K	RN55D-1103F	TRW	4701-03-1103	2
R71 R4	RES,MF,1/8W,1%,12.1K	RN55D-1212F	TRW	4701-03-1212	2
R42 R47 R77 R83 R84	RES,MF,1/8W,1%,1.5K	RN55D-1501F	TRW	4701-03-1501	5
R17	RES,MF,1/8W,1%,15K	RN55D-1502F	TRW	4701-03-1502	1

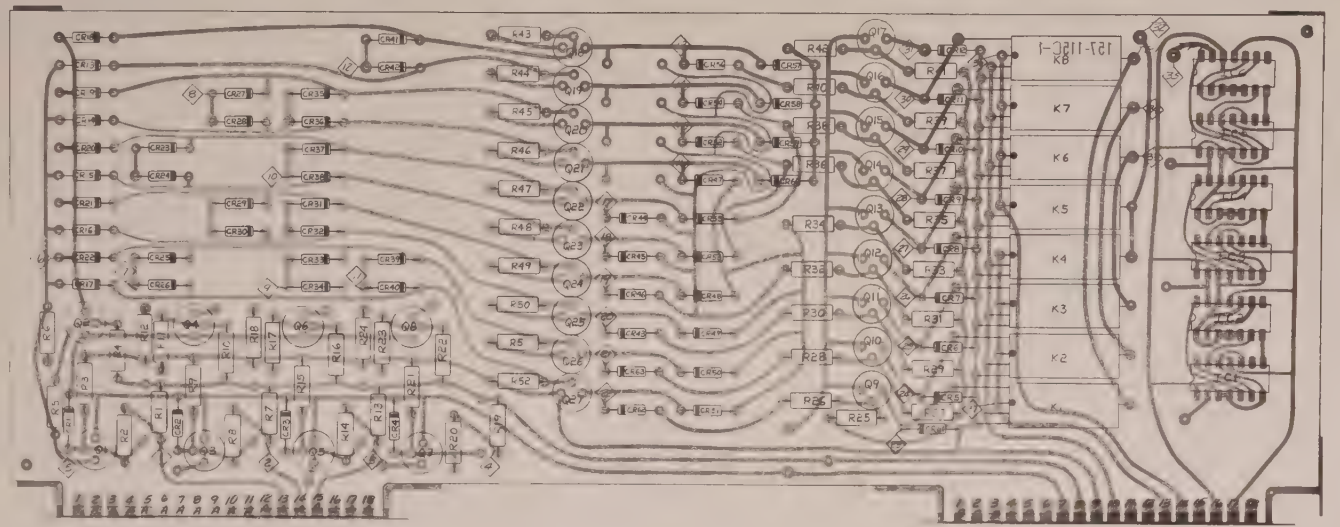
WAVETEK PARTS LIST | TITLE V-C-G | ASSEMBLY NO. 1100-00-0095 | PAGE: 2 | REV N

REFERENCE DESIGNATORS	PART DESCRIPTION	ORIG-MFGR-PART-NO	MFGR	WAVETEK NO.	QTY/PT
CR1 CR10 CR2 CR3 CR4 CR5 CR6 CR8 CR9	DIODE	FD-6666	FAIR	4807-02-6666	9
Q7 Q8	TRANS	2N2905A	NSC	4901-02-9051	2
Q4 Q5	TRANS	2N3563	FAIR	4901-03-5630	2
Q12 Q3 Q6	TRANS	2N3640	FAIR	4901-03-6400	3
Q9	TRANS	2N3646	NSC	4901-03-6460	1
Q11	TRANS	2N3903	NSC	4901-03-9030	1
Q13	TRANS	2N3905	ITT	4901-03-9050	1
Q1 Q14 Q2	TRANS	TO-181	SPRAG	4902-00-1010	3
IC6	IC	MC-816P	MOT	7000-08-1600	1
IC1 IC3 IC4	IC,CLASS I,CA-3030 QTY:1:7000-30-3000	130-501-15	WVTK	7200-00-0001	3
IC5	IC,CLASS II,CA-3030 QTY:1:7000-30-3000	130-501-16	WVTK	7200-00-0002	1
IC2	IC,CLASS I,CA-3039 QTY:1:7000-30-3900	130-501-18	WVTK	7200-00-0004	1

WAVETEK PARTS LIST | TITLE V-C-G | ASSEMBLY NO. 1100-00-0095 | PAGE: 4 | REV N

NOTE UNLESS OTHERWISE SPECIFIED

REMOVE ALL BURRS AND BREAK SHARP EDGES	DRAWN	DATE	WAVETEK SAN DIEGO, CALIFORNIA
MATERIAL	PROJ ENGR		TITLE VCG
	RELEASE APPROV		
FINISH WAVETEK PROCESS	TOLERANCE UNLESS OTHERWISE SPECIFIED XXX ± .010 ANGLES ±1° XX ± .030		
	DO NOT SCALE DWG		MODEL NO 157 / DWG NO 1100-00-0095 / REV N
	SCALE		CODE IDENT 23338 / SHEET OF



D	ECN # 53	7/96	JFH		
E	ECN 036	10	DN		
F	ECN 038	KB	4-9-96		
G	ECN # 40	TAB	11/15/96		
H	RELEASED	EH	12/6/96		

rev	ecn	by	date	app

tolerance unless otherwise specified
XXX ± 0.0
XX ± 030

WAVETEK san diego, ca

scale —

by E. HELMAN date 6/4/96 appr. —

material
1A

finish
11/1

title
ASSY
RANGE MATRIX

model no. 157 | dwg no. 0151-90-0098 | rev G

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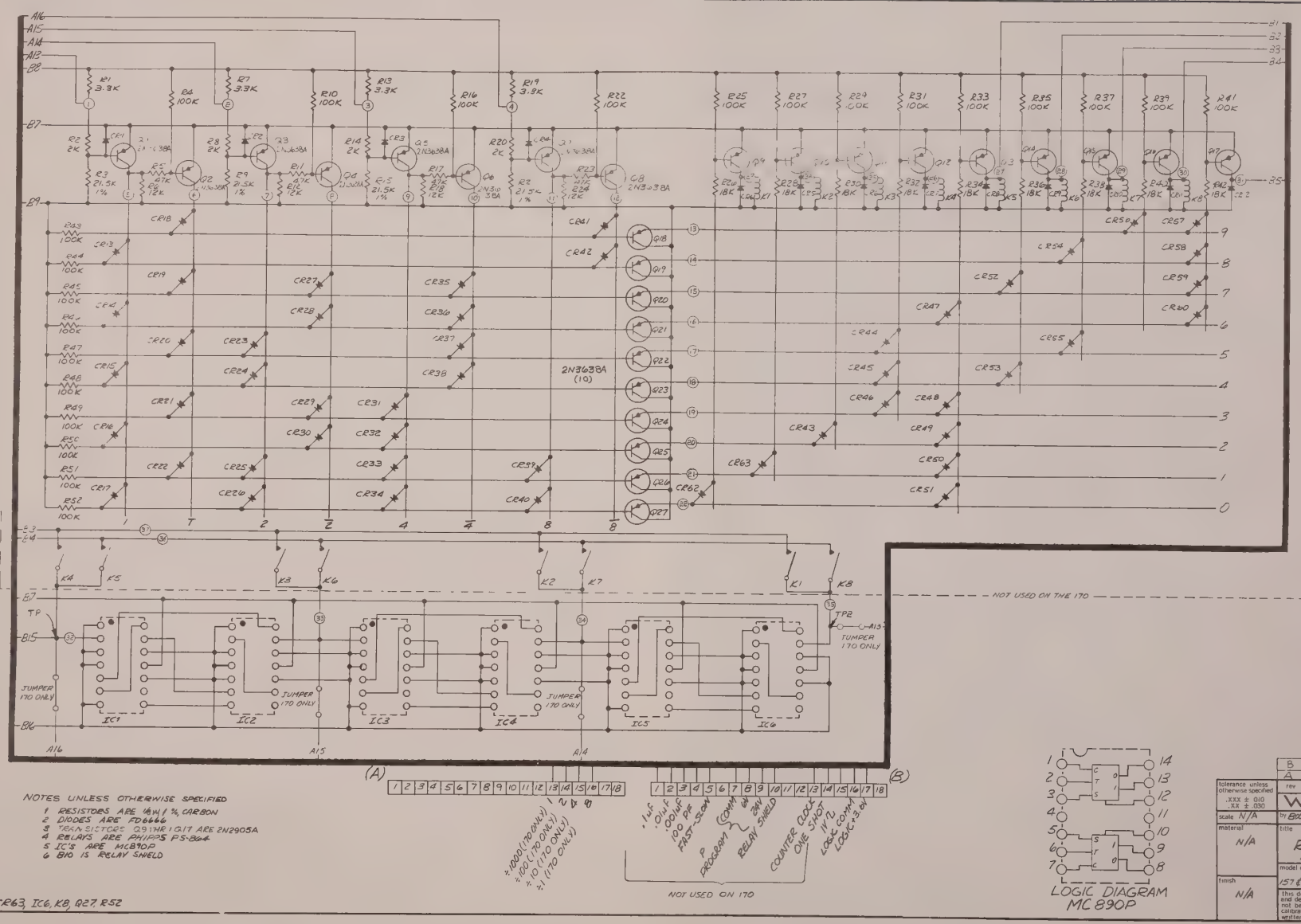

NOTES UNLESS OTHERWISE SPECIFIED
1 RESISTORS ARE 1/8W ± 5% CARBON
2 DIODES ARE FD6666
3 Q1 thru Q17 ARE 2N2905A
4 RELAYS ARE PHILIPS P5-B64
5 IC'S ARE MC890P
6 B10 IS RELAY SHIELD

CR63, IC6, K8, Q27, R52

LOGIC DIAGRAM MC 890P

rev	ecn	by	date	app.
B	ECN # 1868	BD	1-6-70	
A	ECN 1506	ED	2/14/69	

tolerance unless otherwise specified: .XXX ± .010, .XX ± .030

WAVETEK San Diego, Calif.

scale N/A | by BYCHICHO | date 9/25/68 | appr. [illegible]

material N/A

finish N/A

title: SCHEMATIC RANGE MATRIX A CONFIGURATION

model no. 157 & 170 | dwg no. 0103-00-0096 | rev B

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REV	ECN	BY	DATE	APP

REFERENCE DESIGNATORS	PART DESCRIPTION	ORIG-MFGR-PART-NO	MFGR	WAVETEK NO.	QTY/PT
NONE	ASBT DRWG, RANGE MATRIX	0101-00-0096	WVTK	0101-00-0096	1
NONE	SCHEMATIC, RANGE MATRIX	0103-00-0096	WVTK	0103-00-0096	1
NONE	RANGE MATRIX	157-115	WVTK	1700-00-0096	1
NONE	TERM	201081	JSECO	2100-05-0011	2
K1 K2 K3 K4 K5 K6 K7 K8	RELAY,REED,FORM-A	PS-A6A	PHIPP	4500-00-0004	8
R10 R16 R22 R25 R27 R29 R31 R33 R35 R37 R39 R40 R41 R43 R44 R45 R46 R47 R48 R49 R50 R51 R52	RES,MF,1/8W,1%,100K	RN55D-1003F	TRW	4701-03-1003	23
R12 R18 R24 R6	RES,MF,1/8W,1%,12.1K	RN55D-1212F	TRW	4701-03-1212	4
R26 R28 R30 R32 R34 R36 R38 R40 R42	RES,MF,1/8W,1%,18.2K	RN55D-1822F	TRW	4701-03-1822	9
R10 M2 M20 M8	RES,MF,1/8W,1%,2K	RN55D-2001F	TRW	4701-03-2001	4
R15 M21 M3 M9	RES,MF,1/8W,1%,21.5K	RN55D-2152F	TRW	4701-03-2152	4
R1 R13 R19 M7	RES,MF,1/8W,1%,3.32K	RN55D-3321F	TRW	4701-03-3321	4
R11 M17 R23 R5	RES,MF,1/8W,1%,47.5K	RN55D-4752F	TRW	4701-03-4752	4
CR1 CR10 CR11 CR12 CR13 CR14 CR15 CR16	DIODE	FD-6666	FAIR	4807-02-6666	63

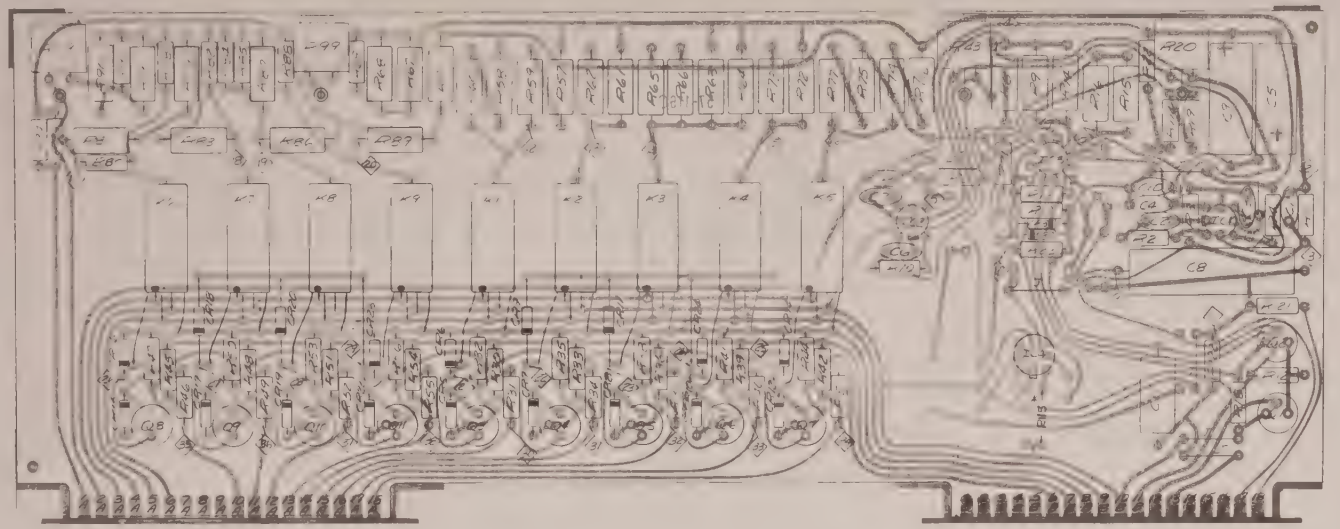
WAVETEK PARTS LIST	TITLE RANGE MATRIX	ASSEMBLY NO 1100-00-0096 PAGE: 1	REV G

REFERENCE DESIGNATORS	PART DESCRIPTION	ORIG-MFGR-PART-NO	MFGR	WAVETEK NO.	QTY/PT
CR17 CR18 CR19 CR2 CR20 CR21 CR22 CR23 CR24 CR25 CR26 CR27 CR28 CR29 CR3 CR30 CR31 CR32 CR33 CR34 CR35 CR36 CR37 CR38 CR39 CR4 CR40 CR41 CR42 CR43 CR44 CR45 CR46 CR47 CR48 CR49 CR5 CR50 CR51 CR52 CR53 CR54 CR55 CR56 CR57 CR58 CR59 CR6 CR60 CR61 CR62 CR63 CR7 CR8 CR9					
Q10 Q11 Q12 Q13 Q14 Q15 Q16 Q17 Q9	TRANS	2N2905A	NSC	4901-02-9051	9
Q1 Q18 Q19 Q2 Q20 Q21 Q22 Q23 Q24 Q25 Q26 Q27 Q3 Q4 Q5 Q6 Q7 Q8	TRANS	2N3638A	LABTR	4901-03-6381	18
IC1 IC2 IC3 IC4 IC5 IC6	IC	MC-A90P	MOT	7000-08-9000	6

WAVETEK PARTS LIST	TITLE RANGE MATRIX	ASSEMBLY NO 1100-00-0096 PAGE: 2	REV G

NOTE UNLESS OTHERWISE SPECIFIED

REMOVE ALL BURRS AND BREAK SHARP EDGES	DRAWN	DATE	WAVETEK SAN DIEGO • CALIFORNIA
MATERIAL	PROJ ENGR		TITLE RANGE MATRIX
	RELEASE APPROV		
FINISH WAVETEK PROCESS	TOLERANCE UNLESS OTHERWISE SPECIFIED XX ± .010 ANGLES ± 1 XXX ± .030		
	DO NOT SCALE DWG	MODEL NO 157	DWG NO 1100-00-96 REV G
	SCALE	CODE IDENT 23338	SHEET OF



NOTE:
* TO EITHER PIN 1 OR PIN 5 IN CALIBRATION

	B	ECN 895	NG	8/69	LP
	rev	eco	by	date	ml

tolerances unless otherwise noted: .XXX ± 0.10, .XX ± .030	WAVETEK san diego, calif.
scale N/A	by BOCHICHIO date 7-7-69 app
material N/A	title ASSY ERROR AMPLIFIER
finish N/A	model no 157 dwg no 0151-00-E97 rev A

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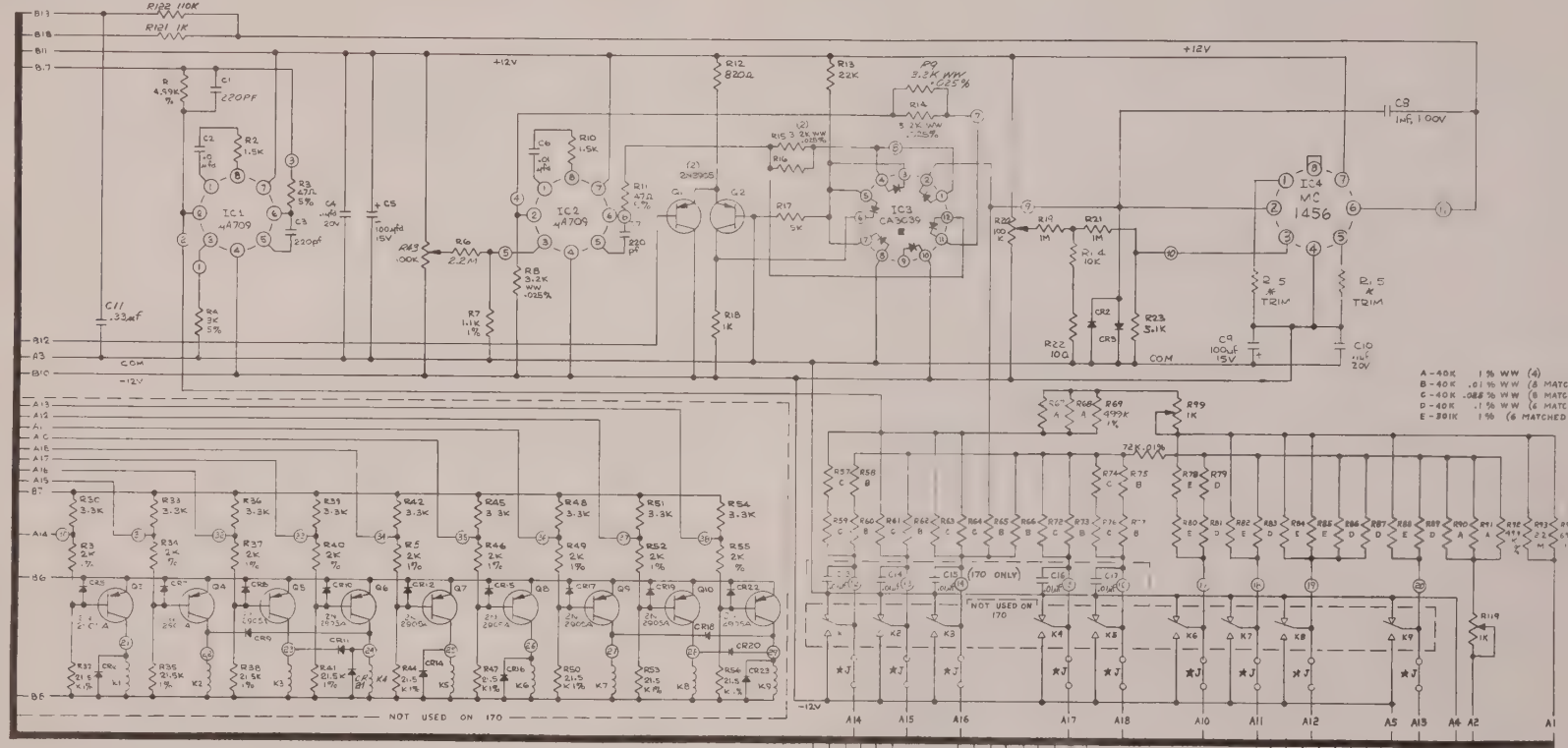
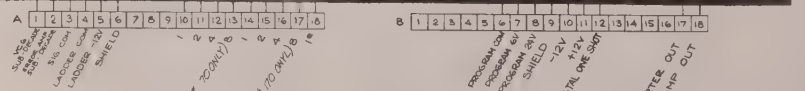
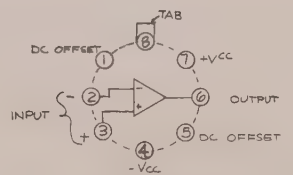
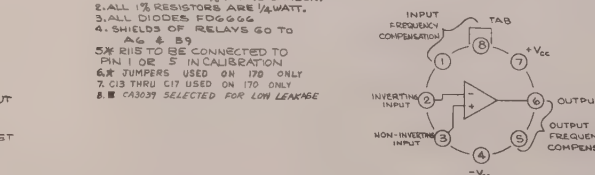




NOTES – UNLESS OTHERWISE SPECIFIED
1. ALL RESISTORS ⅛ W 1 % CARBON.
2. ALL 1% RESISTORS ARE ¼ WATT.
3. ALL DIODES FDG666
4. SHIELDS OF RELAYS GO TO A6 & B9
5. R115 TO BE CONNECTED TO PIN 1 OR 5 IN CALIBRATION
6. JUMPERS USED ON 170 ONLY
7. C13 THRU C17 USED ON 170 ONLY
8. CA3039 SELECTED FOR LOW LEAKAGE



LOGIC DIAGRAM
MC 1456



LOGIC DIAGRAM FOR μA709 & 8018

LATEST COMPONENT DESIGNATOR USED

IC4	R120
CR25	Q15
C12	K9

L	ECN 1847	BDG	11 21 68	
K	ECN 1641	DC	11-14-67	
J	ECN 1506	RD	2-24-67	
H	ECN 1210	DC	9-15-66	3
G	ECN 980			
F	ADD C11-R121-R122	FGS	4-18-66	
E	ADD C11-R121-R122	FGS	3-13-66	
D	ADD ATA	FGS	2-24-66	
C	ADD AMP ASS'Y	FGS	2-13-66	
B	ADD 170 OPTION	FGS	1-30-66	
A	REDRAW LOGIC			

tolerance unless otherwise specified	rev	ecn	by	date	app.
XXX ± .010 XX ± .030	WAVETEK san diego, calif				
scale NONE	dr'n L. ADAMSON date 9/17/66				
material N/A	title A CONFIGURATION ERROR AMPLIFIER & 1ST & 2ND FREQUENCY DECADE SCHEM.				
finish N/A	model no. 157	dwg no. 0103-00-0097		rev M

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REV	ECN	BY	DATE	APP

REFERENCE DESIGNATORS	PART DESCRIPTION	ORIG-MFGR-PART-NO	MFGR	WAVETEK NO.	QTY/PT
NONE	ASSY DRWG,ERROR AMP	0101-00-0097	WVTK	0101-00-0097	1
NONE	SCHEMATIC,ERROR AMP	0103-00-0097	WVTK	0103-00-0097	1
C2 C6	CAP,CER,.01MF,50V	CK-103	CRL	1500-01-0310	2
C10 C4	CAP,CER,.1MF,20V	UK20-104	CRL	1500-01-0413	2
C1 C5 C7	CAP,CER,220PF,1KV	DD-221	ARCO	1500-02-2111	3
C5 C9	CAP,ELECT,100MF,16V	500D107G016DC7	SPRAG	1500-31-0101	2
C8	CAP,POLYC,1MF,100V	210-81C-105F	IMB	1500-41-0504	1
C11	CAP,MYLR,.33MF,200V	X683F .33MF	TFM	1500-43-3414	1
NONE	ERROR AMP	157-116	WVTK	1700-00-0097	1
NONE	TERM	201081	USECO	2100-05-0011	4
K1 K2 K3 K4 K5 K6 K7 K8 K9	RELAY,REED,FORM-C	RS-865	PHIPP	4500-00-0005	9
R119 R99	POT,TRIM,1K	FR-102M	AB	4600-01-0200	2
R20 R85	POT,TRIM,100R	FR-100M	AB	4600-01-0400	2
R6 R93	RES,C,1/2W,10%,2,2M	MC20GF-225	STKP	4700-25-2206	2
R22	RES,MF,1/8W,1%,100	RN55D-1000F	TRW	4701-03-1000	1
R121 R18	RES,MF,1/8W,1%,1K	RN55D-1001F	TRW	4701-03-1001	2
R119	RES,MF,1/8W,1%,10K	RN55D-1002F	TRW	4701-03-1002	1

WAVETEK PARTS LIST	TITLE ERROR AMP	ASSEMBLY NO 1100-00-0097 PAGE: 1	REV H

REFERENCE DESIGNATORS	PART DESCRIPTION	ORIG-MFGR-PART-NO	MFGR	WAVETEK NO.	QTY/PT
	RES,NW,MIXED SET	157-501-23	WVTK	4789-00-0038	1
R14 R15 R16 R8 R9	RES,NW1/8W,.025%,3.2K PART OF 4789-00-0038 QTY:10% PART OF 4789-00-0038 QTY:(5)				
R57 R59 R61 R63 R68 R72 R74 R76	RES,NW1/8W,.025%,40K PART OF 4789-00-0038 QTY:8; PART OF 4789-00-0038 QTY:(8)				
R58 R60 R62 R65 R66 R73 R75 R77	RES,NW,1/8W,.01%,40K PART OF 4789-00-0038 QTY:8; PART OF 4789-00-0038 QTY:(8)				
R67 R68 R90 R91	RES,NW,1/8W,1%,40K PART OF 4789-00-0038 QTY:10% PART OF 4789-00-0038 QTY:(4)				
R71	RES,NW,1/8W,.01%,72K PART OF 4789-00-0038 QTY:(1)% PART OF 4789-00-0038 QTY:(1)				
R70 R81 R85 R86 R87 R89	RES,NW,1/8W,.1%,40K PART OF 4789-00-0038 QTY:10%				

WAVETEK PARTS LIST	TITLE ERROR AMP	ASSEMBLY NO 1100-00-0097 PAGE: 3	REV H

REFERENCE DESIGNATORS	PART DESCRIPTION	ORIG-MFGR-PART-NO	MFGR	WAVETEK NO.	QTY/PT
R7	RES,MF,1/8W,1%,1.1K	RN55D-1101F	TRW	4701-03-1101	1
R122	RES,MF,1/8W,1%,110K	RN55D-1103F	TRW	4701-03-1103	1
R10 R17 R2	RES,MF,1/8W,1%,1.5K	RN55D-1501F	TRW	4701-03-1501	3
R31 R38 R37 R40 R46 R44 R45 R52 R55	RES,MF,1/8W,1%,2K	RN55D-2001F	TRW	4701-03-2001	9
R13 R32 R35 R38 R41 R44 R47 R50 R53 R56	RES,MF,1/8W,1%,21.5K	RN55D-2152F	TRW	4701-03-2152	10
R1	RES,MF,1/8W,1%,3.01K	RN55D-3011F	TRW	4701-03-3011	1
R30 R33 R36 R39 R42 R45 R48 R51 R54	RES,MF,1/8W,1%,3.32K	RN55D-3321F	TRW	4701-03-3321	9
R11 R3	RES,MF,1.6W,1%,86.6	RN55D-86R6F	TRW	4701-03-86R6	2
R1 R23	RES,MF,1/8W,1%,8.99K	RN55D-8991F	TRW	4701-03-8991	2
R12	RES,MF,1/8W,1%,825	RN55D-8250F	TRW	4701-03-8250	1
R19 R21	RES,MF,1/4W,1%,1M	RN60D-1004F	TRW	4701-13-1004	2
R69 R92	RES,MF,1/4W,1%,899K	RN60D-8993F	TRW	4701-13-8993	2
R96	RES,MF,1/4W,1%,898K	RN60D-8983F	TRW	4701-13-8983	1
R78 R40 R82 R84 R85 R88	RES,SET,6-301K,1/8W QTY:6	14701-03-3013	157-501-26	WVTK	4789-00-0029	1

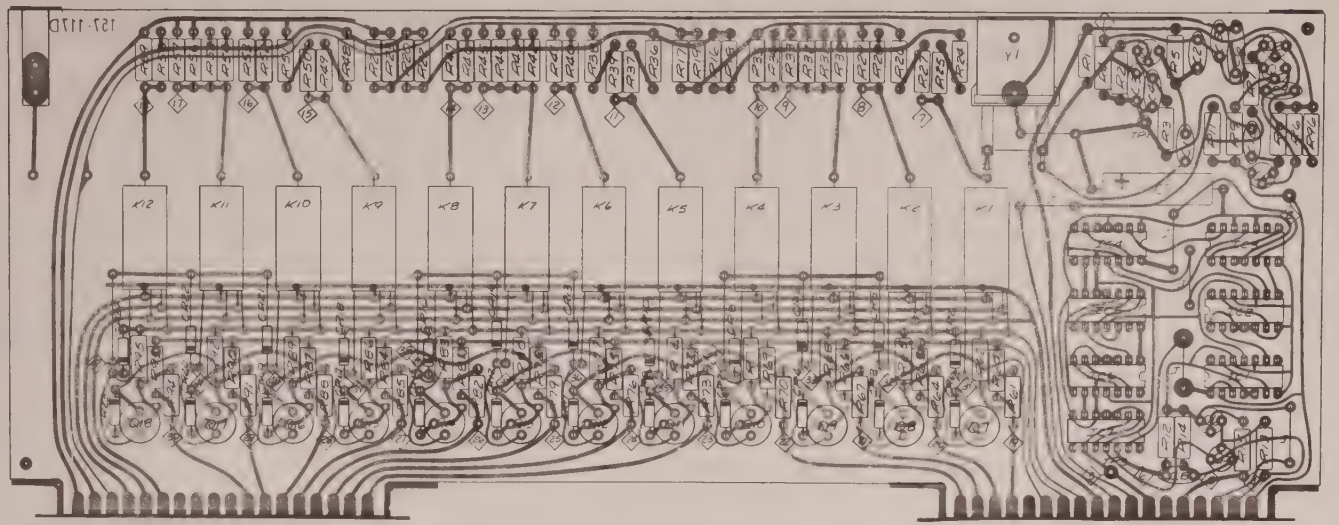
WAVETEK PARTS LIST	TITLE ERROR AMP	ASSEMBLY NO 1100-00-0097 PAGE: 2	REV H

REFERENCE DESIGNATORS	PART DESCRIPTION	ORIG-MFGR-PART-NO	MFGR	WAVETEK NO.	QTY/PT
	RES,NW,MIXED SET			4789-00-0038	
	PART OF 4789-00-0038 QTY:(6)				
CR10 CR11 CR12 CR14 CR15 CR16 CR17 CR18 CR19 CR2 CR20 CR22 CR23 CR24 CR3 CR5 CR6 CR7 CR8 CR9	DIODE	FD-6666	FAIR	4807-02-6666	20
Q10 Q11 Q3 Q4 Q5 Q6 Q7 Q8 Q9	TRANS	2N2905A	NSC	4901-02-9051	9
Q1 Q2	TRANS	2N3904	ITT	4901-03-9050	2
IC4	IC	MC-1456	MOT	7000-16-5600	1
IC3	IC,CLASS I,CA-3039 QTY:1	130-501-18	WVTK	7200-00-0004	1
	17000-30-3900				
IC1 IC2	IC,CLASS 1,MA-709 QTY:1	130-501-13	WVTK	7200-00-0005	2
	17000-07-0900				

WAVETEK PARTS LIST	TITLE ERROR AMP	ASSEMBLY NO 1100-00-0097 PAGE: 4	REV H

NOTE UNLESS OTHERWISE SPECIFIED

REMOVE ALL BURRS AND BREAK SHARP EDGES	DRAWN	DATE	WAVETEK SAN DIEGO, CALIFORNIA
MATERIAL	PROJ ENGR		TITLE
	RELEASE APPROV		ERROR AMPLIFIER
FINISH WAVETEK PROCESS	TOLERANCE UNLESS OTHERWISE SPECIFIED XXX ± .010 ANGLES ± 1 XX ± .030		
	DO NOT SCALE DWG	MODEL NO 157	DWG NO 1100-00-0097 REV H
	SCALE	CODE IDENT 23338	SHEET 1 OF 1



rev	ecn	by	date	app
E	ADD R44	JK	5/7/79	
D	ADD R43	MHS	4/3/79	
C	ADD R40	MHS	4/3/79	
B	RELEASE	JK	2/16/79	

tolerance unless otherwise specified	WAVETEK san diego, calif.
XXX ± .010 XX ± .030	
scale 3	by [illegible] date 2/16/79 rev B
material N/A	title ASS'Y CRYSTAL ONE SHOT
finish N/A	model no 157 · dwg no 0101-30-0033 · rev E

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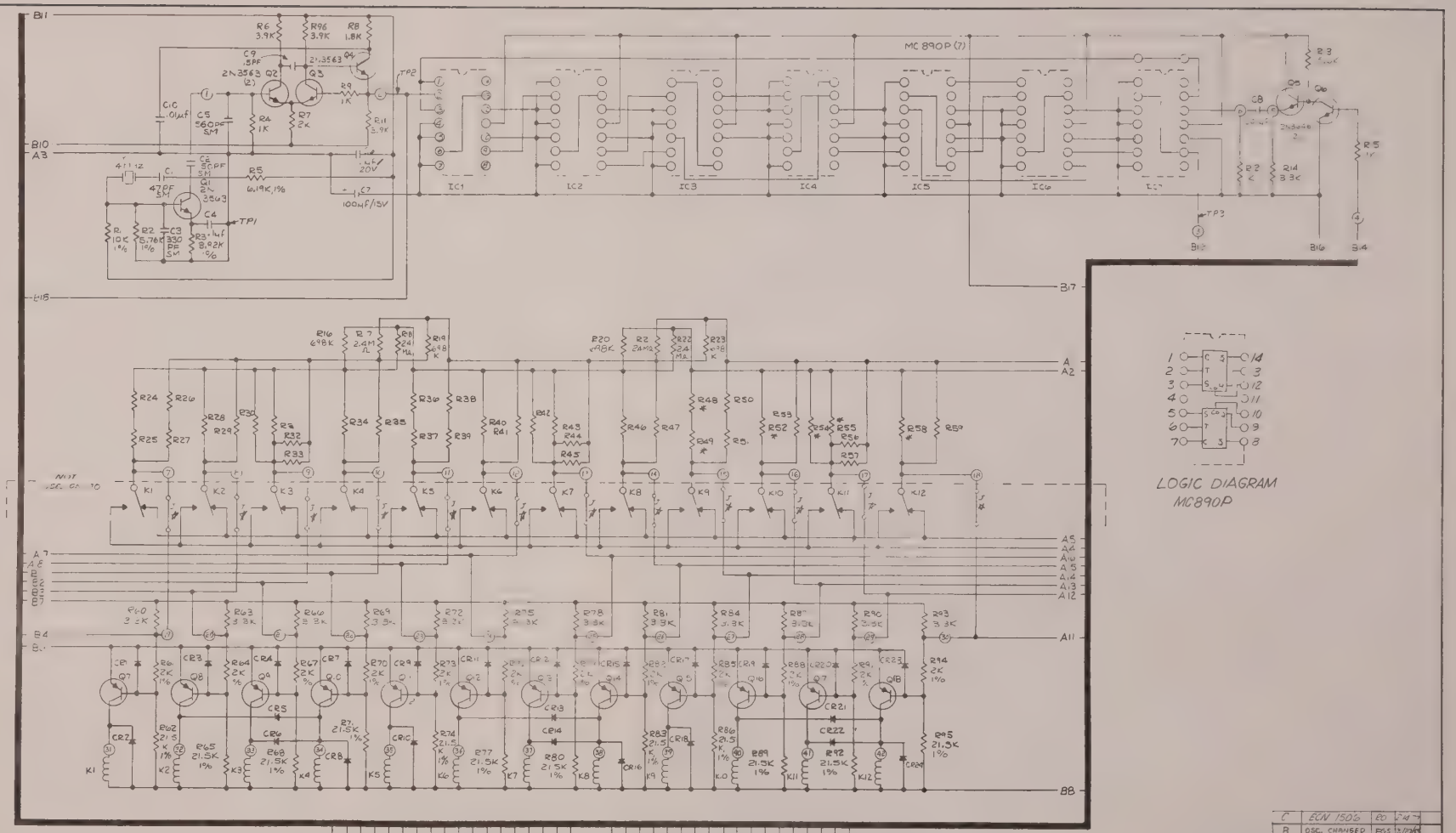

NOTES: UNLESS OTHERWISE SPECIFIED
1. RESISTORS ARE ¼W, 1 % CARBON
2. R24 THRU R59 ARE 301K, 1%
3. DIODES ARE FD6666
4. TRANSISTORS ARE 2N2905A-07-018
5. RELAY SHIELDS GOTO B9
6. 301K, 1% METAL FILM RESISTORS MATCHED TO .1%, SET OF 6.
7. * JUMPERS ONLY USED ON 170

C	ECN 1506	EO 24-7		
B	OSC. CHANGED	FSS	3/27/69	
rev	ecn	by	date	app

tolerance unless otherwise specified .XXX ± 010 XX ± 030

WAVETEK san diego, calif

scale N/A | by BOCHICHIO | date 10/22/68 | app

material N/A

title SCHEMATIC CRYSTAL ONE SHOT A CONFIGURATION

model no. 157 & 170 | dwg no. 0103-00-0098 | rev C

finish N/A

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REV	ECN	BY	DATE	APP

REFERENCE DESIGNATORS	PART DESCRIPTION	DWG-MFGR-PART-NO	MFGR	WAVETEK NO.	QTY/PT
NONE	ASSY DWG,ONE SHOT	0100-00-0098	WVTK	0100-00-0098	1
WIRE	SCHEMATIC,ONE SHOT	0133-00-0098	WVTK	0133-00-0098	1
NONE	COMP, BLOCK	157-515	WVTK	140-00-3053	1
C1	CAP,CER,,.001MF,1KV	SJ-102	CRL	1500-01-0211	
C2	CAP,CER,,.01MF,50V	CK-103	CRL	1500-01-0310	
C3	CAP,CER,,.1MF,16V	UK1C-104	AMCU	1500-01-0412	
C4	CAP,CER,,1MF,25V	UK20-105	AMCU	1500-01-0413	
C5	CAP,CER,,15PF,1KV	SJ-150	CRL	1500-01-5011	
C6	CAP,MICA,150PF,500V	DM15-151J	AMCU	1500-11-5150	
C7	CAP,MICA,330PF,500V	DM15-331J	AMCU	1500-13-5100	
C8	CAP,MICA,470PF,500V	DM15-471J	AMCU	1500-14-7000	
C9	CAP,MICA,560PF,300V	DM15-561J	AMCC	1500-15-0100	
C10	CAP,ELECT,100MF,16V	502D107G016DL2	SPRAG	1500-31-0101	
C11	ONE SHOT	157-117	WVTK	1700-00-0098	
C12	SKT,CRYSTAL	A0L0-01	AUKAT	2100-03-0016	
C13	TERM	260081	USECO	2100-03-0009	6
C14	TERM	201071	USECO	2100-05-0011	5
Y1	CRYSTAL,4MHZ	157-516	WVTK	2300-49-0006	

WAVETEK PARTS LIST

TITLE	ASSEMBLY NO	REV
ONE SHOT	1100-00-0098	
	PAGE: 1	

REFERENCE DESIGNATORS	PART DESCRIPTION	DWG-MFGR-PART-NO	MFGR	WAVETEK NO.	QTY/PT
R5	RES,MF,1/8W,1%,0.1MF	MK55D-01471F	[illegible]	4701-03-0147	1
R18 R19 R23 R25	RES,MF,1/8W,1%,604R	MK55C-06041F	[illegible]	4701-13-6040	4
R6A R6B R52 R53 R55 R56	RES,SPC,0-5K1A,1/5W WITH 4-70-03-5013	157-503-00	[illegible]	4789-00-0004	1
CR1 CR10 CR11 CR12 CR13 CR14 CR15 CR16 CR17 CR18 CR19 CR2 CR20 CR21 CR22 CR23 CR24 CR3 CR4 CR5 CR6 CR7 CR8 CR9	.1N6E	FW-0000	FAIR	4807-02-0000	24
U10 U11 U12 U13 U14 U15 U16 U17 U18 U19 U20 U21	[illegible]	2N2904A	[illegible]	4901-02-0051	12
Q1 Q2 Q3 Q4	[illegible]	2N3903	[illegible]	4901-03-0030	4
Q5 Q6	[illegible]	2N3906	[illegible]	4901-03-0060	2
IC1 IC2 IC3 IC4 IC5 IC6 IC7	[illegible]	MC-8556	[illegible]	7000-00-0000	7

WAVETEK PARTS LIST

TITLE	ASSEMBLY NO	REV
ONE SHOT	1100-00-0098	
	PAGE: 3	

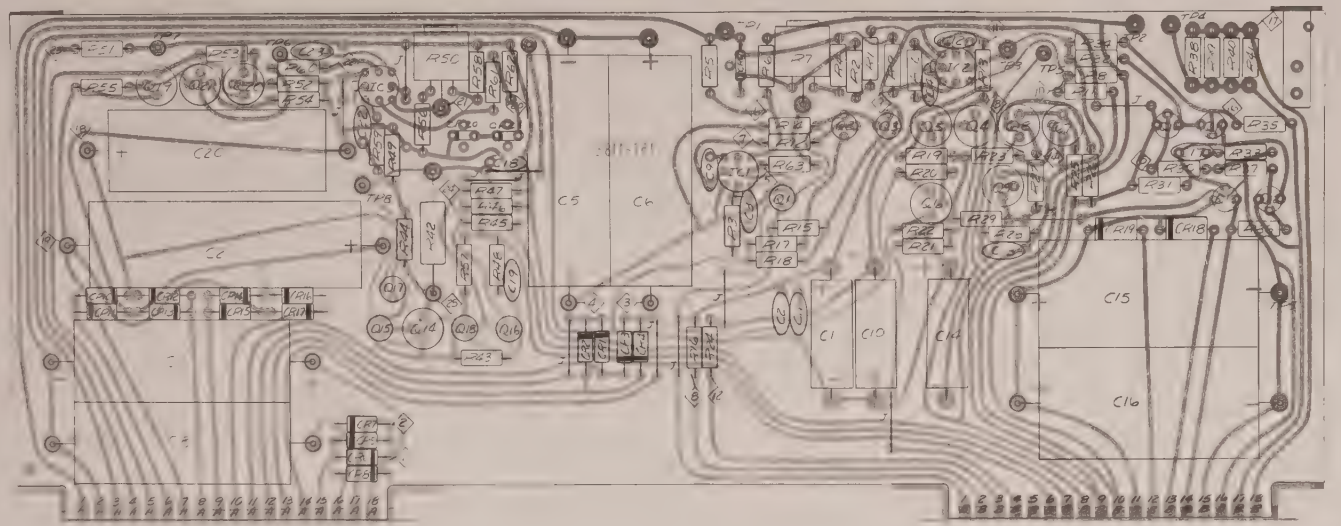
REFERENCE DESIGNATORS	PART DESCRIPTION	DWG-MFGR-PART-NO	MFGR	WAVETEK NO.	QTY/PT
R1 R10 R11 R12 R2 R3 R4 R5 R7 R8 R9	RES,NET,RES,POWER	RS-8R5	[illegible]	4500-00-0105	1
R17 R18 R21 R22	RES,C,1/8W,5%,2.4M	RC20GF-245	[illegible]	4700-25-2406	4
R12 R13 R14 R15	RES,MF,1/8W,1%,1K	MK55L-10019F	[illegible]	4701-03-1001	4
	RES,MF,1/8W,1%,1.1K	[illegible]	[illegible]	4701-03-1101	
R1	RES,MF,1/8W,1%,1.78K	MK55C-17831F	[illegible]	4701-03-1781	
R16 R24 R67 R7 R70 R71 R76 R78 R82 R83 R91 R94	RES,MF,1/8W,1%,2K	MK55C-20019F	[illegible]	4701-03-2001	12
R62 R63 R68 R71 R74 R77 R80 R81 R86 R89 R93	RES,MF,1/8W,1%,21.5K	MK55C-21529F	[illegible]	4701-03-2152	12
R26 R29 R28 R27 R25 R30 R31 R32 R33 R35 R36 R37 R38 R40 R41 R42 R43 R45 R46 R47 R50 R53 R54 R56 R57 R59	RES,MF,1/8W,1%,301K	MK55C-30139F	[illegible]	4701-03-3013	24
R60 R61 R65 R66 R69 R72 R73 R74 R81 R84 R85 R90 R95	RES,MF,1/8W,1%,3.32K	MK55L-33219F	[illegible]	4701-03-3321	13
R1 R3 R6 R49	RES,MF,1/8W,1%,3.92K	MK55L-39219F	[illegible]	4701-03-3921	4
R11 R2	RES,MF,1/8W,1%,5.76K	MK55C-57619F	[illegible]	4701-03-5761	2

WAVETEK PARTS LIST

TITLE	ASSEMBLY NO	REV
[illegible]	1100-00-0098	
	PAGE: 2	

NOTE UNLESS OTHERWISE SPECIFIED

REMOVE ALL BURRS AND BREAK SHARP EDGES	DRAWN	DATE	WAVETEK SAN DIEGO, CALIFORNIA
MATERIAL	PROJ ENGR		TITLE: PARTS LIST ONE SHOT
	RELEASE APPROV		
FINISH WAVETEK PROCESS	TOLERANCE UNLESS OTHERWISE SPECIFIED: XXX ± .010 ANGLES ± 1 XX ± .030		
	DO NOT SCALE DWG		MODEL NO: 157 DWG NO: 1100 00 0098 REV: F
	SCALE		CODE IDENT: 23338 SHEET OF



Q	ECN # 1858	EH	12-6-78	
P	ECN 1547	RD	6-7-77	
N	ECN 1372	EH	4/14/77	
M	ECN 1291	MS	3/23/77	
L	ECN 1236	MS	2/18/77	
K	ECN 1120	EH	1/26/77	
J	ECN 558	MS	1/13/77	
H	ECN 440	MS	1/11/77	
G	ECN 303	MS	12/9/76	
F	323	MS	11/24/76	
E	COP # 37	MS	10/12/76	
D	REDRAWN	EH	9/1/76	
rev	ecn	by	date	app.

tolerance unless otherwise specified
XXX ± 010
XX ± 030

WAVETEK san diego, calif

scale 2/1 | drf E HOLIWAY | date 6/15/69 | app

material: N/A

title: ASS'Y POWER SUPPLY

finish: N/A

model no: 157

dwg no: 0101-00-0099 Q

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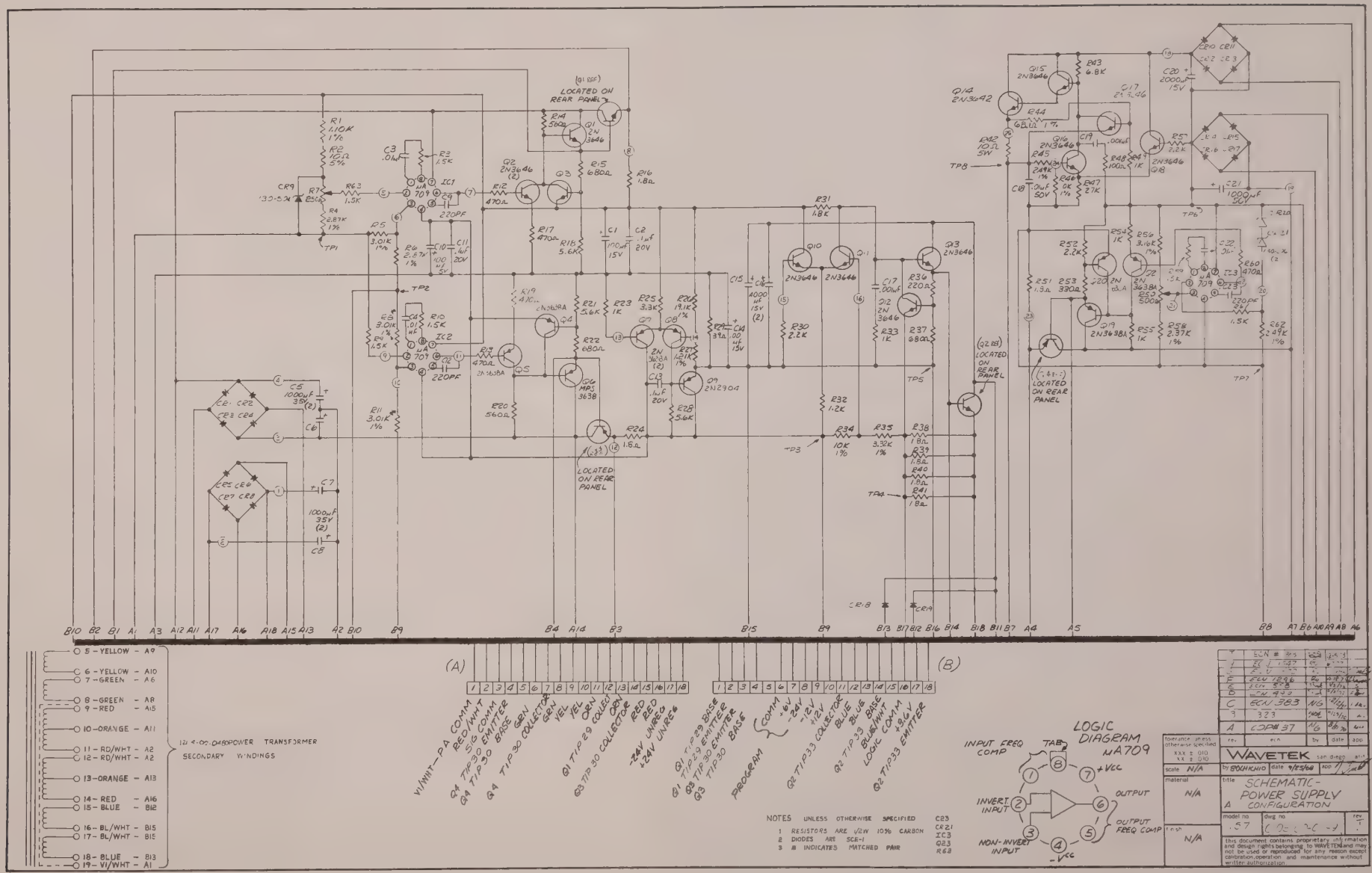
R1 1.10K 1%
R2 10Ω 5%
R3 1.5K
R4 2.87K 1%
R5 3.01K 1%
R6 2.67K 1%
R7 250Ω
R8 3.01K 1%
R9 1.5K
R10 1.5K
R11 3.01K 1%
R12 470Ω
R14 560Ω
R15 680Ω
R16 1.8Ω
R17 470Ω
R18 5.6K
R19 4.7K
R20 560Ω
R21 5.6K
R22 680Ω
R23 1K
R24 3.3K
R25 11K 1%
R26 1.8Ω
R27 12K 1%
R28 5.6K
R29 39Ω
R30 2.2K
R31 1.8K
R32 1.2K
R33 1K
R34 10K 1%
R35 3.32K 1%
R36 220Ω
R37 680Ω
R38 1.8Ω
R39 1.8Ω
R40 1.8Ω
R41 1.8Ω
R42 10Ω 5W
R43 6.8K
R44 6.8Ω 1%
R45 249K 1%
R46 10K 1%
R47 27K
R48 100Ω
R49 1K
R50 3.16K 1%
R51 2.2K
R52 2.2K
R53 330Ω
R54 1K
R55 1K
R56 2.37K 1%
R57 2.49K 1%
R58 470Ω
R59 500Ω
R60 470Ω
C1 100μF 15V
C2 .1μF 20V
C3 .01μF
C4 .01μF
C5 1000μF 35V (2)
C6
C7
C8
C9 100μF 5V
C10
C11 1μF 20V
C12 4000μF 15V (2)
C13 .1μF 20V
C14 .00 μF 15V
C15
C16
C17 .001μF
C18 .01μF 50V
C19 .001μF
C20 2000μF 15V
C21 1000μF 50V
C22 .01μF
C23
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REV	ECN	BY	DATE	APP

REFERENCE DESIGNATORS	PART DESCRIPTION	ORIG-MFGR-PART-NO	MFGR	WAVETEK NO.	QTY/PT
NONE	ASSY DWNG,POWER	0101-00-0099	WVTK	0101-00-0099	1
NONE	SCHEMATIC,POWER	0103-00-0099	WVTK	0103-00-0099	1
J6	CONN, BLOCK	157-315	WVTK	1400-00-3053	1
C17 C19	CAP,CER,.001MF,1KV	DD-102	AMCO	1500-01-0211	2
C18 C22 C3 C4	CAP,CER,.01MF,50V	CK-103	CRL	1500-01-0310	4
C11 C13 C2	CAP,CER,.1MF,20V	UK20-104	CRL	1500-01-0613	3
C12 C23 C9	CAP,CER,220PF,1KV	DD-221	AMCO	1500-02-2111	3
C1 C10 C14	CAP,ELECT,100MF,16V	5800107G016DC7	SPRAG	1500-31-0101	3
C5 C6 C7 C8	CAP,ELECT,1000MF,35V	39D108G035GL6	SPRAG	1500-31-0212	4
C21	CAP,ELECT,1100MF,50V	39D118G050HP6	SPRAG	1500-31-1203	1
C20	CAP,ELECT,1900MF,15V	39D198G015GL6	SPRAG	1500-31-0201	1
C15 C16	CAP,ELECT,8000MF,15V	39D408G015JL6	SPRAG	1500-38-0201	2
NONE	POWER	157-11R	WVTK	1700-00-0099	1
NONE	TERM	20108L	JSECO	2100-05-0011	21
R7	POT,TRIM,250	FR-251M	AB	4600-02-5100	1
R50	POT,TRIM,500	FW-501M	AB	4600-05-0100	1
R16 R24 R38 R39 R40 R41 R51	RES,C,1/2W,5%,1.8	RC20GF-1R8	STKPL	4700-25-0189	7

WAVETEK PARTS LIST | TITLE POWER | ASSEMBLY NO 1100-00-0099 PAGE: 1 | REV Q

REFERENCE DESIGNATORS	PART DESCRIPTION	ORIG-MFGR-PART-NO	MFGR	WAVETEK NO.	QTY/PT
R53	RES,MF,1/8W,1%,332	RN55D-3320F	TRW	4701-03-3320	1
R25 R35	RES,MF,1/8W,1%,3.32K	RN55D-3321F	TRW	4701-03-3321	2
R29	RES,MF,1/8W,1%,39.2	RN55D-39R2F	TRW	4701-03-3929	1
R12 R13 R17 R19 R20	RES,MF,1/8W,1%,464	RN55D-4640F	TRW	4701-03-4640	5
R10 R20	RES,MF,1/8W,1%,576	RN55D-5760F	TRW	4701-03-5760	2
R18 R21 R28	RES,MF,1/8W,1%,5.76K	RN55D-5761F	TRW	4701-03-5761	3
R15 R22 R37	RES,MF,1/8W,1%,681	RN55D-6810F	TRW	4701-03-6810	3
R43	RES,MF,1/8W,1%,6.81K	RN55D-6811F	TRW	4701-03-6811	1
R44	RES,MF,1/8W,1%,68.1	RN55D-68R1F	TRW	4701-03-6819	1
R42	RES,WW,5W,10%,10	PR-5	TRW	4702-66-0100	1
R11 R8	RES,SET,2-3.01K,1/8W QTY:2:6701-03-3011	157-501-28	WVTK	4789-00-0011	1
CR20 CR21 CR9	DIODE	1N4581	MICRO	4801-01-4581	3
CR1 CR10 CR11 CR12 CR13 CR14 CR15 CR16 CR17 CR2 CR3 CR4 CR5 CR6 CR7 CR8	DIODE	SCE-1	SEMI	4801-02-0001	16
CR18 CR19	DIODE	351E	SEMTC	4806-02-0003	2
Q9	TRANS	2N2904	MOT	4901-02-9040	1

WAVETEK PARTS LIST | TITLE POWER | ASSEMBLY NO 1100-00-0099 PAGE: 3 | REV Q

REFERENCE DESIGNATORS	PART DESCRIPTION	ORIG-MFGR-PART-NO	MFGR	WAVETEK NO.	QTY/PT
R48	RES,MF,1/8W,1%,100	RN55D-1000F	TRW	4701-03-1000	1
R23 R33 R49 R58 R55	RES,MF,1/8W,1%,1K	RN55D-1001F	TRW	4701-03-1001	5
R34 R46	RES,MF,1/8W,1%,10K	RN55D-1002F	TRW	4701-03-1002	2
R2	RES,MF,1/8W,1%,10	RN55D-10R0F	TRW	4701-03-1009	1
R1	RES,MF,1/8W,1%,1.1K	RN55D-1101F	TRW	4701-03-1101	1
R27 R32	RES,MF,1/8W,1%,1.21K	RN55D-1211F	TRW	4701-03-1211	2
R10 R3 R59 R61 R63 R9	RES,MF,1/8W,1%,1.5K	RN55D-1501F	TRW	4701-03-1501	6
R31	RES,MF,1/8W,1%,1.78K	RN55D-1781F	TRW	4701-03-1781	1
R26	RES,MF,1/8W,1%,19.1K	RN55D-1912F	TRW	4701-03-1912	1
R36	RES,MF,1/8W,1%,221	RN55D-2210F	TRW	4701-03-2210	1
R30 R52 R57	RES,MF,1/8W,1%,2.21K	RN55D-2211F	TRW	4701-03-2211	3
R54	RES,MF,1/8W,1%,2.37K	RN55D-2371F	TRW	4701-03-2371	1
R45 R62	RES,MF,1/8W,1%,2.49K	RN55D-2491F	TRW	4701-03-2491	2
R47	RES,MF,1/8W,1%,27.4K	RN55D-2742F	TRW	4701-03-2742	1
R4 R5	RES,MF,1/8W,1%,2.87K	RN55D-2871F	TRW	4701-03-2871	2
R13	RES,MF,1/8W,1%,3.01K	RN55D-3011F	TRW	4701-03-3011	1
R56	RES,MF,1/8W,1%,3.16K	RN55D-3161F	TRW	4701-03-3161	1

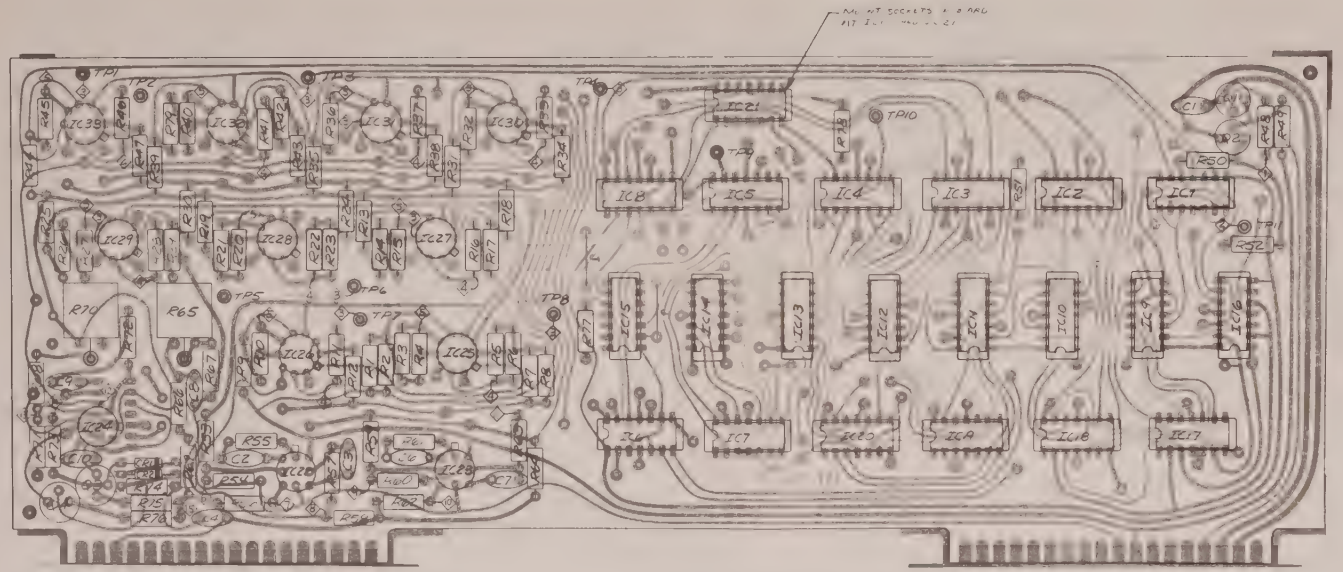
WAVETEK PARTS LIST | TITLE POWER | ASSEMBLY NO 1100-00-0099 PAGE: 2 | REV Q

REFERENCE DESIGNATORS	PART DESCRIPTION	ORIG-MFGR-PART-NO	MFGR	WAVETEK NO.	QTY/PT
Q19 Q20 Q21 Q8 Q5 Q6 Q7 J4	TRANS	2N3638A	CAMTH	4901-03-6381	8
Q14	TRANS	2N3642	FAIR	4901-03-6420	1
Q1 Q10 Q11 Q12 Q13 Q15 Q16 Q17 Q18 Q2 Q3	TRANS	2N3640	NSC	4901-03-6400	11
IC1 IC2 IC3	IC,CLASS II,MA-709 QTY:1:7000-07-0900	130-501-14	WVTK	7200-00-0006	3

WAVETEK PARTS LIST | TITLE POWER | ASSEMBLY NO 1100-00-0099 PAGE: 4 | REV Q

NOTE UNLESS OTHERWISE SPECIFIED

REMOVE ALL BURRS AND BREAK SHARP EDGES	DRAWN	DATE	WAVETEK SAN DIEGO, CALIFORNIA
MATERIAL	ENGINEER		TITLE POWER SUPPLY
	RELEASE APPROV		
FINISH WAVETEK PROCESS	TOLERANCE UNLESS OTHERWISE SPECIFIED: XXX-.010 ANGLES-1 XX-.020		
	DO NOT SCALE DWG		MODEL NO 157 DWG NO 1100-00-0099 REV Q
	SCALE		CODE IDENT 23338 SHEET 1 OF 1



L	ECN 1858	JH	12-11-78	
K	ECN 1606	RD	12-5-78	
J	ECN 1396	RD	10-16-78	
H	ECN 875	RD	7-7-78	
G	ECN 571	RD	4-24-78	
F	ECN 424	RD	2-17-78	
E	ECN 395	RD	12-6-77	

unless otherwise specified: .133 ± .01, .XX ± .030	tol.	by	date / app.
WAVETEK san diego, ca			
scale: 1:1	drawn	date	app.
material: N/A	title: A.S.Y. COUNTER B.D.		
finish: N/A	model no.: 157	dwg. no.: 0101-00 0150	rev.: L

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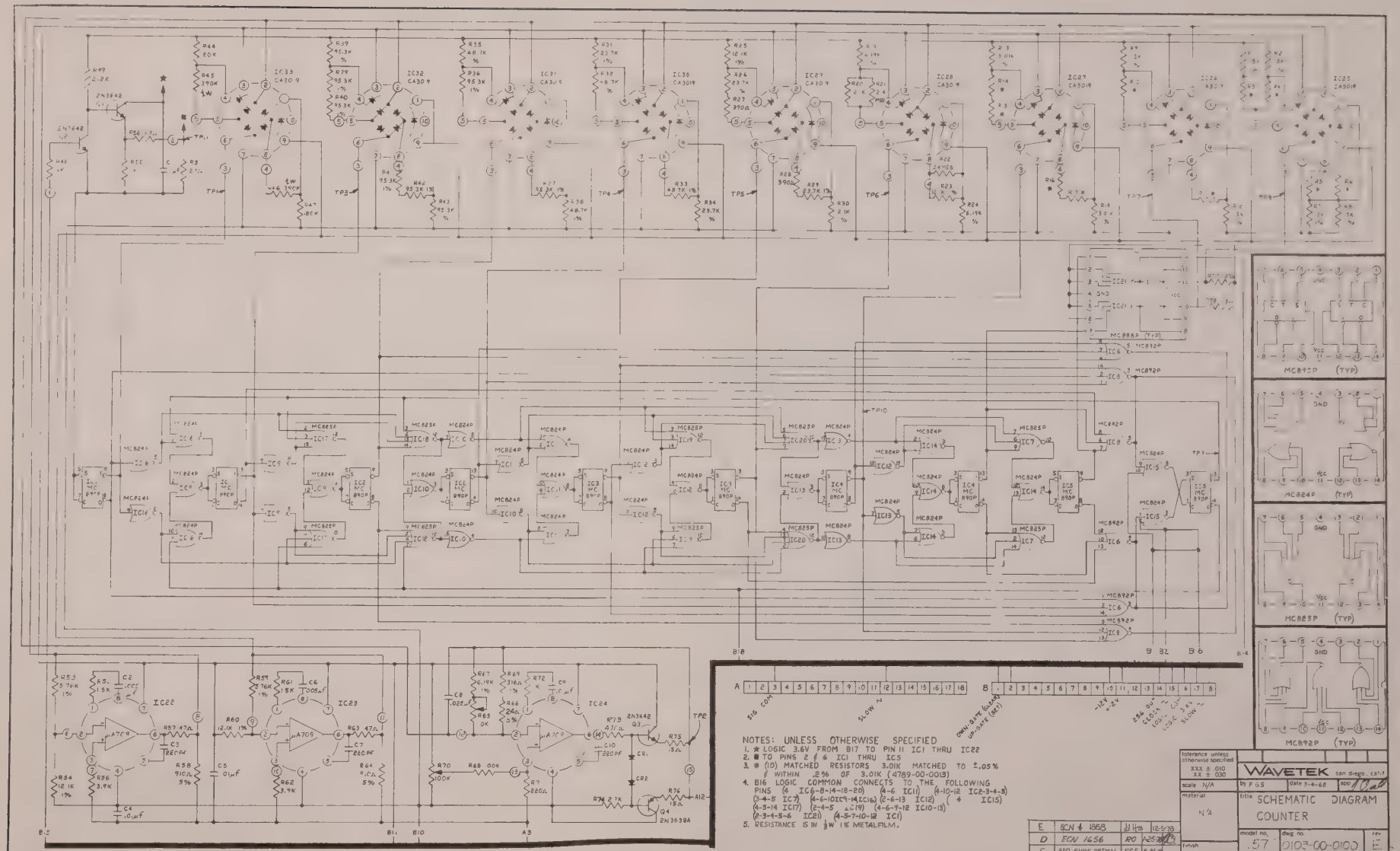

NOTES: UNLESS OTHERWISE SPECIFIED
1. ★ LOGIC 3.6V FROM BI7 TO PIN II IC1 THRU IC22
2. ■ TO PINS 2 & 6 IC1 THRU IC5
3. ✱ (10) MATCHED RESISTORS 3.01K MATCHED TO ±.05% & WITHIN .2% OF 3.01K (4789-00-0013)
4. BI6 LOGIC COMMON CONNECTS TO THE FOLLOWING PINS (4 IC6-8-14-18-20) (4-6 IC11) (4-10-12 IC2-3-4-9) (3-4-5 IC7) (4-6-10IC9-14IC16) (2-6-13 IC12) (4 IC15) (4-5-14 IC17) (2-4-5 ▲IC19) (4-6-9-12 IC10-13) (2-3-4-5-6 IC21) (4-5-7-10-12 IC1)
5. RESISTANCE IS IN ¼W 1% METAL FILM.

REV	ECN	BY	DATE	APP
E	SCN # 1868	JJ	12-5-68	
D	ECN 1656	RO	4-25-68	
C	ADD SYNC OPTION	FGS	3-5-68	
B	RE-DRAWN	FGS	2-15-68	

tolerance unless otherwise specified XXX ± .010 XX ± .030

WAVETEK san diego, calif

scale N/A | by FGS | date 3-4-68 | app

material N/A

finish N/A

title SCHEMATIC DIAGRAM COUNTER

model no. .57 | dwg no. 0103-00-0100 | rev E

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REV	ECN	BY	DATE	APP

REFERENCE DESIGNATORS	PART DESCRIPTION	ORIG-MFGR-PART-NO	MFGR	WAVETEK NO.	QTY/PT
NONE	ASSY DRWG,COUNTER	0101-00-0100	XXXX	0101-00-0100	1
NONE	SCHEMATIC,COUNTER	157-219	XXXX	0103-00-0100	1
C4 C9	CAP,CER,.01MF,50V	CK-103	CMI	1500-01-0310	6
C3	CAP,CER,.1MF,10V	UK10-104	AMCO	1500-01-0812	1
C10 C5 C7	CAP,CER,.220PF,1KV	UG-221	CMI	1500-02-2181	3
C8	CAP,CER,.022MF,25V	MT-523	SPRAG	1500-02-2309	1
C2 C6	CAP,CER,.005MF,1KV	DD-502	CMI	1504-05-0201	2
NONE	COUNTER	157-114	XXXX	1700-03-0100	1
NONE	SKT,IC,14PIN	14-DIP	CINCH	2100-03-0011	10
NONE	TERM	2010M1	3M	2100-05-0011	13
R55	POT,TRIM,10K	FM-103P	XXXX	4801-01-0301	1
R1	POT,TRIM,100K	FM-104P	XXXX	4801-01-0401	1
R21 R22	RES,C,1/2W,5%,2.2M	RC20GF-225J	STACKPOLE	4700-25-2803	2
R43 R46	RES,C,1/2W,10%,39OHM	RC20GF-390K	STACKPOLE	4700-25-3903	2
R17	RES,MF,1/8W,1%,100	MA55L-1000F	XXXX	4701-03-1001	1
R45 R3 R12	RES,MF,1/8W,1%,1K	MA55L-1001F	XXXX	4701-03-1001	3
R28	RES,MF,1/8W,1%,100K	MA55L-1003F	XXXX	4701-03-1003	1
R20 R23 R25 R30 R34	RES,MF,1/8W,1%,12.1K	MA55L-1212F	XXXX	4701-03-1212	5

WAVETEK PARTS LIST	TITLE COUNTER	ASSEMBLY NO 1100-00-0100 PAGE: 1	REV J

REFERENCE DESIGNATORS	PART DESCRIPTION	ORIG-MFGR-PART-NO	MFGR	WAVETEK NO.	QTY/PT
R14 R24 R27	RES,MF,1/8W,1%,8.1K	MA55D-8191F	XXXX	4701-03-8191	3
R15	RES,MF,1/8W,1%,88.1	MA55D-88R1F	XXXX	4701-03-88R1	1
R44 R48	RES,MF,1/8W,1%,909	MA55G-9090F	XXXX	4701-03-9090	2
R35 R37 R39 R40 R41 R42 R43 R19	RES,MF,1/8W,1%,95.3K	MA55E-9532F	XXXX	4701-03-9532	8
R16 R18 R26 R29 R31 R33 R36 R38	RES,SIL,14-5,01+1/8W 4TT11014/01-03-5011	157-501-29	XXXX	0199-00-0013	1
CR1 CR2	DIODE	FR-8000	XXXX	4801-02-8000	2
Q4	TRANS	2N3644	XXXX	4901-03-0571	1
Q1 Q2 Q3	TRANS	2N3646	XXXX	4901-03-0620	3
IC10 IC11 IC12 IC13 IC14 IC15 IC16 IC9	IC	MC-420P	-	7000-05-2006	8
IC17 IC18 IC19 IC20 IC7	IC	MC-425P	-	7000-05-2506	5
IC21	IC	MC-860P	-	7000-00-8600	1
IC1 IC2 IC3 IC4 IC5	IC	MC-660P	-	7000-05-6000	5
IC6 IC8	IC	MC-862P	XXXX	7000-05-8200	2
IC25 IC26 IC27 IC28 IC29 IC30 IC31 IC32 IC33	IC	CA-3019	RCA	7400-30-1900	9

WAVETEK PARTS LIST	TITLE COUNTER	ASSEMBLY NO 1100-00-0100 PAGE: 2	REV J

REFERENCE DESIGNATORS	PART DESCRIPTION	ORIG-MFGR-PART-NO	MFGR	WAVETEK NO.	QTY/PT
R50					
R1 R12 R2 R55 R61 R7 R8 R9	RES,MF,1/8W,1%,1.5K	MA55G-1501F	XXXX	4701-03-1501	8
R13 R10	RES,MF,1/8W,1%,15	MA55G-15R0F	XXXX	4701-03-1509	2
R44 R47	RES,MF,1/8W,1%,178K	MA55D-1783F	XXXX	4701-03-1783	2
R31	RES,MF,1/8W,1%,221	MA55D-2210F	XXXX	4701-03-2211	1
R60	RES,MF,1/8W,1%,2.21K	MA55D-2211F	XXXX	4701-03-2211	1
R24 R32 R35 R36	RES,MF,1/8W,1%,23.7K	MA55E-2372F	XXXX	4701-03-2372	4
R58 R59 R60	RES,MF,1/8W,1%,27.4	MA55E-2742F	XXXX	4701-03-2742	3
R11	RES,MF,1/8W,1%,2.87K	MA55L-2871F	XXXX	4701-03-2871	1
R33 R34	RES,MF,1/8W,1%,3.01K	MA55L-3011F	XXXX	4701-03-3011	2
R64	RES,MF,1/8W,1%,316	MA55L-3160F	XXXX	4701-03-3160	1
R27 R28	RES,MF,1/8W,1%,392	MA55D-3920F	XXXX	4701-03-3920	2
R56 R57	RES,MF,1/8W,1%,4.02K	MA55L-4021F	XXXX	4701-03-4021	2
R53	RES,MF,1/8W,1%,464	MA55D-4640F	XXXX	4701-03-4640	1
R49 R52	RES,MF,1/8W,1%,46.4K	MA55D-4642F	XXXX	4701-03-4642	2
R37 R38 R39 R40	RES,MF,1/8W,1%,4.75K	MA55L-4751F	XXXX	4701-03-4751	4
R51 R54	RES,MF,1/8W,1%,5.76K	MA55D-5761F	XXXX	4701-03-5761	2

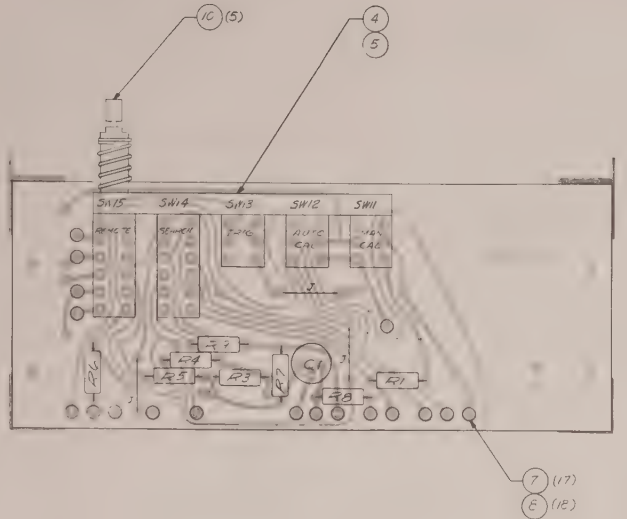
WAVETEK PARTS LIST	TITLE COUNTER	ASSEMBLY NO 1100-00-0100 PAGE: 3	REV J

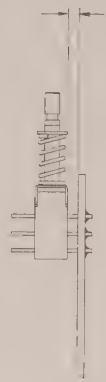
REFERENCE DESIGNATORS	PART DESCRIPTION	ORIG-MFGR-PART-NO	MFGR	WAVETEK NO.	QTY/PT
IC22 IC23 IC24	IC,CLASS II,MW-709 4TT11:7000-07-0000	130-501-1W	XXXX	7200-00-0000	3

WAVETEK PARTS LIST	TITLE COUNTER	ASSEMBLY NO 1100-00-0100 PAGE: 4	REV J

NOTE UNLESS OTHERWISE SPECIFIED

REMOVE ALL BURRS AND BREAK SHARP EDGES	DRAWN	DATE	WAVETEK SAN DIEGO, CALIFORNIA
MATERIAL	PROJENGR		TITLE PARTS LIST COUNTER
	RELEASE APPROV		
FINISH WAVETEK PROCESS	TOLERANCE UNLESS OTHERWISE SPECIFIED XXX ± .010 ANGLES ± 1 XX ± .030		
	DO NOT SCALE DWG		MODEL NO 157 DWG NO 1100-00-0100 REV J
	SCALE		CODE IDENT 23338 SHEET OF





REFERENCE DESIGNATORS	PART DESCRIPTION	WAVETEK PART NO.	QTY	WAVETEK NO.	WT/WT
NONE	SCHEMATIC/INSTRUMENT	0000-00-0003	1	0000-00-0003	.
NONE	ASSY,MAN,LEFT SWITCH	0101-00-0101	1	0101-00-0101	1
*	L SWITCH	157-120	1	1700-00-0101	.
*	LUNA	[illegible]	1	2100-03-0017	1/
*	WIRE,MALE	MILOR-E	1	2100-03-0020	1M
NE	RES,MF,1/8W,1%,1K	MK55D-10G1F	1	4701-03-1001	.
R1	RES,MF,1/8W,1%,13K	MK55D-13G2F	1	4701-03-1302	.
R2	RES,MF,1/8W,1%,15K	MK55D-15G2F	1	4701-03-1502	.
R3	RES,MF,1/8W,1%,3.01K	MK55D-3G01F	1	4701-03-3011	1
R4	RES,MF,1/8W,1%,3.32K	MK55D-3322F	1	4701-03-3321	1
R5	RES,MF,1/8W,1%,402	MK55D-4020F	1	4701-03-4020	1
R6	RES,MF,1/8W,1%,5.11K	MK55D-5G11F	1	4701-03-5011	.
R7	RES,MF,1/8W,1%,7.5K	MK55D-7501F	1	4701-03-7501	1
S1	SWITCH	4936-2	1	4901-03-0020	1
S1	SWITCH ASSY KIT	157-421	1	5103-00-0010	.
*	BUTTON	[illegible]	1	5103-00-0103	5

WAVETEK PARTS LIST	TITLE: LEFT SWITCH	ASSEMBLY NO: 1100-00-0101	REV: C
		PAGE: 1	

C	ECN 1640	DL	11 4 77	
B	ECN 318	NS	4/5/77	
A	RELEASE	EM	4/5/77	
rev	em	by	date	app

tolerance unless otherwise specified: XXX ± .010, XX ± .030

WAVETEK san diego, calif

scale: 2 | by: E. Mitchell | date: 4/5/77 | appd: [illegible]

material: MA

title: LEFT SWITCH BD

finish: A

model no: 157 | dwg no: 0101-00-0101 | rev: C

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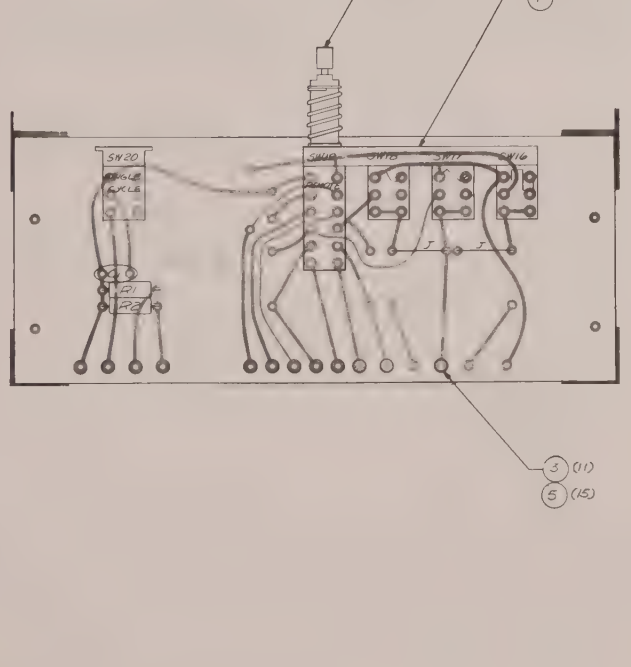

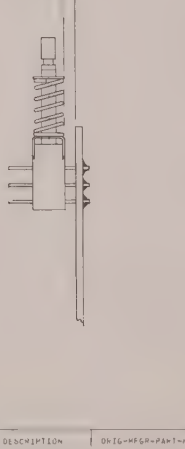

REFERENCE DESIGNATORS	PART DESCRIPTION	OKIE-MFGR-PART-NO	MFGR	WAVETEK NO.	QTY/PI
NONE	SCHEMATIC, INSTRUMENT	0404-00-0083	WVTK	0004-00-0043	1
NONE	ASSY DRAWING SWITCH	0101-00-0102	WVTK	0101-00-0102	1
C1	CAP, CER, 100PF, 1KV	DD-101	CHL	1500-01-0111	1
2	R SWITCH	157-121	WVTK	1700-00-0102	1
3	CONN	66598-8	AMP	2100-05-0017	11
5	PIN, MALE	61182-2	AMP	2100-05-0020	15
R1 R2	RES, MF, 1/8W, 1%, 1K	RN55D-1001F	IRC	0701-03-1004	2
SW20	SWITCH ASSY PB	157-419	WVTK	5102-00-0001	1
7	SWITCH ASSY PB	157-020	WVTK	5103-00-0009	1
8	BUTTON	J-52505-BLACK	CHL	5105-04-0003	5

WAVETEK PARTS LIST	TITLE RIGHT SWITCH	ASSEMBLY NO. 1100-00-0102 PART: 1	REV A

	A	REDRAWN	EH	6/16/67
rev	ecn	by	date	app.

tolerance unless otherwise specified: .XXX ± .010, XX ± .030

WAVETEK San Diego, Calif.

scale: 2/1 | by: E. HAMAN | date: 6/16/67 | app:

material: N/A

title: ASS'Y RIGHT SWITCH BD

model no.: 157 | dwg no.: 0101-00-0102 | rev: A

finish: N/A

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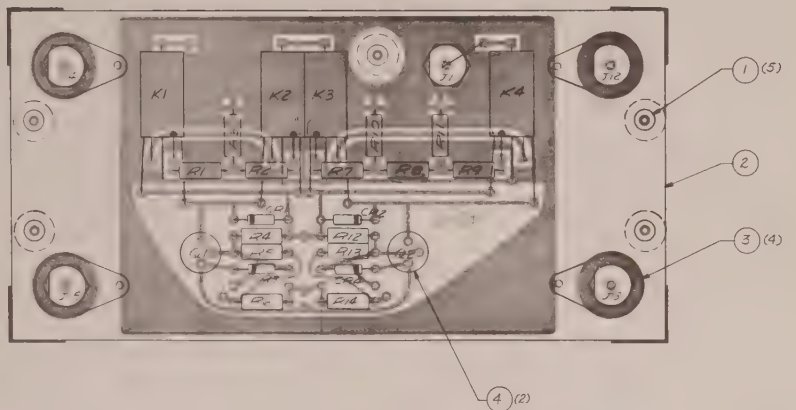

REFERENCE DESIGNATION	PART DESCRIPTION	MFG'R PART NUMBER	MFG'R	WAVETEK NO.	QTY/PI
A/R	SCHEMATIC,INSTRUMENT	0004-00-0003	WVTK	0004-00-0003	1
MAP	ASSY DWG,ATTEN	0101-00-0103	WVTK	0101-00-0103	1
-	STANDOFF	110-511	WVTK	1000-00-0123	5
C	ATTN	157-164	WVTK	1700-00-0103	1
J11 J12 J13 J14 J15	BNC CONN	UG657/U	AMPH	2100-01-0003	5
3	SOLDER LUG	1497	AMPH	2100-00-0012	4
-	TRANSIPAD	101234	ARIES	2600-11-0003	6
K1 K2 K3 K4	RELAY,REED,FLMWVE	MD-2E5	[illegible]	4500-00-0105	4
R13 R3	RES,MF,1/8W,1%,2K	MA55D-2001F	[illegible]	4701-03-2001	2
R12 R4	RES,MF,1/8W,1%,21.5K	MA55D-2152F	[illegible]	4701-03-2152	2
R14 R6	RES,MF,1/8W,1%,332	MA55D-3320F	[illegible]	4701-03-3321	2
R11 R11 R5	RES,MF,1/8W,1%,10	MA00D-10R0F	[illegible]	[illegible]	3
R1 R7	[illegible]	MA00D-40R2F	[illegible]	4701-13-4020	2
R2 R9	[illegible]	[illegible]	[illegible]	4701-13-4124	2
R8	RES,MF,1/8W,1%,80.6	MA00D-80R6F	[illegible]	[illegible]	1
CR1 CR2 CR3 CR4	DIODE	FD-6000	[illegible]	[illegible]	4
W1 W2	JUMPER	2429054	FAIR	6901-02-4051	2

WAVETEK PARTS LIST	TITLE ATTENUATOR	ASSEMBLY NO 1100-04-0103	REV F
		PAGE 1	

F	ECN 1506	RD	12/77	
E	ECN 896	GW	4/76	
D	ECN 295	NG	10/75	
C	ECN 1	EM	9/74	
rev	ecn	by	date	app

tolerance unless otherwise specified: XXX ± .010, XX ± .030

WAVETEK san diego, calif

scale 2:1 | by E. TORRES | date 8-16-74 | app

material: N/A

finish: N/A

title: ASS'Y ATTENUATOR BOARD

model no: 157 | dwg no: 0101-00-0103 | rev: F

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157-S-131A

The 157-S-131A is a standard 157 modified to conform to Hughes drawing 65-2863. It also has a ramp waveform via a ramp generator placed in the extender board slot. Program the ramp wave by contact closure from pin CC to pin HH of the remote program connector. The front panel function switch must be in the remote position.

Calibration of the Ramp Generator (342-010)

1. After completing the standard calibration procedure, observe the plus end of C4 and adjust R2 for +6.00V.

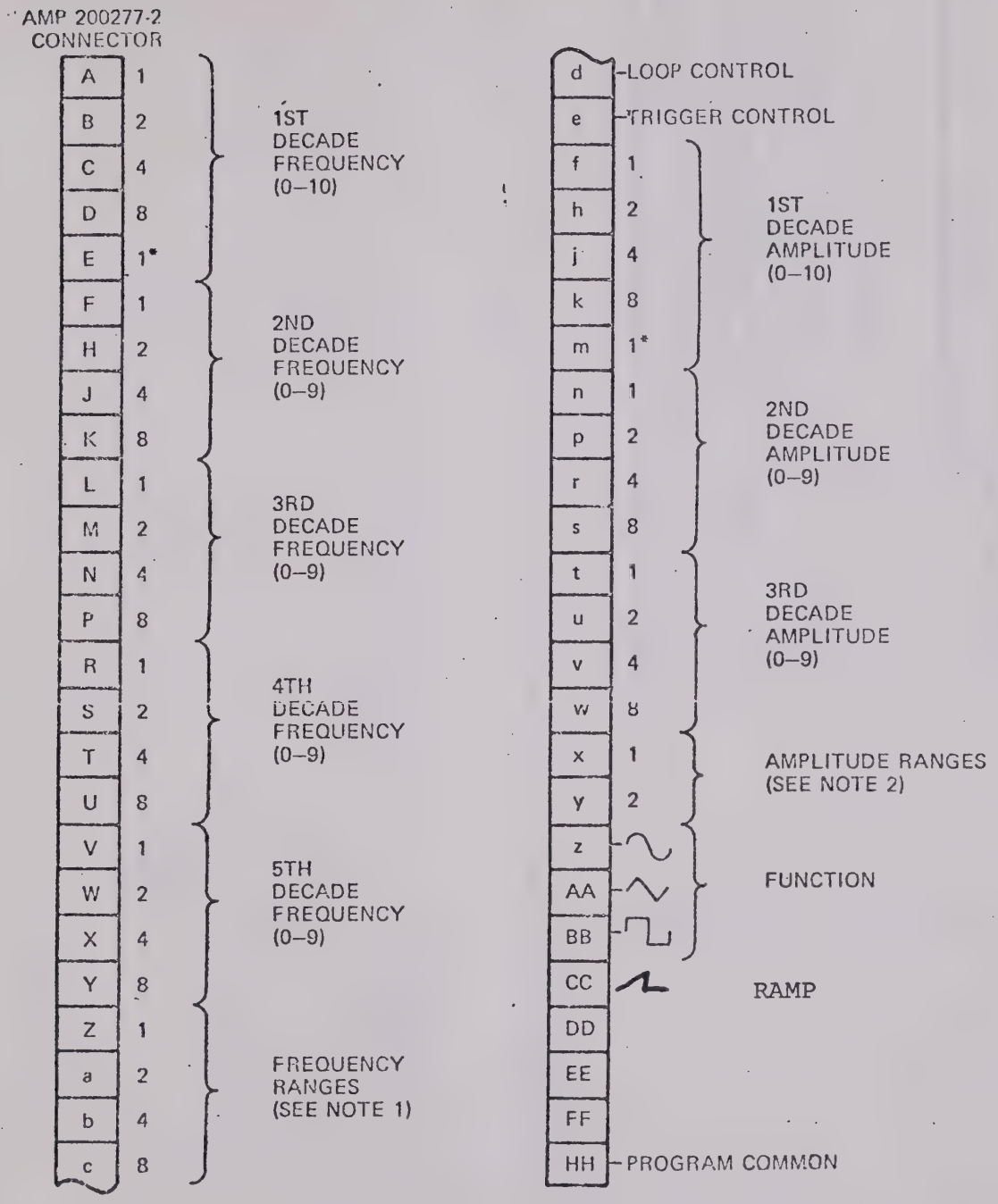
2. Observe the negative end of C6 and adjust R5 for -6.00V.

3. Connect a 50 ohm load (± 0.1%) to the 50 ohm output and set the 157 for 100.00 Hz.

4. Use a scope having dc offset and adjust R20 for zero overshoot at the ramp bade line.

5. Change from the square wave to ramp and adjust R22 for a positive peak within 5 mV.

REMOTE PROGRAMMING–CONNECTOR DIAGRAM



NOTE 1

PROGRAM	FREQ RANGE
0	1000 μHz
1	10 mHz
2	100 mHz
3	1000 mHz
4	10 Hz
5	100 Hz
6	1000 Hz
7	10 kHz
8	100 kHz
9	1000 kHz

To program "10", the code is 8, 1 & 1.

NOTE 2

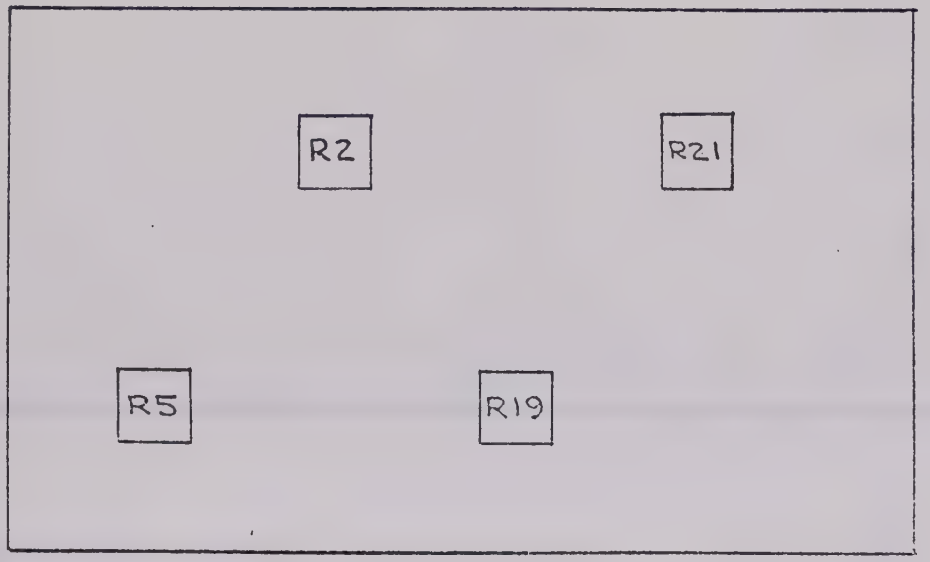
PROGRAM	AMPL RANGE
0	10 mV
1	100 mV
2	1000 mV
3	10 V

PROGRAM VOLTAGE INFORMATION

TRUE or LOGIC "1" = 0 V ±1 V
Note: 0 V sinks ~1 mA.
FALSE or LOGIC "0" = +2 V to +10 V
Note: Open circuit voltage is ~ +2 V.
Front panel switches override all programmed information.

REAR BNC CONNECTORS

50-Ω amplifier output
VCG input, Trig in, Synt in, Sync out.



tolerance unless otherwise specified	rev	ecn	by	date	app.
.XXX ± .010 .XX ± .030	WAVETEK san diego, calif				
scale —	by Shack	date 7-31-69	app.		
material —	title RAMP GENERATOR BOARD No. 342-110				
finish —	model no. 157-S-131	dwg no.	rev		

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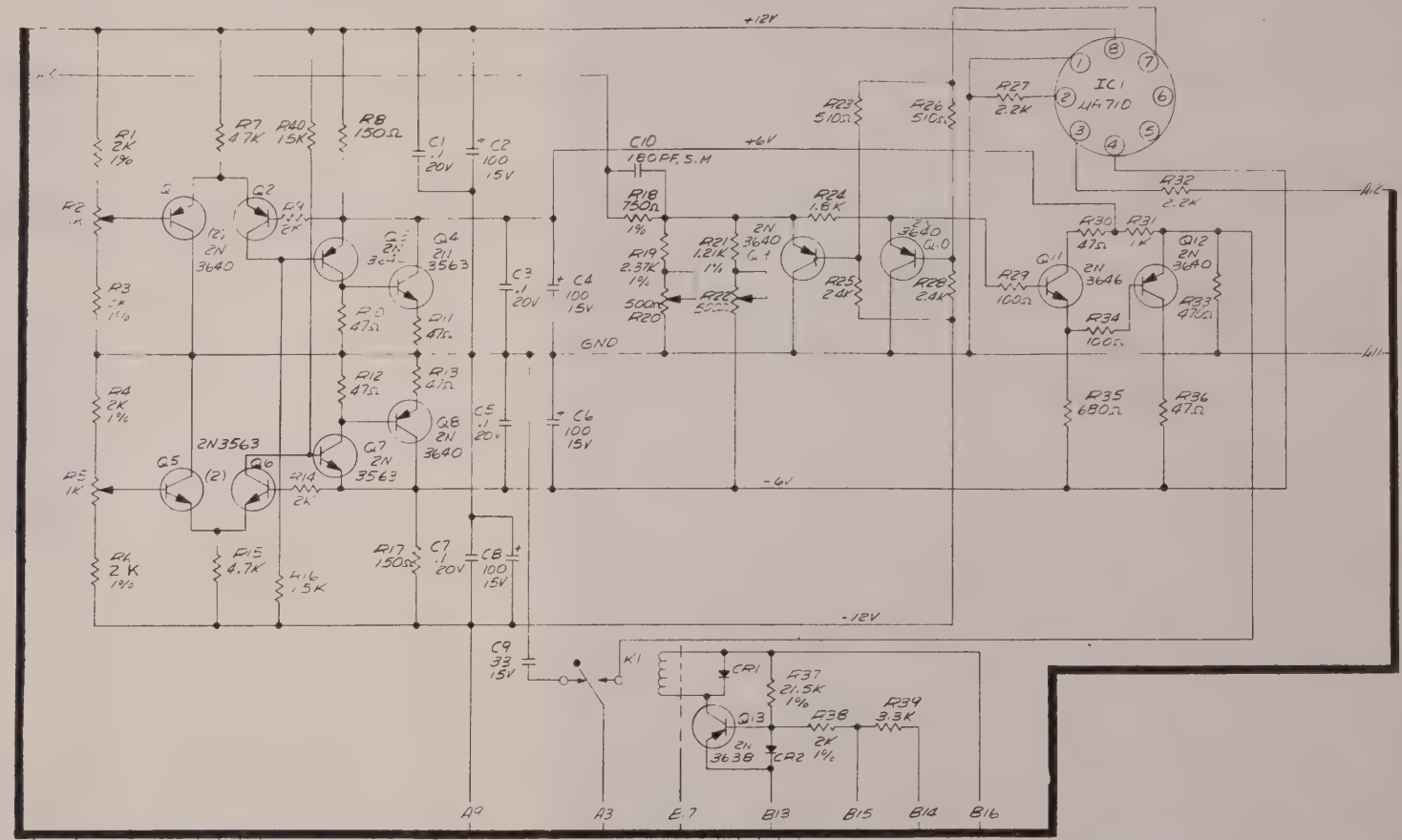

1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18

+12V POWER AMP
-12V
1V SYNC
GND
SYNC

1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18

PROGRAM (GND TO +6V)
CONTROL LINE (R_P)
-24V
SHIELD

NOTES: UNLESS OTHERWISE SPECIFIED

1. RESISTORS ARE IN OHMS, 1/2W, 10% CARBON
2. CAPACITANCE IS IN MICROFARADS
3. DIODES ARE FD6666

LAST COMPONENT DESIGNATOR USED

R40
C10
CR2
Q13
IC1
K1

tolerances unless otherwise specified	rev. | chg | by | date | appr
XXX ± .010 XX ± .030	WAVETEK san diego, calif.
scale N/A	by E. HUANG, date 6/25/69, appr
material N/A	title RAMP GENERATOR
finish N/A	model no 157-S-131, dwg no C342-210, rev

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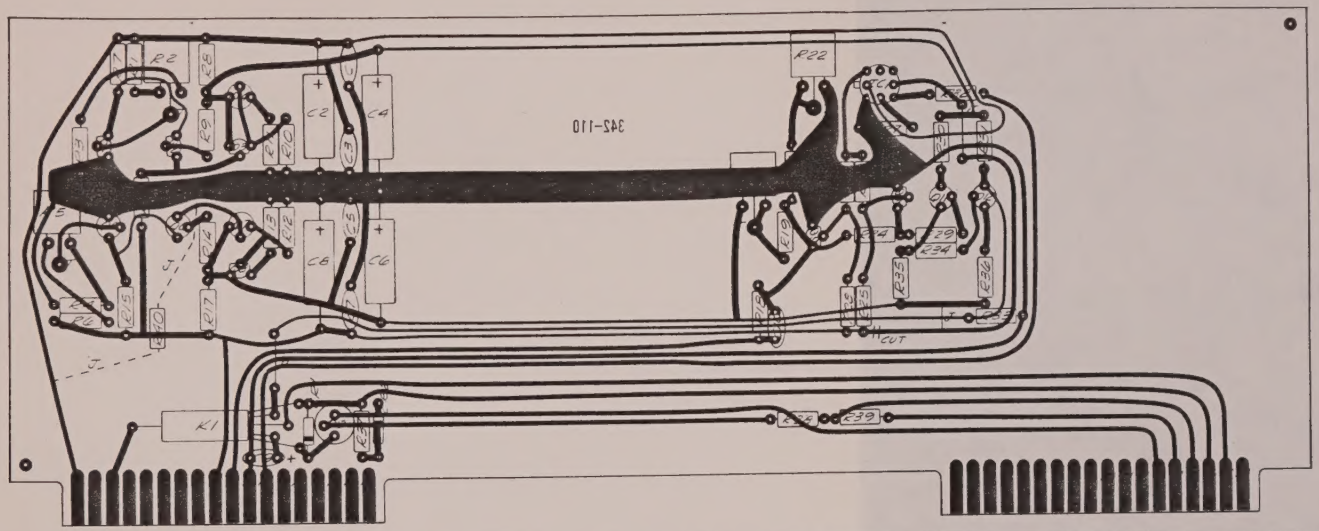

PARTS LIST

ITEM	REF DES	DESCRIPTION	MFGR	MFGR NO.	QTY
1	C10	SLIDE MICA, 500PF, 1%	ARCO	CMM-181J	1
2	C1, C3, C5, C7	CERAMIC DISC, 20KV, .01μF	CEL	CRDC-103	4
3	C9	TAN TANTALUM, 15V, 35μF	KEMET	K35E10	1
4	C2, C4 C6, C8	ELECTROLYTIC, 15V, 100μF	SPRAGUE	500D107-G015DD7	4
5					
6					
7					
8		DIODES			
9	CR1, CR2	FDG666			2
10					
11					
12		INTEGRATED CIRCUITS			
13	IC1	μA7C	FAIRCHILD	μA7C	1
14					
15					
16		POTENTIOMETER			
17	R20, R22	500Ω	A/B	ER3CIM	2
18	R2, R5	1K	A/B	ER10RA1	2
19					
20					
21					
22					
23					
24					
25					
26					
27					
28					
29					

PARTS LIST

ITEM	REF DES	DESCRIPTION	MFGR	MFGR NO.	QTY
30		RESISTOR			
31	R10-19, R30, R34	CARBON, 1/2W, 10% 47Ω	STACKPOLE	RC20GF470K	6
32	R29, R34	100Ω		101K	2
33	R8, R17	150Ω		151K	2
34	R33	470Ω		471K	1
35	R28, R26	511Ω		511K	2
36	R35	665Ω		681K	1
37	R31	1K		102K	1
38	R24	1.6K		182K	1
39	R9, R21	2K		202K	2
40	R27, R32	2.2K		222K	2
41	R25, R23	2.4K		242K	2
42	R39	3.3K		332K	1
43	R7, R15	4.7K		472K	2
44	R16, R40	15K		153K	2
45					
46					
47					
48	R18	METAL FILM, 1/4W, 1%, 75PPM	CORNING	RN60D	1
49	R21	1.21K			1
50	R1, R3, R4, R6 R36	2K			5
51	R19	2.37K			1
52	R37	21.5K			1
53					
54					
55					
56		RELAYS			
57	K1		JENKO	RR30-1023	1
58					

PARTS LIST

ITEM	REF DES	DESCRIPTION	MFGR	MFGR NO	QTY
59		TRANSISTORS			
60	Q4-Q7	2N3563	FAIRCHILD	2N3563	4
61	Q13	2N3638		2N3638	1
62	Q1-Q3, Q8-Q10, Q12	2N3640		2N3640	7
63	Q11	2N3646		2N3646	1
64					
65					
66					
67					
68	T	TERMINALS	USECO	20108	4
69					
70					
71					
72		CIRCUIT BOARD	WAVETEK	342-110	1
73					
74					
75					

tolerance unless otherwise specified: XXX ± .010, XX ± .030	rev	ecn	by	date / app
scale 8/1	WAVETEK san diego, calif.			
material N/A	by GRAY	date 2-3-71	app	
finish N/A	title ASSY RAMP GENERATOR			
	model no 157S-131	dwg no D342-010	rev	

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